



DMN2011UFDF

#### **Product Summary**

BV <sub>DSS</sub>	Rds(on)	I <sub>D Max</sub> T <sub>A</sub> = +25°C
20V	$9.5 m\Omega @ V_{GS} = 4.5 V$	11.7A
200	$11m\Omega @ V_{GS} = 2.5V$	10.8A

#### Description

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Top View

#### **Applications**

PROTECTED

- General Purpose Interfacing Switch
- Power Management Functions

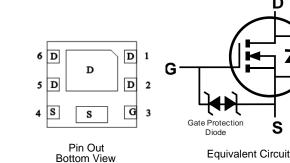
## 20V N-CHANNEL ENHANCEMENT MODE MOSFET

#### Features

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm<sup>2</sup>
- Low Gate Threshold Voltage
- Low On-Resistance
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: U-DFN2020-6 (Type F)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.0065 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Reel Size (inches)	Quantity per Reel
DMN2011UFDF-7	U-DFN2020-6 (Type F)	7	3,000
DMN2011UFDF-13	U-DFN2020-6 (Type F)	13	10,000

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

U-DFN2020-6(Type F)

Pin1

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**



Bottom View

 $\begin{array}{l} N2 = \mbox{Product Type Marking Code} \\ YM = \mbox{Date Code Marking} \\ Y = \mbox{Year (ex: D = 2016)} \\ M = \mbox{Month (ex: 9 = September)} \end{array}$ 

Date Code Key												
Year	2016		2017	2018		2019	2020		2021	2022		2023
Code	D		E	F		G	Н			J		K
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V <sub>DSS</sub>	20	V		
Gate-Source Voltage	V <sub>GSS</sub>	±12	V		
	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	11.7 9.3	A
Continuous Drain Current (Note 6) $V_{GS} = 4.5V$	t<10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	14.2 11.4	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	I <sub>DM</sub>	80	A		
Maximum Body Diode Continuous Current	ls	2.5	A		
Avalanche Current (Notes 7) L = 0.1mH	alanche Current (Notes 7) L = 0.1mH			18	A
Avalanche Energy (Notes 7) L = 0.1mH	E <sub>AS</sub>	17	mJ		

### **Thermal Characteristics**

Characteristic		Symbol	Value	Unit	
Tatal Dawan Diasin stian (Nata 5)	T <sub>A</sub> = +25°C		0.73	14/	
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$ P <sub>D</sub>		0.47	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R	175	°C/W	
memai Resistance, Junction to Ambient (Note 5)	t<10s	$R_{ heta}JA$	128	C/vv	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	Р	2.1	W	
Total Power Dissipation (Note 6)	$T_A = +70^{\circ}C$	PD	1.3		
Thermal Registeres, Junction to Ambient (Note 6)	Steady State	P	61		
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{ heta JA}$	45	°C/W	
Thermal Resistance, Junction to Case (Note 6)	$R_{\theta JC}$	9.3			
Operating and Storage Temperature Range		T <sub>J.</sub> T <sub>STG</sub>	-55 to +150	°C	

# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Turn	Max	Unit	Test Condition
	Symbol	WIIN	Тур	wax	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)		20			V	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20				$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	I <sub>DSS</sub>		_	1	μA	$V_{DS} = 16V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	_	_	±10	μA	$V_{GS} = \pm 10V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)			1			
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.4	—	1.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
			6.5	9.5		$V_{GS} = 4.5V, I_D = 7A$
Static Drain-Source On-Resistance	Proven		7.5	11	mΩ	$V_{GS} = 2.5V, I_D = 7A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		10	20	11152	$V_{GS} = 1.8V, I_D = 5A$
			15	35		$V_{GS} = 1.5V, I_D = 3A$
Diode Forward Voltage	V <sub>SD</sub>	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 8.5A$
DYNAMIC CHARACTERISTICS (Note 9)						·
Input Capacitance	Ciss	_	2248	—	pF	
Output Capacitance	C <sub>oss</sub>	_	295	—	pF	− V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V, − f = 1.0MHz
Reverse Transfer Capacitance 4	C <sub>rss</sub>		265	—	pF	
Gate Resistance	Rg		1.5	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qq		24	—	nC	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg		56	_	nC	
Gate-Source Charge	Q <sub>gs</sub>		3.5	_	nC	$V_{DS} = 10V, I_D = 8.5A$
Gate-Drain Charge	Q <sub>gd</sub>	_	5.1	_	nC	7
Turn-On Delay Time	t <sub>D(ON)</sub>	_	3.6	_	ns	
Turn-On Rise Time	t <sub>R</sub>	_	2.6	—	ns	$V_{DS} = 10V, I_D = 8.5A$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	21.6	_	ns	$V_{GS} = 4.5V, R_{g} = 1.8\Omega$
Turn-Off Fall Time	t <sub>F</sub>	_	13.5	_	ns	
Reverse Recovery Time	T <sub>RR</sub>	_	12.8	_	ns	
Reverse Recovery Charge	Q <sub>RR</sub>	_	6.9	_	nC	I <sub>F</sub> = 8.5A, di/dt = 210A/μs

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

7. I<sub>AS</sub> and E<sub>AS</sub> rating are based on low frequency and duty cycles to keep  $T_{\rm J}$  = +25°C.

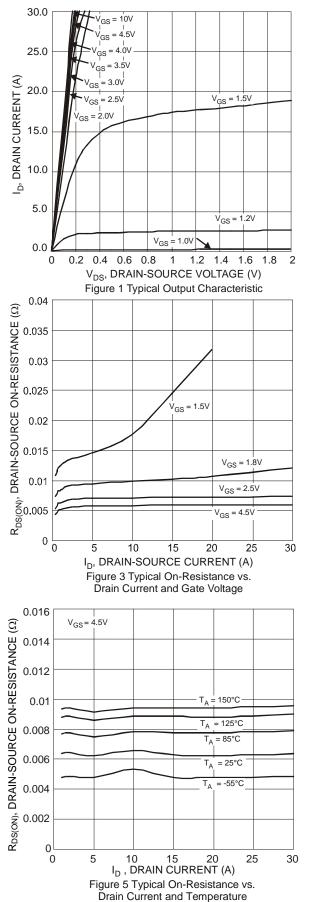
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

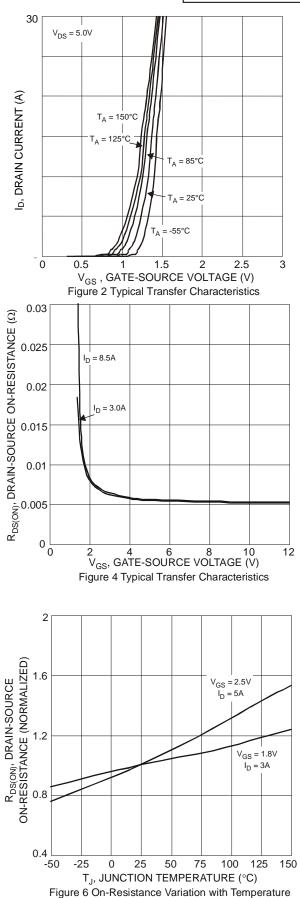
Notes:

Downloaded From Oneyac.com





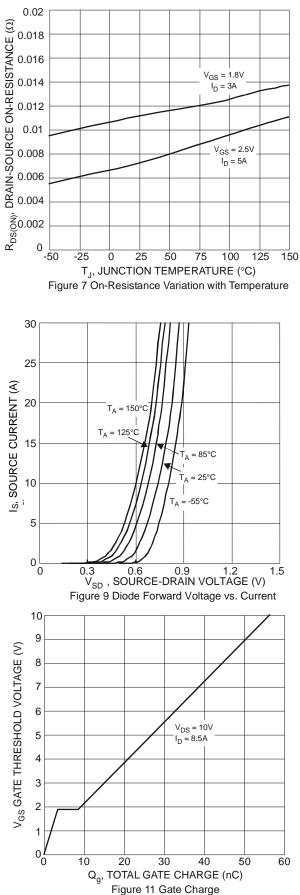


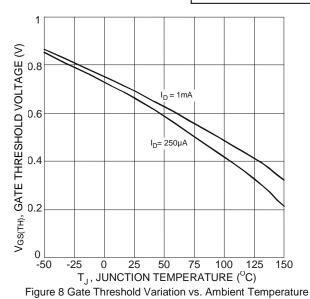


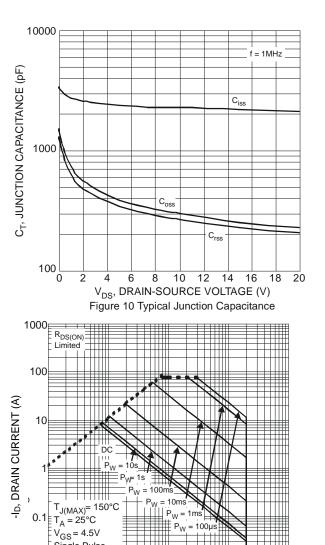
DMN2011UFDF Datasheet number: DS37734 Rev. 1 - 2



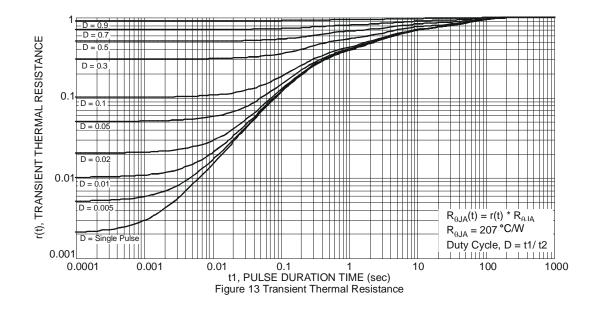










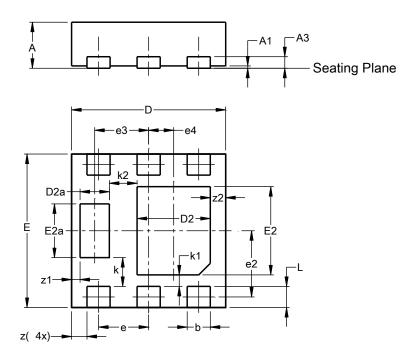




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

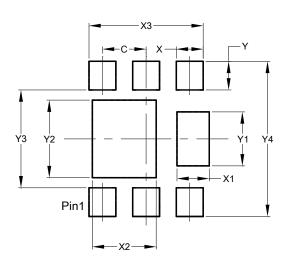
#### U-DFN2020-6 (Type F)



U-DFN2020-6								
(Type F)								
Dim	Min	Min Max Ty						
Α	0.57	0.63	0.60					
A1	0.00	0.05	0.03					
A3	-	-	0.15					
b	0.25	0.35	0.30					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
D2a	0.33	0.43	0.38					
E	1.95	2.05	2.00					
E2	1.05	1.25	1.15					
E2a	0.65	0.75	0.70					
е	0.65 BSC							
e2	0.863 BSC							
e3	0.70 BSC							
e4	C	).325 BS	SC					
k		0.37 BS	С					
k1		0.15 BS	С					
k2		0.36 BS	С					
L	0.225	0.325	0.275					
z	0.20 BSC							
z1	0.110 BSC							
z2	0.20 BSC							
All D	Dimens	ions in	mm					

## Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



#### U-DFN2020-6 (Type F)

Dimensions	Value (in mm)
С	0.650
Х	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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