



#### **60V N-CHANNEL ENHANCEMENT MODE MOSFET** PowerDI3333-8

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C
60V	16mΩ @ V <sub>GS</sub> = 10V	31.8A
607	$22m\Omega$ @ V <sub>GS</sub> = 4.5V	27.6A

#### **Features**

- 100% Unclamped Inductive Switching (UIS) Test in Production-Ensures More Reliable and Robust End Application
- Low On-Resistance
- Small Form Factor Thermally Efficient Package Enables Higher **Density End Products**
- Wettable Flank for Improved Optical Inspection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

## **Description and Applications**

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Backlighting
- **Power Management Functions**
- **DC-DC Converters**

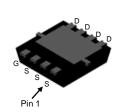
#### **Mechanical Data**

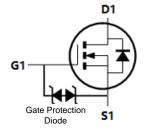
- Case: PowerDI®3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.072 grams (Approximate)

PowerDI3333-8 (SWP) (Type UX)









Top View

**Bottom View** 

**Equivalent Circuit** 

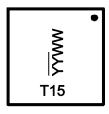
## Ordering Information (Note 4)

Part Number	Case	Packaging
DMT6015LFVW-7	PowerDI3333-8 (SWP) (Type UX)	2000/Tape & Reel
DMT6015LFVW-13	PowerDI3333-8 (SWP) (Type UX)	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## Marking Information



T15 = Product Type Marking Code YYWW = Date Code Marking  $\overline{YY}$  = Last Two Digits of Year (ex: 20 = 2020) WW = Week Code (01 to 53)

PowerDI is a registered trademark of Diodes Incorporated.



## **Maximum Ratings** (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V <sub>DSS</sub>	60	V		
Gate-Source Voltage	V <sub>GSS</sub>	±16	V		
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 6) $T_C = +25^{\circ}C$ $T_C = +70^{\circ}C$			ΔI	31.8 25.5	А
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 5) Steady State		T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	10.0 8.0	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	Ідм	127	Α		
Maximum Continuous Body Diode Forward Current (No	Is	31.8	Α		
Pulsed Body Diode Forward Current (10µs Pulse, Duty	lsм	127	Α		
Avalanche Current, L = 0.1mH	I <sub>AS</sub>	20.7	Α		
Avalanche Energy, L = 0.1mH	Eas	21.4	mJ		

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	$T_A = +25$ °C	$P_{D}$	2.8	W
Thermal Resistance, Junction to Ambient (Note 5)  Steady State		$R_{\theta JA}$	44.2	°C/W
Total Power Dissipation (Note 6) $T_C = +25^{\circ}C$		PD	28.4	W
Thermal Resistance, Junction to Case (Note 6)	$R_{ heta JC}$	4.4	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C	

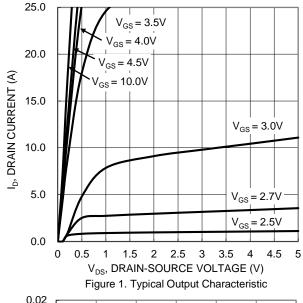
## Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

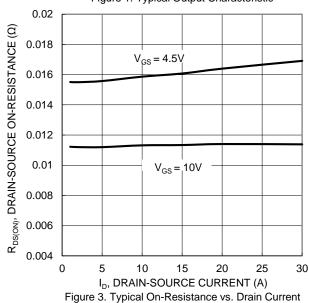
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)						•	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	$V_{GS} = 0V$ , $I_{D} = 250 \mu A$	
Zero Gate Voltage Drain Current			_	1	μΑ	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	Igss		_	±10	μΑ	$V_{GS} = \pm 16V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	VGS(TH)	1.2	_	2.5	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	D-scarii		_ 11.3 16	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 10A		
Static Drain-Source On-Resistance	RDS(ON)	_	15.8	22	11122	$V_{GS} = 4.5V, I_D = 6A$	
Diode Forward Voltage	VsD	_	0.7	1.2	V	V <sub>G</sub> S = 0V, I <sub>S</sub> = 1A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	808	_	pF		
Output Capacitance	Coss	_	279	_	pF	$V_{DS} = 30V, V_{GS} = 0V,$ - f = 1MHz	
Reverse Transfer Capacitance	Crss		30	_	рF	1 – 1101112	
Gate Resistance	Rg		1.4	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (Vgs = 4.5V)	Qg	_	8.6	_	nC		
Total Gate Charge (Vgs = 10V)	Qg	_	15.7	_	nC	\/ 20\/ I- 40A	
Gate-Source Charge	Qgs	_	2.9	_	nC	$V_{DS} = 30V, I_{D} = 10A$	
Gate-Drain Charge	Qgd	_	3.4	_	nC		
Turn-On Delay Time	t <sub>D</sub> (ON)	_	7.5	_	ns	V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V,	
Turn-On Rise Time	t <sub>R</sub>	_	2.7	_	ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	17.4	_	ns	$R_g = 6\Omega$ , $I_D = 10A$	
Turn-Off Fall Time	tF	_	8.9	_	ns		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	26.7	_	ns	100 11/11 1000/	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	_	16.2	_	nC	I <sub>F</sub> = 10A, di/dt = 100A/μs	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

5. Device involved on the state of the state of







and Gate Voltage

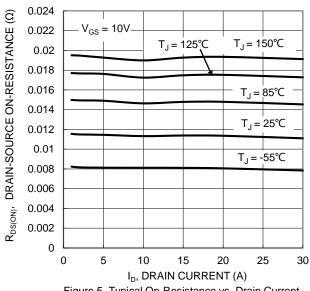
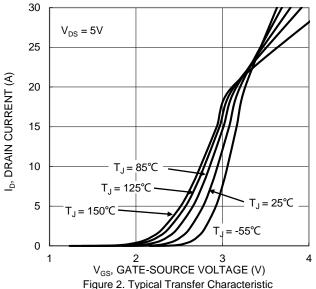
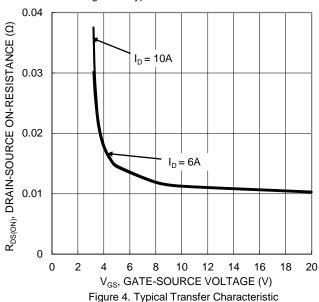


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





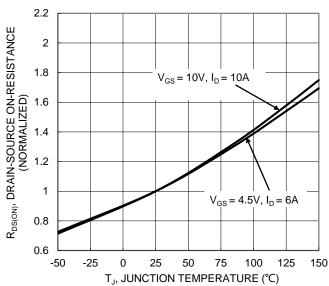


Figure 6. On-Resistance Variation with Temperature



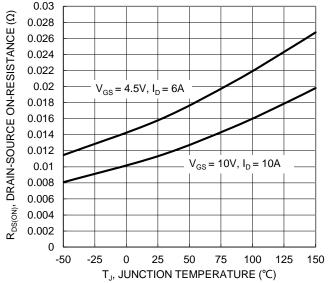


Figure 7. On-Resistance Variation with Temperature

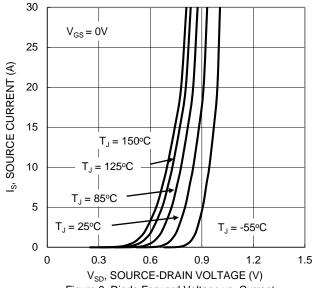


Figure 9. Diode Forward Voltage vs. Current

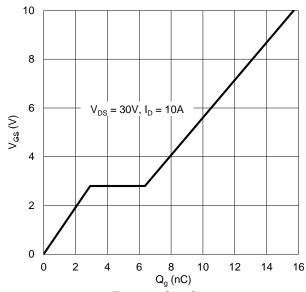


Figure 11. Gate Charge

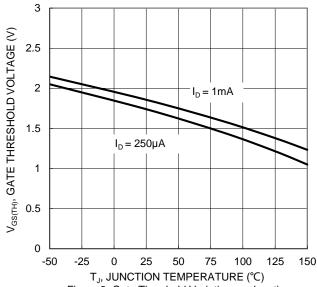
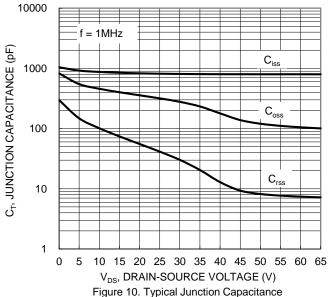


Figure 8. Gate Threshold Variation vs. Junction Temperature



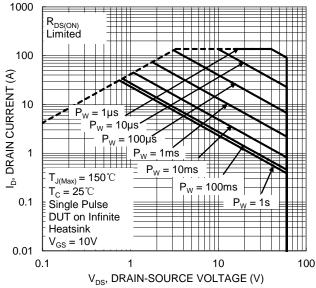


Figure 12. SOA, Safe Operation Area



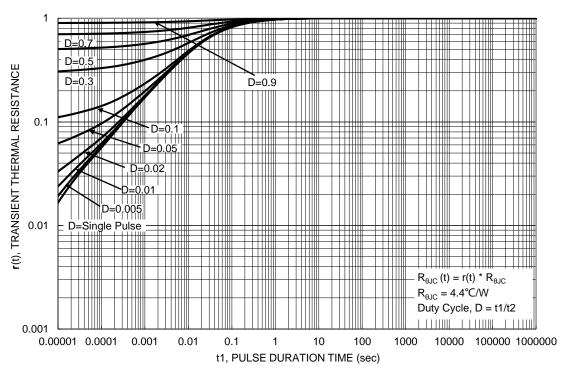


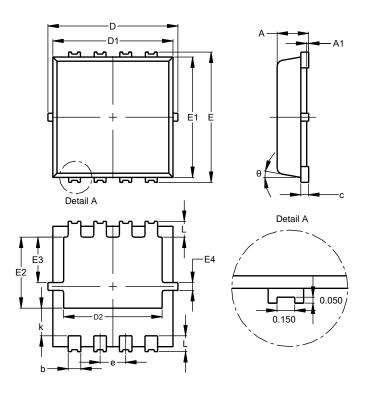
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

 $\label{prop:package-outlines.html} Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$ 

#### PowerDI3333-8 (SWP) (Type UX)

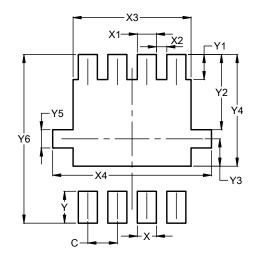


PowerDI3333-8 (SWP)						
(Type UX)						
Dim	Min Max		Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05				
b	0.25	0.40	0.32			
С	0.10	0.25	0.15			
D	3.20	3.40	3.30			
D1	2.95	3.15	3.05			
D2	2.30	2.70	2.50			
Е	3.20	3.40	3.30			
E1	2.95	3.15	3.05			
E2	1.60	2.00	1.80			
E3	0.95	1.35	1.15			
E4	0.10	0.30	0.20			
е	_	_	0.65			
k	0.50	0.90	0.70			
L	0.30	0.50	0.40			
θ	0°	12°	10°			
All Dimensions in mm						

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI3333-8 (SWP) (Type UX)



Dimensions	Value (in mm)			
С	0.650			
X	0.420			
X1	0.420			
X2	0.230			
Х3	2.600			
X4	3.500			
Υ	0.700			
Y1	0.550			
Y2	1.650			
Y3	0.600			
Y4	2.450			
Y5	0.400			
Y6	3.700			

July 2020



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