



DUAL N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Product Summary

V _{(BR)DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C	
50V	3.5Ω @ V _{GS} = 10V	200mA	

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

Load Switch

SOT363



Top View

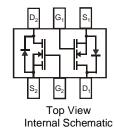
Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The BSS138DWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: SOT-363
- Case Material: Molded Plastic. "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe.
 Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)



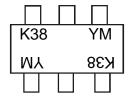
Ordering Information (Note 4)

Part Number	Case	Packaging
BSS138DWQ-7	SOT363	3,000/Tape & Reel
BSS138DWQ-13	SOT363	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



K38 = Product Type Marking Code YM = Date Code Marking Y or = Year (ex: D = 2016) M = Month (ex: 9 = September)

Date Code Key

Code 1 2 2 4 5 6 7 9 0 0 N 5	Year	2005	2006		2016	201	7 20	18 2	2019	2020	2021	2022	2023
Code 1 2 2 4 5 6 7 9 0 0 N 5	Code	S	Т		D	Е	F	=	G	Η	I	J	K
Code 1 2 2 4 5 6 7 9 0 0 N 5	Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Odd	Code	1	2	3	4	5	6	7	8	9	0	N	D

BSS138DWQ 1 of 6
Document number: DS38849 Rev. 2 - 2



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	BSS138DW	Units
Drain-Source Voltage		V_{DSS}	50	V
Drain-Gate Voltage (Note 7)		V_{DGR}	50	V
Gate-Source Voltage	Continuous	V _{GSS}	±20	V
Drain Current (Note 5)	Continuous	I _D	200	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	BSS138DW	Units
Total Power Dissipation (Note 5)	P_{D}	200	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

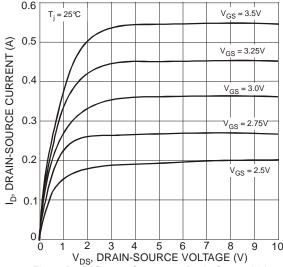
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

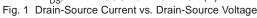
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)				•			
Drain-Source Breakdown Voltage	BV _{DSS}	50	75	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	0.5	μΑ	$V_{DS} = 50V, V_{GS} = 0V$	
Gate-Body Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(TH)}	0.5	1.2	1.5	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	1.4	3.5	Ω	$V_{GS} = 10V, I_D = 0.22A$	
Forward Transconductance	g _{FS}	100	_	_	mS	$V_{DS} = 25V$, $I_D = 0.2A$, $f = 1.0KHz$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{ISS}	_	_	50	pF		
Output Capacitance	Coss	_	_	25	pF	$V_{DS} = 10V, V_{GS} = 0V, f = 1.0MHz$	
Reverse Transfer Capacitance	C _{RSS}	_	_	8.0	pF	1	
SWITCHING CHARACTERISTICS							
Turn-On Delay Time	t _{D(ON)}		_	20	ns	$V_{DD} = 30V, I_D = 0.2A,$	
Turn-Off Delay Time	t _{D(OFF)}	_		20	ns	$R_{GEN} = 50\Omega$	

Notes:

- 5. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown at http://www.diodes.com/package-outlines.html.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. $R_{GS} \le 20K\Omega$.







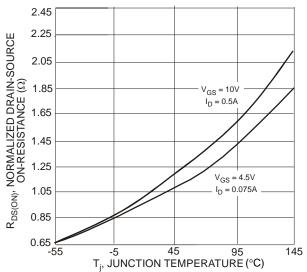


Fig. 3 Drain-Source On Resistance vs. Junction Temperature

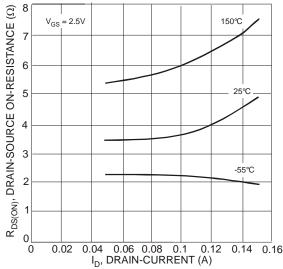
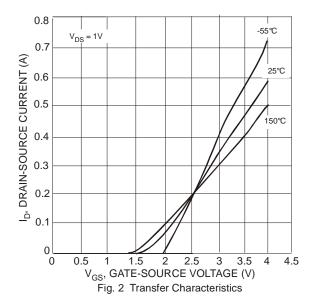


Fig. 5 Drain-Source On-Resistance vs. Drain-Current



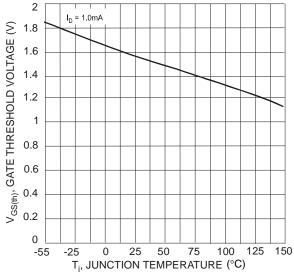


Fig. 4 Gate Threshold Voltage vs. Junction Temperature

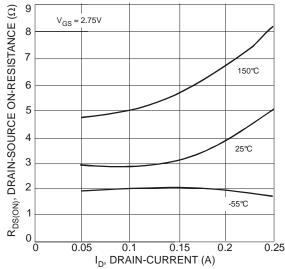
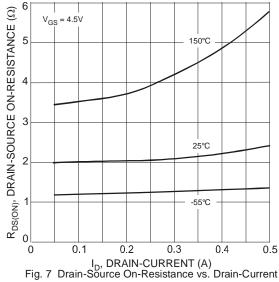
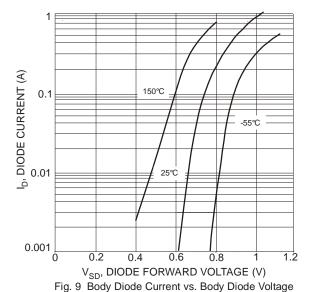


Fig. 6 Drain-Source On-Resistance vs. Drain-Current







3.5 150℃ $R_{DS(ON)},\,DRAIN\text{-}SOURCE\,ON\text{-}RESISTANCE}\,(\Omega)$ V_{GS} = 10V 25°C -55°C 0.5 0 0.1 0.2 0.3 0.4 0.5 I_D, DRAIN-CURRENT (A) Fig. 8 Drain-Source On-Resistance vs. Drain-Current

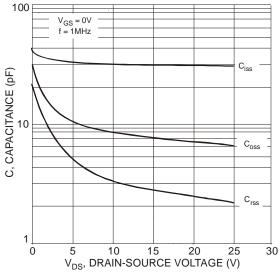


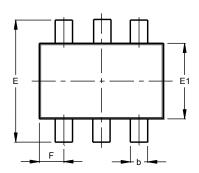
Fig. 10 Capacitance vs. Drain-Source Voltage

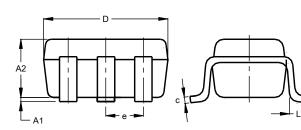


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



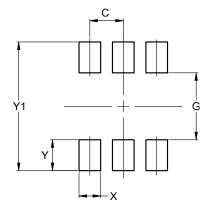


SOT363							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	1.00				
b	0.10	0.30	0.25				
С	0.10	0.22	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
e	e 0.650 BSC						
F	0.40	0.45	0.425				
L	0.25	0.40	0.30				
а	0°	8°	_				
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



Dimensions	Value
	(in mm)
C	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2.500



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