

#### HEX INVERTERS WITH SCHMITT TRIGGER INPUTS

### **Description**

The 74HC14 provides provides six independent Schmitt trigger input inverters with standard push-pull outputs. The device is designed for operation with a power supply range of 2.0V to 6.0V.

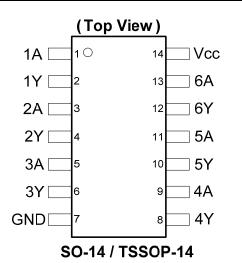
The gates perform the Boolean function:



#### **Features**

- Wide Supply Voltage Range from 2.0V to 6.0V
- Sinks or Sources 4mA at V<sub>CC</sub> = 4.5V
- **CMOS Low Power Consumption**
- Schmitt Trigger Action at All Inputs
- ESD Protection Exceeds JESD 22
  - 200-V Machine Model (A115-A)
  - 2000-V Human Body Model (A114-A)
  - Exceeds 1000-V Charged Device Model (C101C)
- Range of Package Options SO-14 and TSSOP-14
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

### **Pin Assignments**



### **Applications**

- General Purpose Logic
- Wide array of products such as:
  - PCs, Networking, Notebooks, Netbooks
  - Computer Peripherals, Hard Drives, CD/DVD ROM
  - TV, DVD, DVR, Set Top Box

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

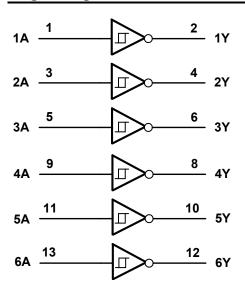
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## **Pin Descriptions**

Pin Number	Pin Name	Function
1	1A	Data Input
2	1Y	Data Output
3	2A	Data Input
4	2Y	Data Output
5	3A	Data Input
6	3Y	Data Output
7	GND	Ground
8	4Y	Data Output
9	4A	Data Input
10	5Y	Data Output
11	5A	Data Input
12	6Y	Data Output
13	6A	Data Input
14	Vcc	Supply Voltage

## **Logic Diagram**



## **Function Table**

Input	Output
Α	Υ
Н	L
L	Н



## Absolute Maximum Ratings (Note 4) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V <sub>CC</sub>	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range (Note 5)	-0.5 to +7.0	V
I <sub>IK</sub>	Input Clamp Current V <sub>I</sub> < -0.5V or Vi > V <sub>CC</sub> +0.5V	±20	mA
lok	Output Clamp Current $V_O < -0.5V$ or $V_O > V_{CC} +0.5V$	±20	mA
Io	Continuous Output Current -0.5V < V <sub>O</sub> V <sub>CC</sub> +0.5V	+/- 25	mA
Icc	Continuous Current Through V <sub>CC</sub>	50	mA
I <sub>GND</sub>	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C
Ртот	Total Power Dissipation	500	mW

## Recommended Operating Conditions (Note 6) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	Supply Voltage		2.0	6.0	V
VI	Input Voltage		0	V <sub>CC</sub>	V
Vo	Output Voltage		0	$V_{CC}$	V
		V <sub>CC</sub> = 2.0V		625	
Δt/ΔV	Input Transition Rise or Fall Rate	V <sub>CC</sub> = 4.5V		140	ns/V
		V <sub>CC</sub> = 6.0V		85	
T <sub>A</sub>	Operating Free-Air Temperature		-40	+125	°C

Note:

6. Unused inputs should be held at  $V_{\text{CC}}$  or Ground.

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<sup>4.</sup> Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

<sup>5.</sup> Input Voltage cannot exceed  $V_{\text{CC}}$  to the extent the Maximum clamp current is exceeded



## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Cumbal	Parameter	Test Conditions	V	T <sub>A</sub> = -40°	C to +85°C	T <sub>A</sub> = -40°C	to +125°C	Unit
Symbol	Parameter	rest Conditions	V <sub>CC</sub>	Min	Max	Min	Max	Unit
	D O		2.0V	0.7	1.5	0.7	1.5	V
$V_{T+}$	Positive-Going Input Threshold Voltage		4.5V	1.7	3.15	1.7	3.15	
	Thiconola voltage		6.0V	2.1	4.2	2.1	4.2	
	Negative-Going		2.0V	0.3	0.9	0.3	0.9	
$V_{T-}$	Input Threshold		4.5V	0.9	2.0	0.9	2.0	V
	Voltage		6.0V	1.2	2.6	1.2	2.6	
	Hyeteresis		2.0V	0.2	1.0	0.2	1.0	
$\Delta V_{T}$	Hysteresis (V <sub>T+</sub> - V <sub>T-)</sub>		4.5V	0.4	1.4	0.4	1.4	V
	( • 1 + • 1 -)		6.0V	0.6	1.6	0.6	1.6	
		$I_{OH} = -20 \mu A$	2.0V	1.9		1.9		
		$I_{OH} = -20\mu A$	4.5V	4.4		4.4		V
VoH	High Level Output Voltage	I <sub>OH</sub> = -20μA	6.0V	5.9		5.9		
	Voltage	$I_{OH} = -4.0 \text{mA}$	4.5V	3.84		3.7		
		I <sub>OH</sub> = -5.2mA	6.0V	5.34		5.2		1
		I <sub>OL</sub> = 20μA	2.0V		0.1		0.1	
		I <sub>OL</sub> = 20μA	4.5V		0.1		0.1	
V <sub>OL</sub>	Low Level Output Voltage	I <sub>OL</sub> = 20μA	6.0V		0.1		0.1	V
	Voltago	I <sub>OL</sub> = 4mA	4.5V		0.33		0.40	
		I <sub>OL</sub> = 5.2mA	6.0V		0.33		0.40	
l <sub>l</sub>	Input Current	V <sub>I</sub> =GND to 5.5V	6.0V		± 1		± 1	μA
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}, I_O = 0$	6.0V		20		40	μΑ

# **Switching Characteristics**

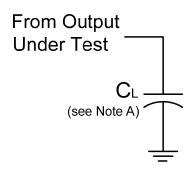
Symbol	Symbol Parameter Test		Voc		Γ <sub>A</sub> = +25°0	:	-40°C to +85°C	-40°C to +125°C	Unit
Syllibol	Parameter	Conditions	VCC	Min	Тур	Max	Max	Max	Oilit
	December	Figure 1	2.0V	_	42	125	155	190	
$t_{PD}$	Propagation Figure 1 Delay $A_N$ to $Y_N$ $C_L = 50 pF$	4.5V	_	15	25	31	38	ns	
		6.0V	_	12	21	26	32		
		Figure 4	2.0V	_	19	75	95	110	
t <sub>t</sub>	t <sub>t</sub> Transition Time	Figure 1 C <sub>L</sub> = 50 pF	4.5V	_	7	15	19	22	ns
		CL = 50 pr	6.0V	_	6	13	16	19	

## Operating Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

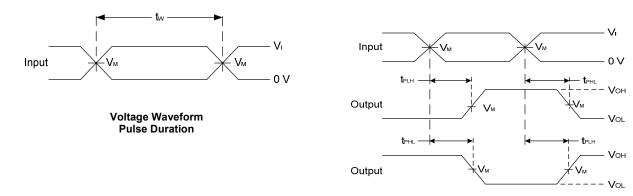
Parameter		Test Conditions	V <sub>CC</sub> = 6V	Unit
C <sub>pd</sub>	Power dissipation capacitance per gate	f = 1 MHz	<b>Typ</b> 20	pF
Cı	Input Capacitance	$V_1 = V_{CC} - \text{ or GND}$	4	pF



### **Parameter Measurement Information**



Vcc	lnį	outs	V <sub>M</sub>	CL
	Vı	t <sub>r</sub> /t <sub>f</sub>		
2.0V to 6.0V	V <sub>CC</sub>	6ns	V <sub>CC</sub> /2	15pF, 50pF



Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz
- C. Inputs are measured separately one transition per measurement.
- D.  $t_{\text{PLH}}$  and  $t_{\text{PHL}}$  are the same as  $t_{\text{PD}}.$

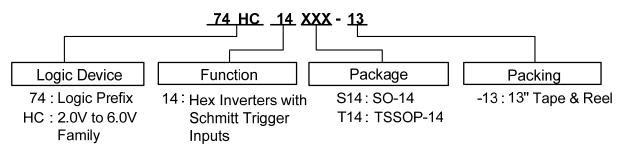
Figure 1 Load Circuit and Voltage Waveforms

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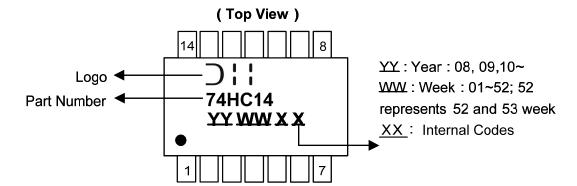
## **Ordering Information**



	Davies Backage Code		Device Package Code Packaging		e and Reel
	Device	Package Code	Packaging	Quantity	Part Number Suffix
Pb Lead-free Green	74HC14S14-13	S14	SO-14	2500/Tape & Reel	-13
Lead-free Green	74HC14T14-13	T14	TSSOP-14	2500/Tape & Reel	-13

### **Marking Information**

(1) SO-14, TSSOP-14



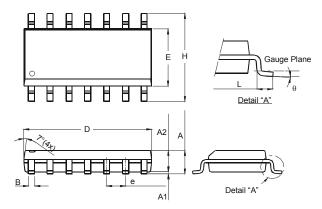
Part Number	Package
74HC14S14	SO-14
74HC14T14	TSSOP-14



## Package Outline Dimensions (All dimensions in mm.)

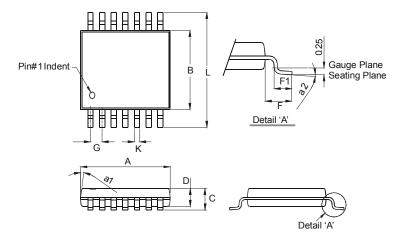
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

#### Package Type: SO-14



	SO-14					
Dim	Min	Max				
Α	1.47	1.73				
A1	0.10	0.25				
A2	1.45 Typ					
В	0.33	0.51				
D	8.53	8.74				
Е	3.80	3.99				
е	1.27	Тур				
Н	5.80	6.20				
L	0.38	1.27				
θ	0°	8°				
All Dimensions in mm						

### Package Type: TSSOP-14



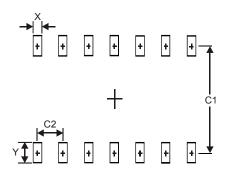
	TSSOP-14					
Dim	Min Max					
a1	7° (	4X)				
a2	0°	8°				
Α	4.9	5.10				
В	4.30	4.50				
С	_	1.2				
D	0.8	1.05				
F	1.00	Тур				
F1	0.45	0.75				
G	0.65 Typ					
K	0.19	0.30				
L	6.40	Тур				
All Dimensions in mm						



### **Suggested Pad Layout**

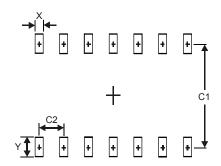
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.

#### Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Υ	1.50
C1	5.4
C2	1.27

#### Package Type: TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Y	1.45
C1	5.9
C2	0.65



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