



100V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
100V	9.5mΩ @ V _{GS} = 10V	100A

Features

- Rated to +175°C—Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching—Ensures More Reliable and Robust End Application
- Low R_{DS(ON)}—Minimizes Power Losses
- Low Qg-Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Description and Applications

This new generation N-channel enhancement mode MOSFET is designed to minimize R_{DS(ON)} yet maintain superior switching performance. This device is ideal for high-efficiency power management applications.

- Synchronous Rectification
- Inverter
- **DC-DC Converters**

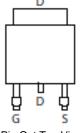
Mechanical Data

- Case: TO263AB (D2PAK)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 1.7 grams (Approximate)

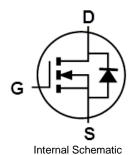




Top View



Pin Out Top View



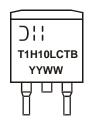
Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH10H010LCTB-13	TO263AB (D2PAK)	800 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



☐ H= Manufacturer's Marking T1H10LCTB = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 18 = 2018) WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V_{DSS}	100	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I _D	100 71	А
Maximum Continuous Body Diode Forward Current $T_C = +25$ °C		Is	110	Α
Pulsed Drain Current (10µs Pulse, T _C =+25°C, Package Limited)		I _{DM}	400	Α
Pulsed Body Diode Forward Current (10µs Pulse, Tc=+25°C, Package Limited)		I _{SM}	400	Α
Avalanche Current, L=0.3mH (Note 7)		I _{AS}	35	Α
Avalanche Energy, L=0.3mH (Note 7)		E _{AS}	187	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	P_{D}	3.9	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\Theta JA}$	32	°C/W
Total Power Dissipation	$T_C = +25^{\circ}C$	P_{D}	125	W
Thermal Resistance, Junction to Case		Rejc	1.0	°C/W
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

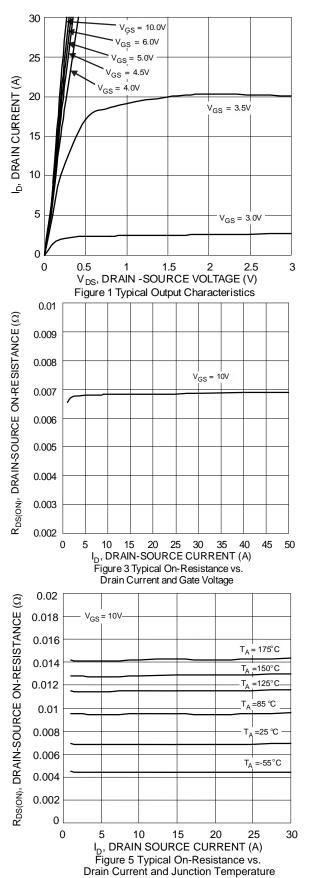
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)						•	
Drain-Source Breakdown Voltage	BV _{DSS}	100	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	1		1	μΑ	$V_{DS} = 80V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		I	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(TH)}	1.4	2.0	3.5	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance			8.7	9.5	mΩ	$V_{GS} = 10V, I_D = 13A$	
Static Diani-Source On-Resistance	R _{DS(ON)}	1	13.2	17	11122	$V_{GS} = 4.5V, I_D = 13A$	
Diode Forward Voltage	V_{SD}	1	0.8	1.3	V	$V_{GS} = 0V, I_{S} = 13A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C _{iss}	1	2592	_		V _{DS} = 50V, V _{GS} = 0V f = 1MHz	
Output Capacitance	Coss		792	_	рF		
Reverse Transfer Capacitance	C _{rss}	_	45	_			
Gate Resistance	R_g	1	2	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	1	53.7	_			
Gate-Source Charge	Q _{gs}	_	10.6	_	nC	$V_{DD} = 50V, I_D = 13A,$ $V_{GS} = 10V$	
Gate-Drain Charge	Q _{gd}	_	8.2	_		VGS = 10V	
Turn-On Delay Time	t _{D(ON)}	_	11.6	_			
Turn-On Rise Time	t _R	_	14.1	_		$V_{DD} = 50V, V_{GS} = 10V,$	
Turn-Off Delay Time	t _{D(OFF)}	_	42.9	_	ns	$I_D = 13A$, $R_g = 6\Omega$	
Turn-Off Fall Time	t _F		22	_			
Reverse Recovery Time	t _{RR}	_	49.8	_	ns	1 424 4:/44 4004/	
Reverse Recovery Charge	Q _{RR}		85.1	_	nC	$I_F = 13A$, di/dt = 100A/ μ s	

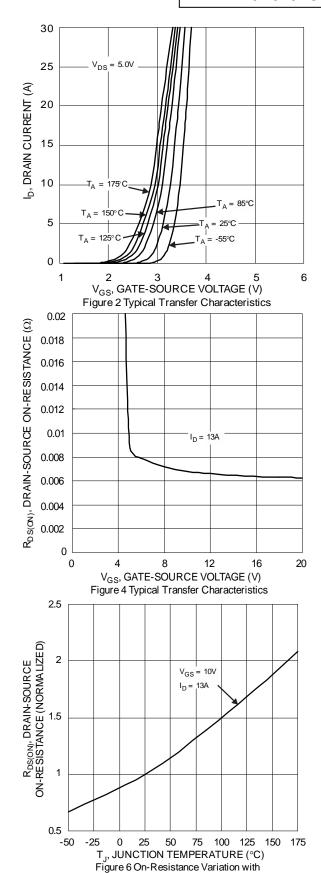
Notes:

- 5. Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.6. Short duration pulse test used to minimize self-heating effect.7. Guaranteed by design. Not subject to product testing.



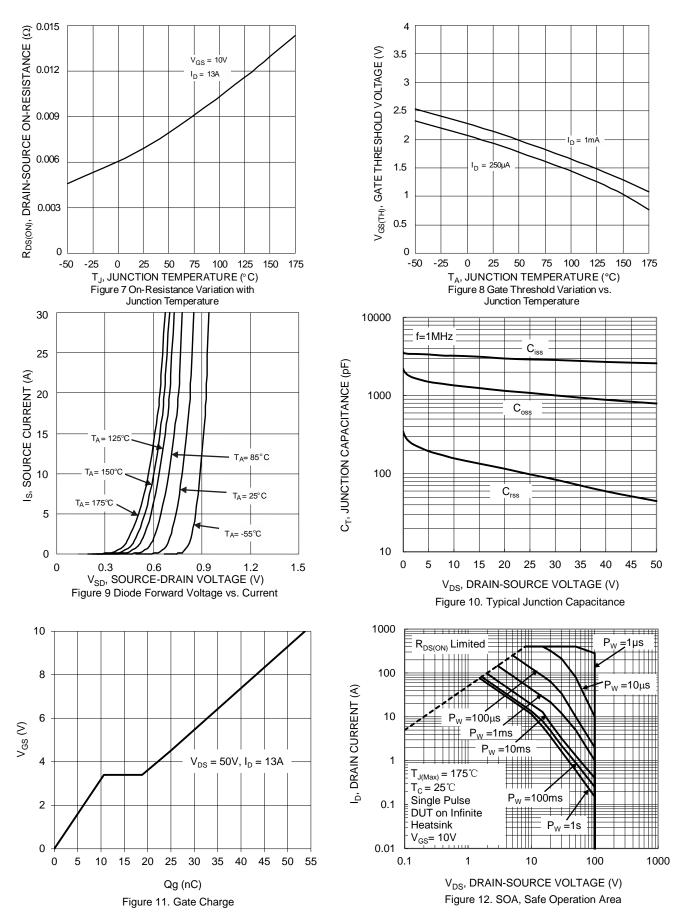






Junction Temperature







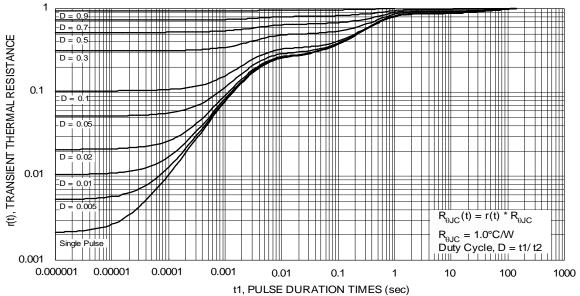
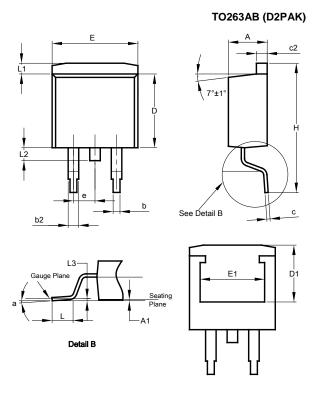


Figure 13 Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

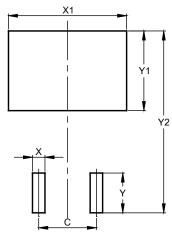


TO263AB (D2PAK)				
Dim	Min	Max	Тур	
Α	4.07	4.82	-	
A1	0.00	0.25	-	
b	0.51	0.99	-	
b2	1.15	1.77	-	
С	0.356	0.73	-	
c2	1.143	1.65	-	
D	8.39	9.65	-	
D1	6.55	6.95	-	
е	2.54 TYP			
Е	9.66	10.66	-	
E1	6.23	8.23	-	
Н	14.61	15.87	-	
L	1.78	2.79	-	
L1	-	1.67	-	
L2	-	1.77	-	
L3	-	-	0.254	
а	0°	8°	-	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO263AB (D2PAK)



Dimensions	Value (in mm)
С	5.08
Х	1.10
X1	10.41
Y	3.50
Y1	7.01
Y2	15.99



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