

# ZDT1048 SM-8 Dual NPN medium power high gain transistors

### **Summary**

 $BV_{CEO} > 17.5V$ 

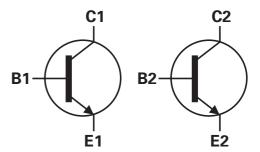
 $I_{C(cont)} = 5A$ 

V<sub>CE(sat)</sub> < 75mV @ 1A

 $P_{D} = 2.75W$ 



Advanced process capability has been used to achieve this high performance device. Combining two NPN transistors in the SM-8 package provides a compact solution for the intended applications.



#### **Features**

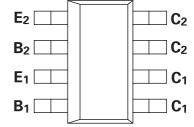
- · Dual NPN device
- · Very low saturation voltage
- · High gain
- SM 8 package

### **Applications**

- · CCFL invertors
- Royer circuits

## **Ordering information**

DEVICE	Reel size (inches)	Tape width (mm)	Quantity per reel
ZDT1048TA	7	12	1000



### **Device marking**

T1048

## **Absolute maximum ratings**

Parameter	Symbol	Value	Unit
Collector-base voltage	V <sub>CBO</sub>	50	V
Collector-emitter voltage	V <sub>CEO</sub>	17.5	V
Emitter-base voltage	V <sub>EBO</sub>	5	V
Peak pulse current	I <sub>CM</sub>	20	Α
Continuous collector current	I <sub>C</sub>	5	А
Base current	I <sub>B</sub>	500	mA
Operating and storage temperature range	T <sub>j</sub> :T <sub>stg</sub>	-55 to +150	°C

### **Thermal Characteristics**

Parameter	Symbol	Value	Unit
Total power dissipation at T <sub>amb</sub> = 25°C*	P <sub>tot</sub>		
Any single die "on"		2.25	W
Both die "on" equally		2.75	W
Derate above 25°C*			V
Any single die "on"		18	mW/°C
Both die "on" equally		22	mW/°C
Thermal resistance - junction to ambient*			
Any single die "on"		55.6	°C/W
Both die "on" equally		45.5	°C/W

<sup>\*</sup> The power which can be dissipated assuming the device is mounted in a typical manner on a PCB with copper equal to 2 inches square.

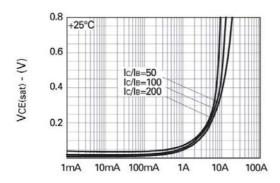
## Electrical characteristics (at $T_{amb} = 25$ °C unless otherwise stated).

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	V <sub>(BR)CBO</sub>	50	85		V	I <sub>C</sub> =100μA
Collector-emitter breakdown voltage	V <sub>CES</sub>	50	85		V	I <sub>C</sub> =100μA
Collector-emitter breakdown voltage	V <sub>CEO</sub>	17.5	24		V	I <sub>C</sub> =10mA
Collector-emitter breakdown voltage	V <sub>CEV</sub>	50	85		V	I <sub>C</sub> =100μA, V <sub>EB</sub> =1V
Emitter-base breakdown voltage	V <sub>(BR)EBO</sub>	5	8.7		V	I <sub>E</sub> =100μA
Collector cut-off current	I <sub>CBO</sub>		0.3	10	nA	V <sub>CB</sub> =35V
Emitter cut-off current	I <sub>EBO</sub>		0.3	10	nA	V <sub>EB</sub> =4V
Collector-emitter cut-off current	I <sub>CES</sub>		0.3	10	nA	I <sub>CES</sub> =35V
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>		27 55 120 200 200	45 75 160 240 300	mV mV mV mV	I <sub>C</sub> =0.5A, I <sub>B</sub> =10mA (*) I <sub>C</sub> =1A, I <sub>B</sub> =10mA(*) I <sub>C</sub> =2A, I <sub>B</sub> =10mA(*) I <sub>C</sub> =5A, I <sub>B</sub> =100mA(*) I <sub>C</sub> =5A, I <sub>B</sub> =50mA(*)
Base-emitter saturation voltage	V <sub>BE(sat)</sub>		1000	1100	mV	I <sub>C</sub> =5A, I <sub>B</sub> =100mA <sup>(*)</sup>
Base-emitter turn on voltage	V <sub>BE(on)</sub>		900	1000	mV	I <sub>C</sub> =5A, V <sub>CE</sub> =2V <sup>(*)</sup>
Static forward current transfer ratio	h <sub>FE</sub>	280 300 300 250 50	440 450 450 300 80	1200		I <sub>C</sub> =10mA, V <sub>CE</sub> =2V <sup>(*)</sup> I <sub>C</sub> =0.5A, V <sub>CE</sub> =2V <sup>(*)</sup> I <sub>C</sub> =1A, V <sub>CE</sub> =2V <sup>(*)</sup> I <sub>C</sub> =5A, V <sub>CE</sub> =2V <sup>(*)</sup> I <sub>C</sub> =20A, V <sub>CE</sub> =2V <sup>(*)</sup>
Transition frequency	f <sub>T</sub>		150		MHz	I <sub>C</sub> =50mA, V <sub>CE</sub> =10V f=50MHz
Output capacitance	C <sub>obo</sub>		60	80	pF	V <sub>CB</sub> =10V, f=1MHz
Switching times	t <sub>on</sub>		120		ns	I <sub>C</sub> =4A, I <sub>B</sub> =40mA,V <sub>CC</sub> =10V
	t <sub>off</sub>		250		ns	$I_C=4A$ , $I_B=\pm 40$ mA, $V_{CC}=10$ V

#### NOTES:

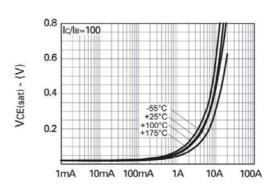
(\*) Measured under pulsed conditions. Pulse width=300 $\mu$ s. Duty cycle  $\leq 2\%$ 

### **Typical characteristics**



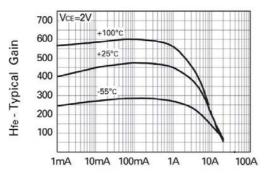
**IC-Collector Current** 

### VCE(sat) v IC



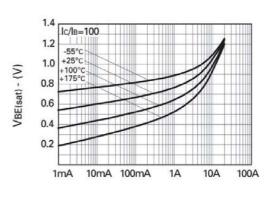
**IC-Collector Current** 

### VCE(sat) v IC



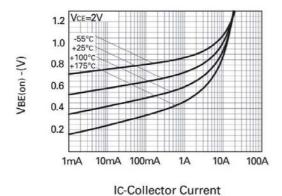
**IC-Collector Current** 

hfe v lc

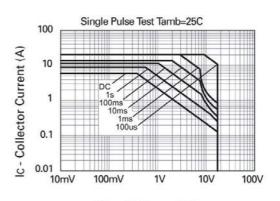


**IC-Collector Current** 

### VBE(sat) v lc



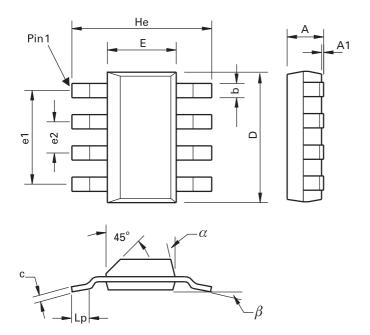
VBE(on) v IC



VCE - Collector Voltage

Safe Operating Area

## Package outline - SM8



DIM	N	1illimete	rs	Inches		s DIM		Millimeters			Inches		
	Min.	Max.	Тур.	Min.	Max.	Тур.		Min.	Max.	Тур.	Min.	Max.	Тур.
Α	-	1.7	-	-	0.067	-	e1	-	-	4.59	-	-	0.1807
A1	0.02	0.1	-	0.0008	0.004	-	e2	-	-	1.53	-	-	0.0602
b	-	-	0.7	-	-	0.0275	He	6.7	7.3	-	0.264	0.287	-
С	0.24	0.32	-	0.009	0.013	-	Lp	0.9	-	-	0.035	-	-
D	6.3	6.7	-	0.248	0.264	-	α	-	15°	-	-	15°	-
Е	3.3	3.7	-	0.130	0.145	-	β	-	-	10°	-	-	10°

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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  - 1. are intended to implant into the body

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