

## Product Summary

$BV_{DSS}$	$R_{DS(ON)}$ max	$I_D$ max $T_C = +25^\circ\text{C}$
60V	8m $\Omega$ @ $V_{GS} = 10\text{V}$	70A

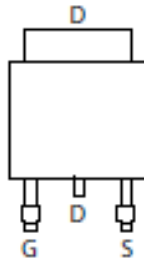
## Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

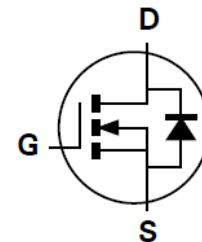
- Power Management Functions
- DC-DC Converters
- Backlighting



Top View



Pin Out Top View



Equivalent Circuit

## Features

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- Low  $R_{DS(ON)}$  – Ensures On State Losses Are Minimized
- Excellent  $Q_{gd} \times R_{DS(ON)}$  Product (FOM)
- Advanced Technology for DC/DC Converters
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **An Automotive-Compliant Part is Available Under Separate Datasheet ([DMTH6010SK3Q](#))**

## Mechanical Data

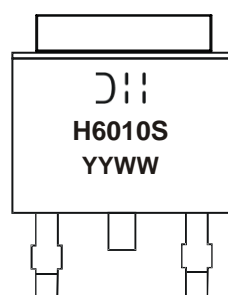
- Case: TO252
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish - Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.33 grams (Approximate)

## Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH6010SK3-13	TO252 (DPAK)	2,500/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



= Manufacturer's Marking  
 H6010S = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY = Last Two Digits of Year (ex: 18 = 2018)  
 WW = Week Code (01 to 53)

**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	60	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Continuous Drain Current (Note 5)	$I_D$	$T_A = +25^\circ\text{C}$	16.3
		$T_A = +70^\circ\text{C}$	13.6
Continuous Drain Current (Note 6)	$I_D$	$T_C = +25^\circ\text{C}$	70
		$T_C = +100^\circ\text{C}$	49
Maximum Continuous Body Diode Forward Current (Note 5)	$I_S$	3	A
Pulsed Drain Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%)	$I_{DM}$	280	A
Pulsed Body Diode Forward Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%)	$I_{SM}$	280	A
Avalanche Current, L=0.1mH	$I_{AS}$	20	A
Avalanche Energy, L=0.1mH	$E_{AS}$	27.7	mJ

**Thermal Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	$P_D$	3.1	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	47	$^\circ\text{C/W}$
Total Power Dissipation (Note 6)	$P_D$	59	W
Thermal Resistance, Junction to Case (Note 6)	$R_{\theta JC}$	2.5	$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +175	$^\circ\text{C}$

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	60	-	-	V	$V_{GS} = 0V, I_D = 1mA$
Zero Gate Voltage Drain Current	$I_{DSS}$	-	-	1	$\mu\text{A}$	$V_{DS} = 48V, V_{GS} = 0V$
Gate-Source Leakage	$I_{GSS}$	-	-	$\pm 100$	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	2	-	4	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	-	5.4	8	m $\Omega$	$V_{GS} = 10V, I_D = 20A$
Diode Forward Voltage	$V_{SD}$	-	0.84	1.2	V	$V_{GS} = 0V, I_S = 20A$
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	$C_{iss}$	-	2841	-	pF	$V_{DS} = 30V, V_{GS} = 0V, f = 1MHz$
Output Capacitance	$C_{oss}$	-	690	-		
Reverse Transfer Capacitance	$C_{rss}$	-	46	-		
Gate Resistance	$R_g$	-	0.55	-	$\Omega$	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge	$Q_g$	-	38.1	-	nC	$V_{DS} = 30V, I_D = 20A, V_{GS} = 10V$
Gate-Source Charge	$Q_{gs}$	-	8.3	-		
Gate-Drain Charge	$Q_{gd}$	-	9.3	-		
Turn-On Delay Time	$t_{D(ON)}$	-	8.6	-	ns	$V_{DD} = 30V, V_{GS} = 10V, I_D = 20A, R_G = 3\Omega$
Turn-On Rise Time	$t_R$	-	8.2	-		
Turn-Off Delay Time	$t_{D(OFF)}$	-	17.4	-		
Turn-Off Fall Time	$t_F$	-	5.7	-		
Body Diode Reverse Recovery Time	$t_{RR}$	-	33.8	-	ns	$I_F = 20A, di/dt = 100A/\mu\text{s}$
Body Diode Reverse Recovery Charge	$Q_{RR}$	-	35.6	-	nC	

- Notes:
5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1 inch square copper plate.
  6. Device mounted on infinite heat sink and measured by thermal couple attached on bottom heat sink of package.
  7. Short duration pulse test used to minimize self-heating effect.
  8. Guaranteed by design. Not subject to product testing.

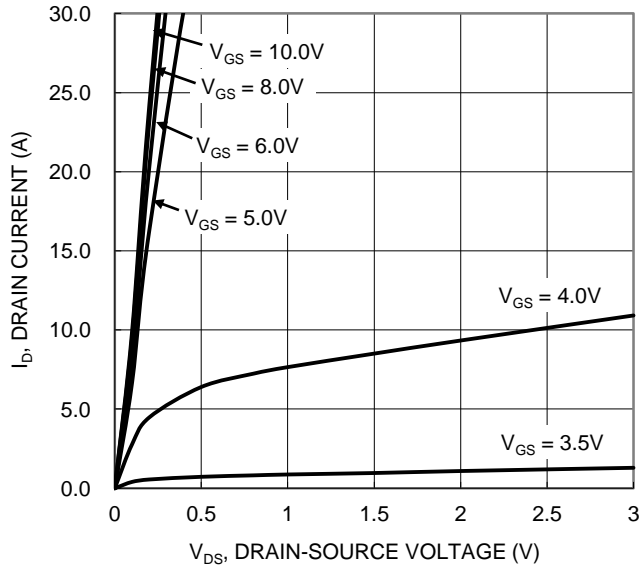


Figure 1. Typical Output Characteristic

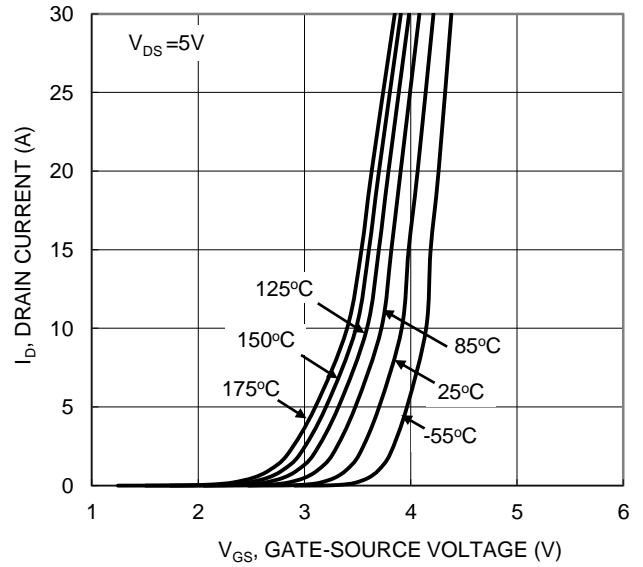


Figure 2. Typical Transfer Characteristic

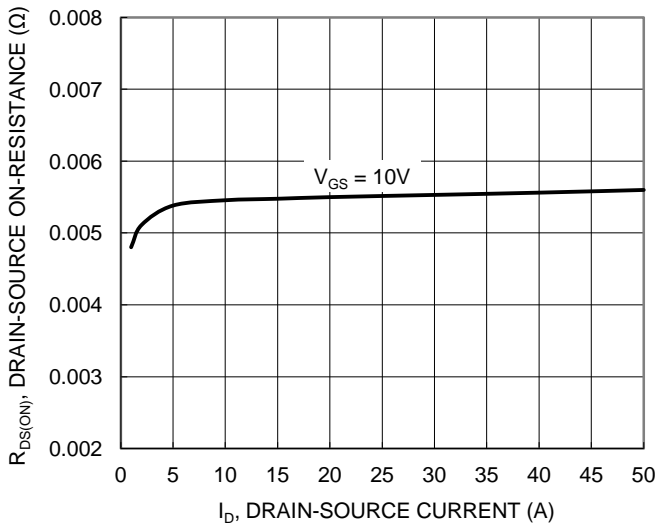


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

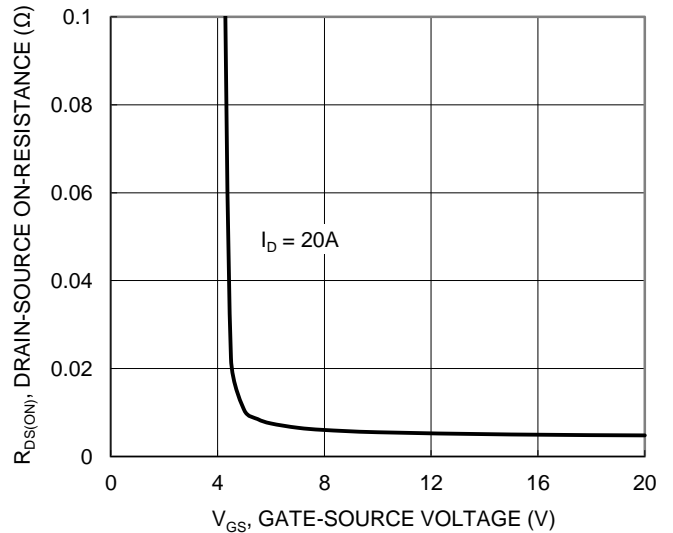


Figure 4. Typical Transfer Characteristic

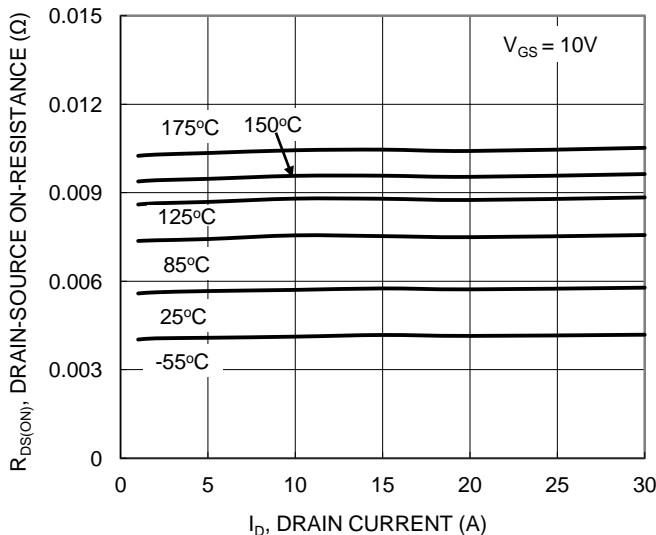


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

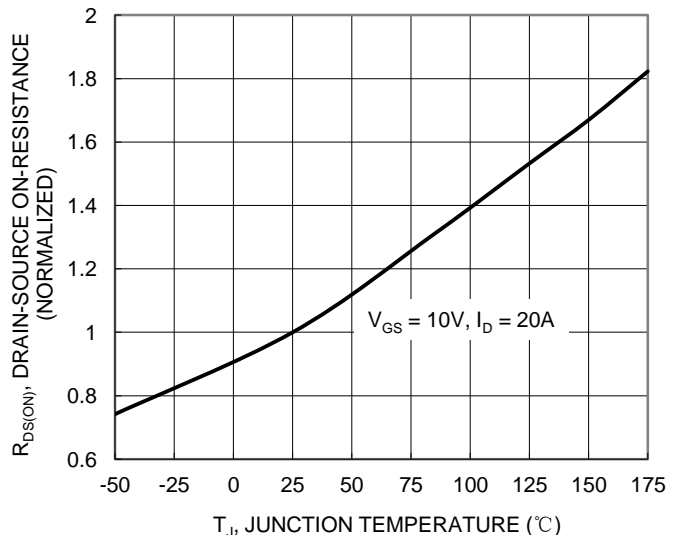


Figure 6. On-Resistance Variation with Junction Temperature

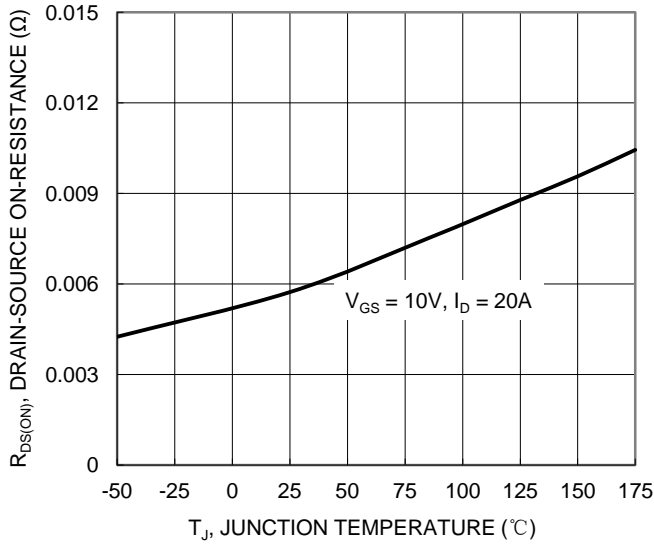


Figure 7. On-Resistance Variation vs. Junction Temperature

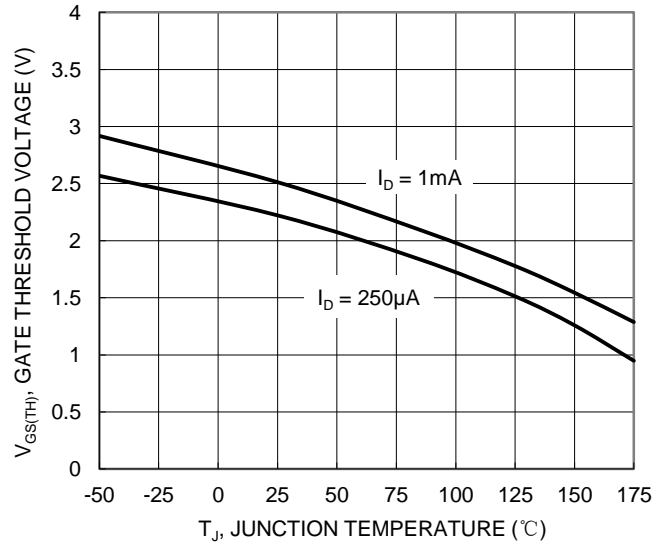


Figure 8. Gate Threshold Variation vs. Junction Temperature

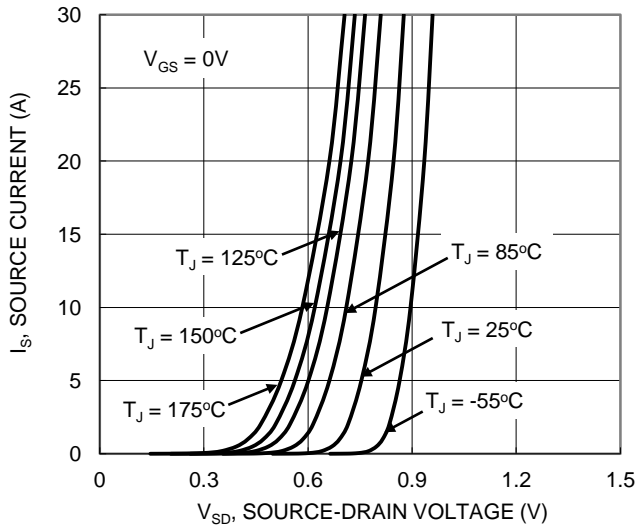


Figure 9. Diode Forward Voltage vs. Current

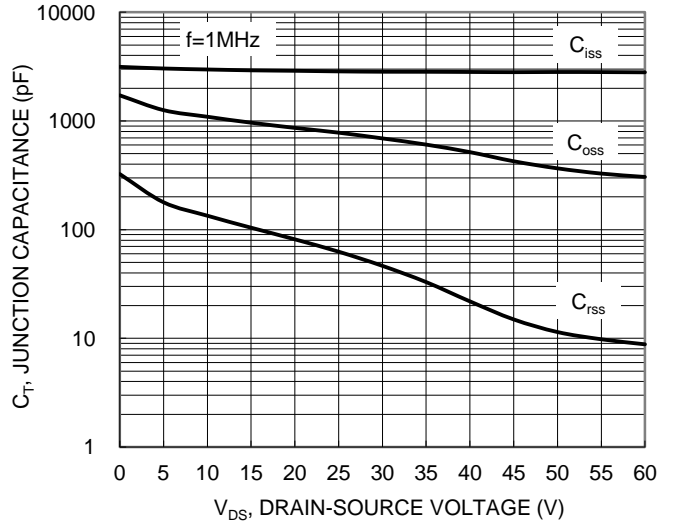


Figure 10. Typical Junction Capacitance

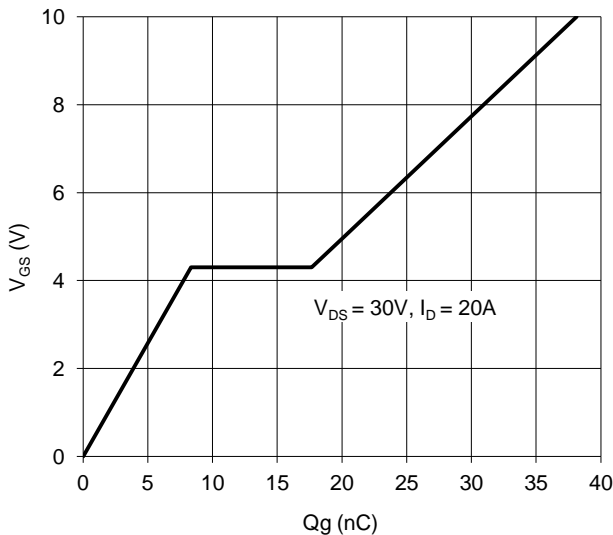


Figure 11. Gate Charge

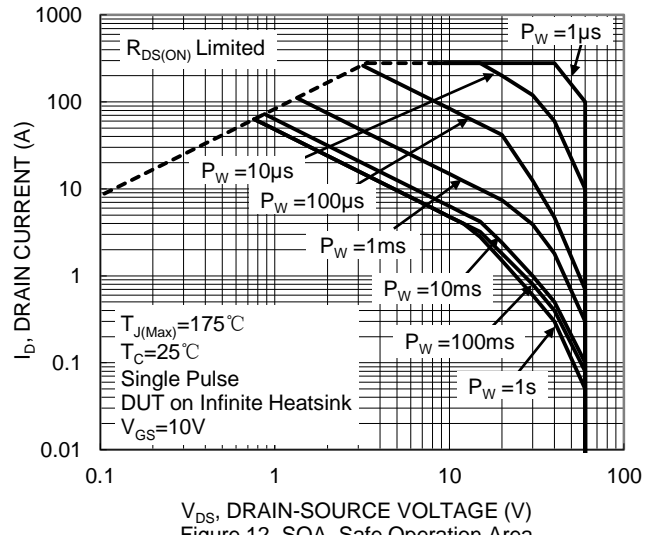


Figure 12. SOA, Safe Operation Area

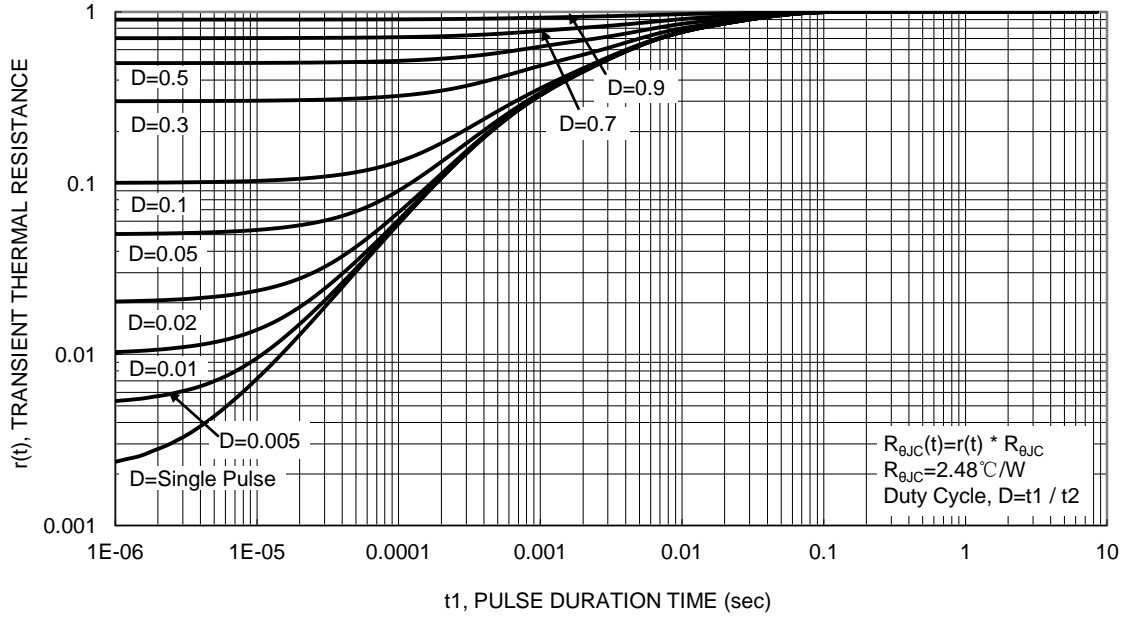
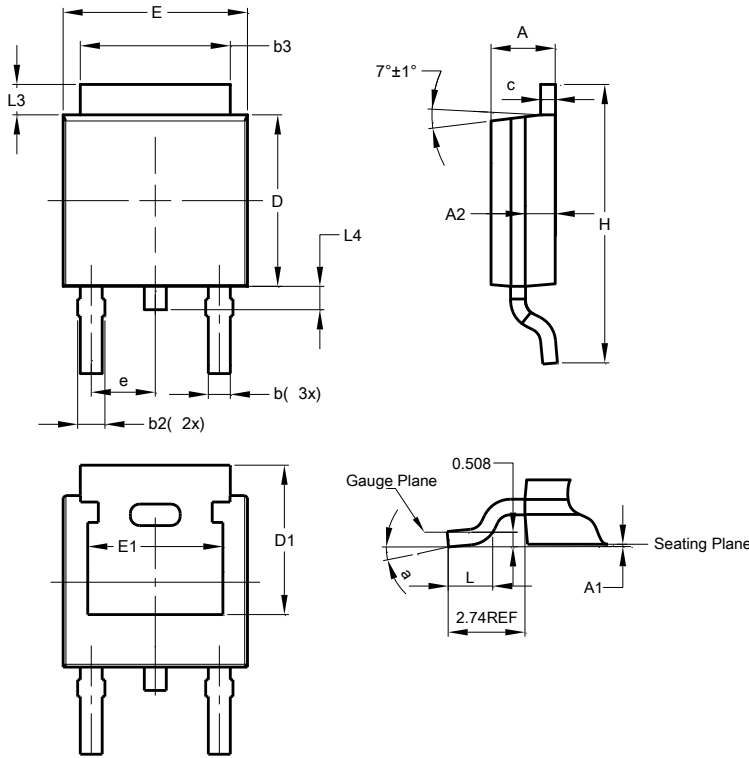


Figure 13. Transient Thermal Resistance

**Package Outline Dimensions**

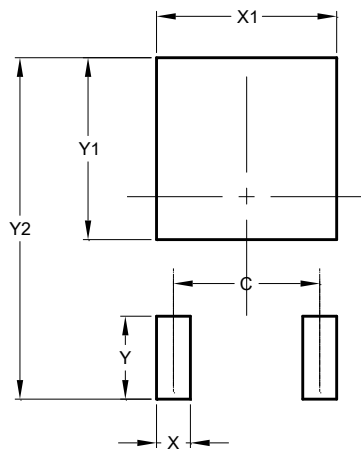
Please see <http://www.diodes.com/package-outlines.html> for the latest version.



TO252 (DPAK)			
Dim	Min	Max	Typ
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b2	0.76	1.14	0.95
b3	5.21	5.46	5.33
c	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	-	-
e	-	-	2.286
E	6.45	6.70	6.58
E1	4.32	-	-
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	-
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Dimensions	Value (in mm)
C	4.572
X	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700

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