



DMS3015SSS

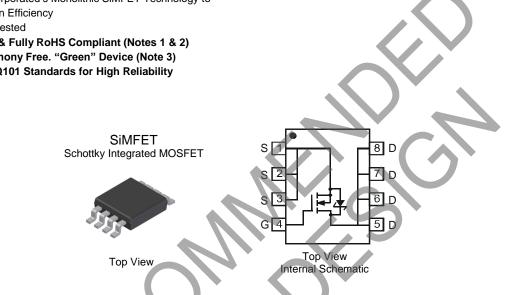
N-CHANNEL ENHANCEMENT MODE MOSFET WITH SCHOTTKY DIODE

Features

- High Density UMOS with Schottky Barrier Diode
- Low Leakage Current at High Temp.
- High Conversion Efficiency
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Utilizes Diodes Incorporated's Monolithic SiMFET Technology to Increase Conversion Efficiency
- 100% UIS and R_{α} Tested
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.072 grams (Approximate)



Ordering Information (Note 4)

	Part Number	Case	Packaging			
	DMS3015SSS-13	SO-8	2500 / Tape & Reel			
Notes:	s: 1. No purposely added lead, Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.					

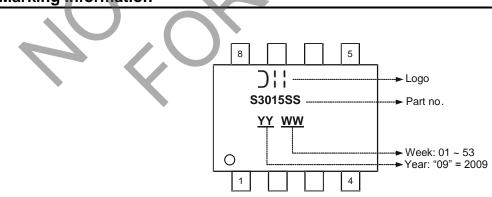
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

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Marking Information





Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic			Value	Unit
Drain-Source Voltage			30	V
		V _{GSS}	±20	V
Steady State	T _A = +25°C T _A = +85°C	ID	11 6.6	A
Pulsed Drain Current (Note 6)			80	A
Avalanche Current (Notes 6 & 7)			17	A
Repetitive Avalanche Energy (Notes 6 & 7) L = 0.3mH			43	mJ
	Steady State	Steady $T_A = +25^{\circ}C$ State $T_A = +85^{\circ}C$	$\begin{tabular}{ c c c c c } \hline & & & & & & & & \\ \hline & & & & & & & & &$	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	1.55	W
Thermal Resistance, Junction to Ambient $@T_A = +25$ °C (Note 5)	R _{eja}	81.3	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS		-	0.1	mA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	-		±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	1.0	1.5	2.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	Passau	-	8.5 9.5	11.9 14.9	mΩ	$V_{GS} = 10V, I_D = 11A$	
Static Drain-Source On-Resistance	R _{DS(ON)}					$V_{GS} = 4.5V, I_D = 8.8A$	
Forward Transfer Admittance	Y _{fs}	-	18	-	S	$V_{DS} = 5V, I_D = 10A$	
Diode Forward Voltage	V _{SD}	-	0.45	1	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	-	1276	-	pF		
Output Capacitance	C _{oss}		160	-	pF	│V _{DS} = 15V, V _{GS} = 0V, │f = 1.0MHz	
Reverse Transfer Capacitance	Crss	-	136	-	pF	1 = 1.00012	
Gate Resistance	Rg	-	1.48	2.7	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	-	14.3	-	nC	V _{DS} = 15V, V _{GS} = 4.5V, I _D = 8.8A	
Total Gate Charge (V _{GS} = 10V)		-	30.6	-	nC		
Gate-Source Charge	Q _{gs}	-	3.4	-	nC	V _{DS} = 15V, V _{GS} = 10V, I _D = 8.8A	
Gate-Drain Charge	Q _{gd}	-	4.3	-	nC	1	
Turn-On Delay Time	t _{D(ON)}	-	15.8	-	ns		
Turn-On Rise Time	t _R	-	27.8	-	ns	V _{GS} = 4.5V, V _{DS} = 15V,	
Turn-Off Delay Time	t _{D(OFF)}	-	29.7	-	ns	R _G = 1.8Ω, I _D =8.8A	
Turn-Off Fall Time	t _F	-	13.6	-	ns		

 Device mounted on 1in * 1in FR-4 PCB with 2oz. Copper. The value in any given application depends on the user's specific board design.
Repetitive rating, pulse width limited by junction temperature. Notes:

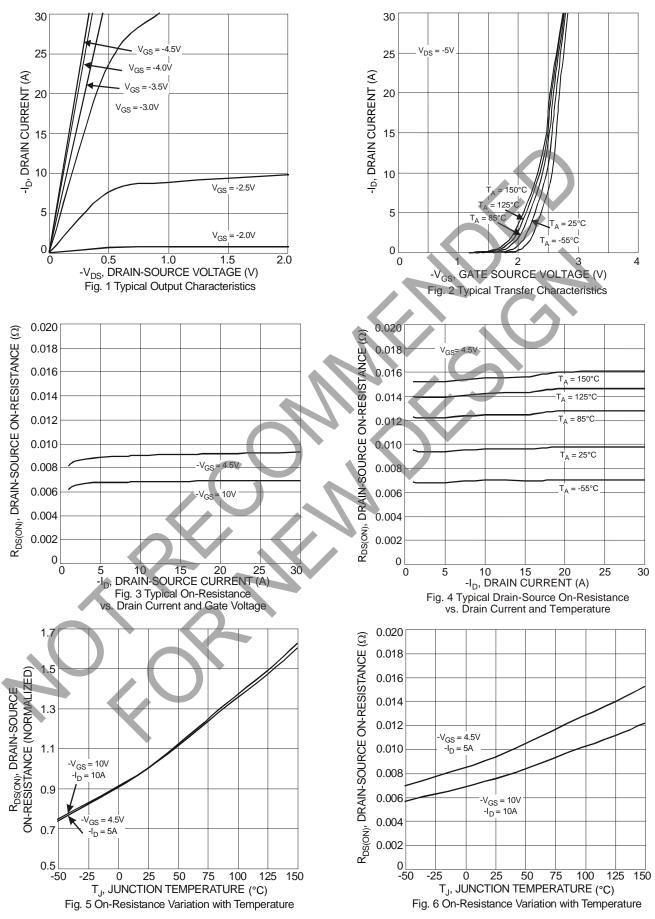
7. I_{AR} and E_{AR} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$.

Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to production testing.



NOT RECOMMENDED FOR NEW DESIGN -NO ALTERNATE PART

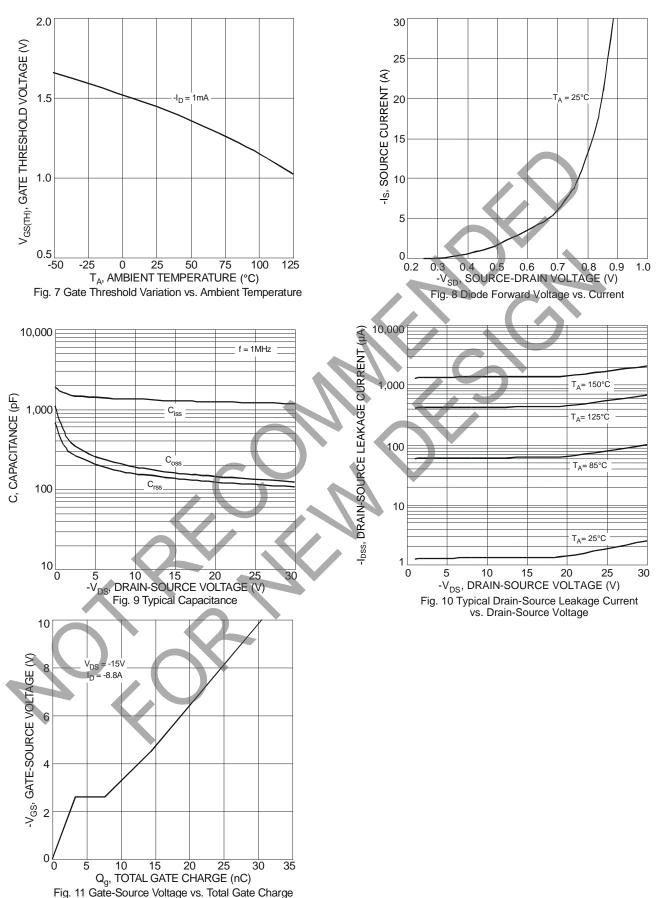
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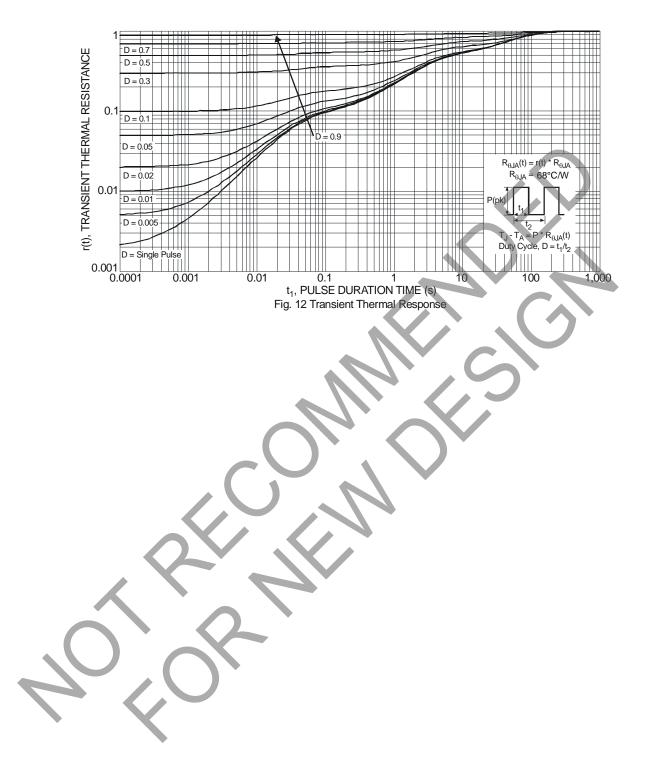
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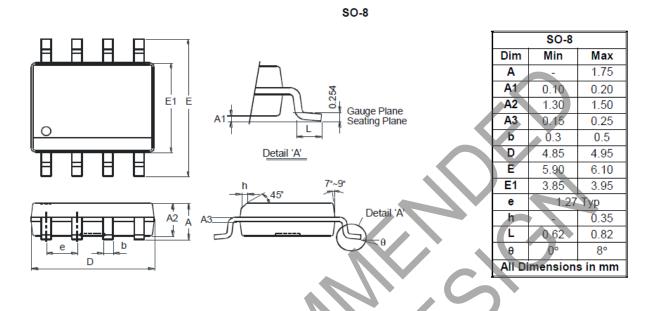
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

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	Dimensions	Value (in mm)
	X	0.60
	Y	1.55
	C1	5.4
C2	C2	1.27



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