



**Features and Benefits** 

Complementary P-channel Type ZVP4525E6

UL Flammability Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020

Weight: 0.015 grams (Approximate)

Solderable per MIL-STD-202, Method 208 @3

Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2) Halogen and Antimony Free. "Green" Device (Note 3)

Case Material: Molded Plastic, "Green" Molding Compound.

Terminals: Finish - Matte Tin Annealed over Copper Leadframe.

High voltage Low on-resistance Fast switching speed Low threshold Low gate drive

Mechanical Data

Case: SOT26

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#### **Product Summary**

V <sub>(BR)DSS</sub>	Max R <sub>DS(on)</sub>	Max I <sub>D</sub> T <sub>A</sub> = +25°C
250V	$8.5\Omega @ V_{GS} = 10V$	230mA

# **Description and Applications**

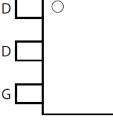
This 250V enhancement mode N-channel MOSFET provides users with a competitive specification. It offers efficient power handling capability, high impedance and is free from thermal runaway and thermally induced secondary breakdowns. Applications benefiting from this device include a variety of Telecom and general highvoltage circuits.

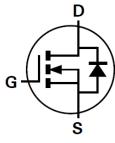
SOT89 and SOT223 versions are also available.

- Earth Recall and Dialing Switches
- Electronic Hook Switches .
- High Voltage Power MOSFET Drivers •
- **Telecom Call Routers**
- Solid State Relays



Top View





**Pinout Top-view** 

Device symbol

#### Ordering Information (Note 4)

Part Number	Reel Size (inch)	Tape Width (mm)	Quantity Per Reel
ZVN4525E6TA	7	8	3,000
ZVN4525E6TC	13	8	10,000

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl)

and <1000ppm antimony compounds. 4. For packaging details, go to our website at http://www.diodes.com.

## Marking Information

SOT26	$\Box$
N52	ź
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N52 = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: C = 2015) M or  $\overline{M}$  = Month (ex: 9 = September)

Date Code Kev

Dale Coue	rtey												
Year	201	5	2016	2017	2018	2019	2020	202	1 20	22	2023	2024	2025
Code	С		D	E	F	G	Н			J	К	L	М
Montl	n	Jan	Fel	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	•	1	2	3	4	5	6	7	8	9	0	N	D



#### **Maximum Ratings** (@T<sub>A</sub> = +25 °C, unless otherwise specified.)

	Characteristic	;	Symbol	Value	Unit	
Drain-Source Voltage			V <sub>DSS</sub>	250	V	
Gate-Source Voltage			V <sub>GS</sub>	±40	V	
Continuous Drain Current	101/	T <sub>A</sub> =+25 ℃ (Note 4)	I <sub>D</sub>	230	~ ^	
Continuous Drain Current	$V_{GS} = 10V$	T <sub>A</sub> =+70 ℃ (Note 4)		183	mA	
Pulsed Drain Current (Note	6)	-	I <sub>DM</sub>	1.44	А	
Continuous Source Current	(Body Diode)		Is	1.1	А	
Pulsed Source Current (Bod	y Diode)		I <sub>SM</sub>	1.44	А	

#### Thermal Characteristics (@TA = +25 °C, unless otherwise specified.)

Value	Unit					
1.1	W					
8.8	mW/℃					
113	°C/W					
65	℃/W					
Operating and Storage Temperature Range T <sub>J</sub> , T <sub>STG</sub> -55 to +150 ℃						
Operating and Storage Temperature Range   TJ, TSTG   -55 to +150   C     Notes:   4. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.						

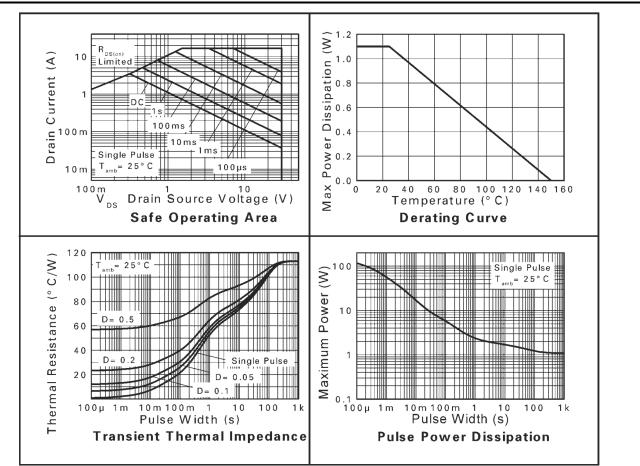
For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
For a device surface mounted on FR4 PCB measured at t≤5 secs.

6. Repetitive rating - pulse width limited by maximum junction temperature. Refer to Transient Thermal.

#### **NB High Voltage Applications**

For high voltage applications, the appropriate industry sector guidelines should be considered with regards to voltage spacing between conductors.

## Thermal Characteristics





# Electrical Characteristics (@ T<sub>A</sub> = +25°C, unless otherwise specified.)

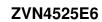
			_				
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS			r	1	1		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	250	285	-	V	$I_D = 1mA$ , $V_{GS} = 0V$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	35	500	νA	$V_{DS} = 250V, V_{GS} = 0V$	
Gate-Body Leakage	IGSS		±1	100	nA	$V_{GS}=\pm40V,V_{DS}=0V$	
ON CHARACTERISTICS							
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	0.8	1.4	1.8	V	$I_D = 1mA, V_{DS} = V_{GS}$	
			5.6	8.5	Ω	$V_{GS} = 10V, I_D = 500mA$	
Static Drain-Source On-State Resistance (Note 7)	R <sub>DS</sub> (ON)	-	5.9	9.0	Ω	$V_{GS} = 4.5V, I_D = 360mA$	
			6.4	9.5	Ω	$V_{GS} = 2.4V, I_D = 20mA$	
Forward Transconductance (Note 9)	<b>g</b> <sub>fs</sub>	0.3	0.475	-	S	$V_{DS} = 10V, I_D = 0.3A$	
Diode Forward Voltage (Note 7)	V <sub>SD</sub>			0.97	V	T <sub>J</sub> =+25 ℃ , I <sub>S</sub> = 360mA, V <sub>GS</sub> = 0V	
DYNAMIC CHARACTERISTICS (Notes 8 & 9)						-	
Input Capacitance	Ciss	-	72	-	pF		
Output Capacitance	Coss	-	11	-	pF	─V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0V ─f = 1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	-	3.6	-	pF		
Total Gate Charge	Qg	-	2.6	3.65	nC		
Gate-Source Charge	Q <sub>gs</sub>	-	0.2	0.28	nC	$V_{GS} = 10V, V_{DS} = 25V$	
Gate-Drain Charge	Q <sub>gd</sub>	-	0.5	0.7	nC	$I_{D} = 360 \text{mA}(\text{refer to test circuit})$	
Reverse Recovery Time (Note 9)	t <sub>rr</sub>	-	186	260	ns	T <sub>J</sub> =+25 ℃, IF=360A,	
Reverse Recovery Charge (Note 9)	Q <sub>rr</sub>	-	34	48	nC	di/dt= 100A/µs	
Turn-On Delay Time	t <sub>d(on)</sub>	-	1.25	-	ns		
Turn-On Rise Time		-	1.7	-	ns	$V_{DD} = 30V, V_{GS} = 10V$	
Turn-Off Delay Time	t <sub>d(off)</sub>	-	11.40	-	ns	$I_D = 360 \text{mA}, R_G = 50 \Omega$	
Turn-Off Fall Time	tf	-	3.5	-	ns	(refer to test circuit)	

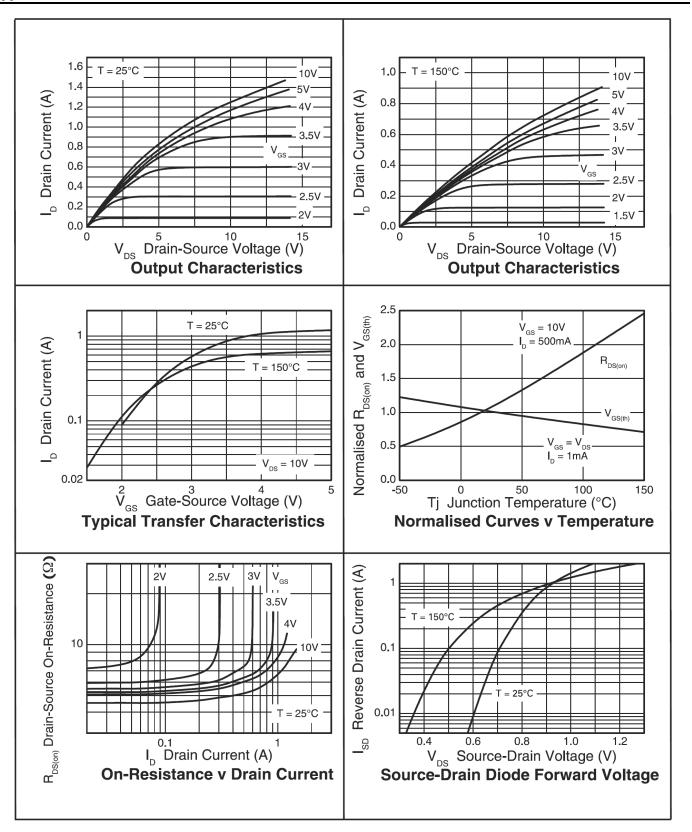
Notes:

7. Measured under pulsed conditions. Width=300µs. Duty cycle ≤ 2%.
8. Switching characteristics are independent of operating junction temperature.
9. For design aid only, not subject to production testing.



# **Typical Characteristics**

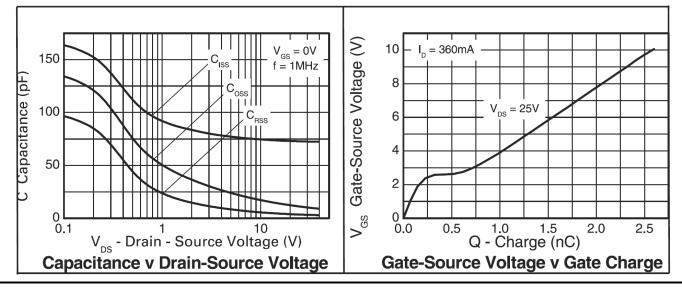




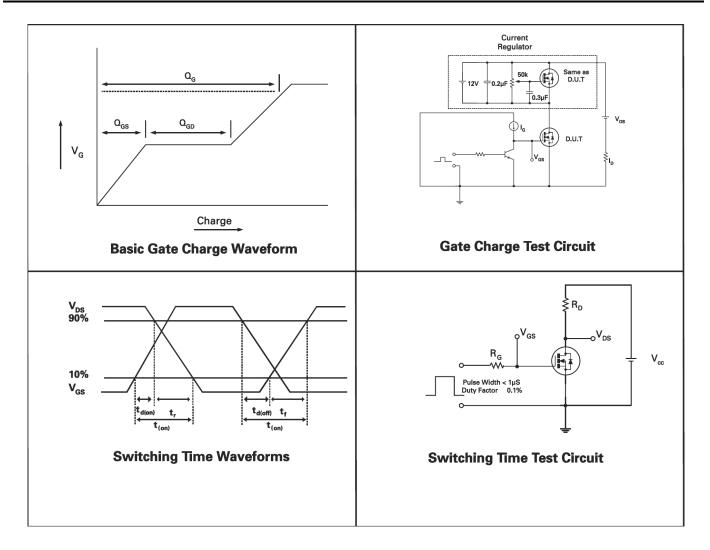


ZVN4525E6

## Typical Characteristics (continued)



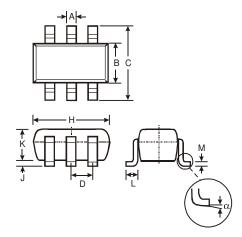
# **Test Circuits**





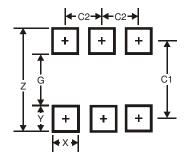
## **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT26						
Dim	Min	Max	Тур			
Α	0.35	0.50	0.38			
В	1.50	1.70	1.60			
С	2.70	3.00	2.80			
D			0.95			
Н	2.90	3.10	3.00			
J	0.013	0.10	0.05			
Κ	1.00	1.30	1.10			
L	0.35	0.55	0.40			
М	0.10	0.20	0.15			
α 0° 8° —						
aii d	imensi	ons in	mm			

Suggested Pad Layout Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95



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