



Features

Environments

Mechanical Data
 Case: TO252

and Robust End Application

Low R_{DS(ON)} – Minimises Power Losses

Low Q_a – Minimises Switching Losses

Datasheet (DMNH10H028SK3Q)

UL Flammability Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020 Terminal Connections: See Diagram

Solderable per MIL-STD-202, Method 208 @3

Weight: 0.33 grams (Approximate)

DMNH10H028SK3

100V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET

Rated to +175°C – Ideal for High Ambient Temperature

Lead-Free Finish; RoHS Compliant (Notes 1 & 2)

Halogen and Antimony Free. "Green" Device (Note 3)

Qualified to AEC-Q101 Standards for High Reliability

An Automotive-Compliant Part is Available Under Separate

Case Material: Molded Plastic, "Green" Molding Compound.

Terminals: Finish - Matte Tin Annealed over Copper Leadframe

100% Unclamped Inductive Switching - Ensures More Reliable

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _C = +25°C
100V	$28m\Omega @ V_{GS} = 10V$	55A

Description

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high-efficiency power management applications.

Applications

- Engine Management Systems
- Body Control Electronics
- DC-DC Converters

Pin Out Top View

G

Equivalent Circuit

Ordering Information (Note 4)

Case	Packaging
TO252	2,500/Tape & Reel
	TO252

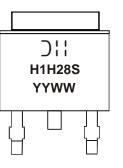
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

Notes:

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



) : = Manufacturer's Marking
H1H28S = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Digit of Year (ex: 15 = 2015)
WW = Week Code (01 to 53)

Top View

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^{2.} See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	100	V
Gate-Source Voltage	V _{GSS}	±20	V	
Continuous Drain Current, V _{GS} = 10V	T _C = +25°C T _C = +100°C	ID	55 39	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	58	A	
Maximum Continuous Body Diode Forward Current (Note 5)	ls	2.2	A	
Avalanche Current, L = 0.1mH		I _{AS}	29	A
Avalanche Energy, L = 0.1mH		E _{AS}	43	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	2.0	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	74	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{ extsf{ heta}JA}$	25	
Total Power Dissipation (Note 6)		PD	3.7	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	40	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{ extsf{ heta}JA}$	13	
Thermal Resistance, Junction to Case		$R_{\theta JC}$	1.2	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	eysei		. 76	mux	onit		
Drain-Source Breakdown Voltage	BV _{DSS}	100	—	—	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current, TJ = +25°C	IDSS	_	—	1	μA	V _{DS} = 100V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)						·	
Gate Threshold Voltage	V _{GS(TH)}	2.0	2.5	3.3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	20	28	mΩ	$V_{GS} = 10V, I_D = 20A$	
Diode Forward Voltage	V _{SD}		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1.0A$	
DYNAMIC CHARACTERISTICS (Note 8)			•	•		·	
Input Capacitance	C _{iss}	_	2245	_	pF		
Output Capacitance	Coss	_	173	—	pF	V _{DS} = 50V, V _{GS} = 0V, f = 1MHz	
Reverse Transfer Capacitance	C _{rss}	_	68	—	pF		
Gate Resistance	Rg	_	1.9	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg	_	36	—	nC		
Total Gate Charge (V _{GS} = 6V)	Qg	_	22	—	nC		
Gate-Source Charge	Q _{gs}	_	7.3	_	nC	$-V_{DS} = 50V, I_{D} = 20A$	
Gate-Drain Charge	Q _{gd}	_	9.2	_	nC	7	
Turn-On Delay Time	t _{D(ON)}	_	6.4	—	ns		
Turn-On Rise Time	t _R	_	5.8	—	ns	$V_{GS} = 10V, V_{DS} = 50V,$	
Turn-Off Delay Time	t _{D(OFF)}	—	17.8	—	ns	$R_G = 3\Omega$, $I_D = 20A$	
Turn-Off Fall Time	t _F	—	4.8	—	ns		
Body Diode Reverse Recovery Time	t _{RR}	_	35	—	ns	I _F = 20A, di/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Q _{RR}		47	_	nC	I _F = 20A, di/dt = 100A/µs	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

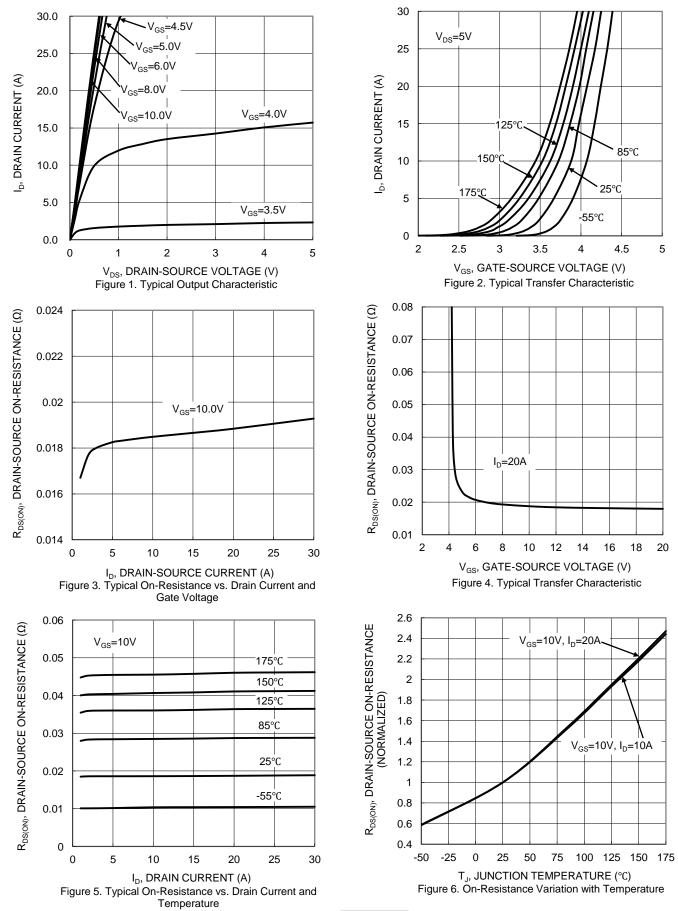
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.



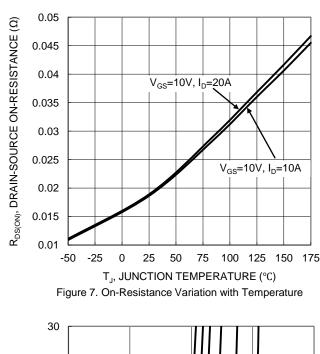
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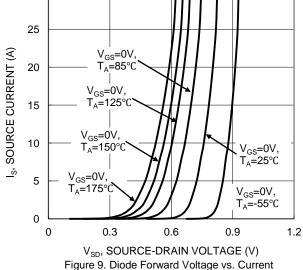


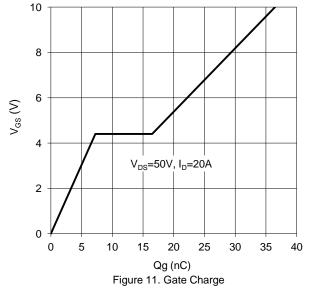
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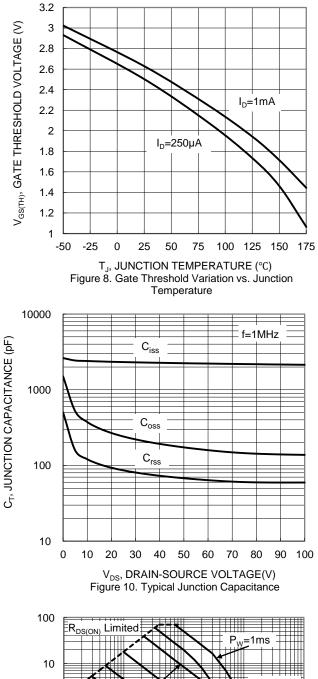


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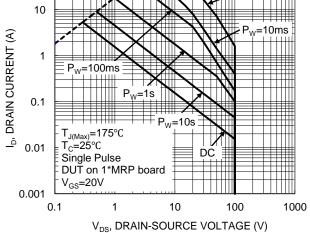


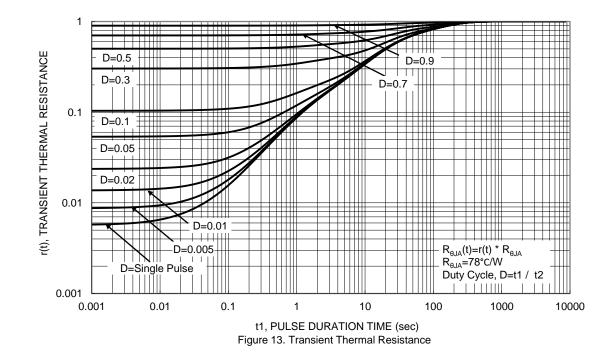
Figure 12. SOA, Safe Operation Area

DMM

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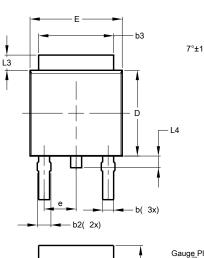


NEW PRODUCT

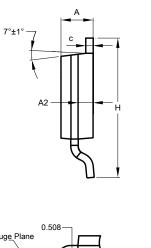


Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



D1



TO252 (DPAK)

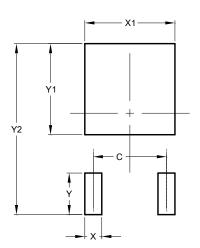
Gauge Plane	0.508	- Seating Plane

TO252 (DPAK)				
Dim	Min	Max	Тур	
Α	2.19	2.39	2.29	
A1	0.00	0.13	0.08	
A2	0.97	1.17	1.07	
b	0.64	0.88	0.783	
b2	0.76	1.14	0.95	
b3	5.21	5.46	5.33	
С	0.45	0.58	0.531	
D	6.00	6.20	6.10	
D1	5.21	-	-	
е	-	-	2.286	
Е	6.45	6.70	6.58	
E1	4.32	-	-	
Н	9.40	10.41	9.91	
L	1.40	1.78	1.59	
L3	0.88	1.27	1.08	
L4	0.64	1.02	0.83	
а	0°	10°	-	
All	Dimen	sions i	n mm	

Suggested Pad Layout

E1

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



TO252 (DPAK)

Dimensions	Value (in mm)
С	4.572
Х	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700



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