



#### SINGLE 2-INPUT POSITIVE AND GATE

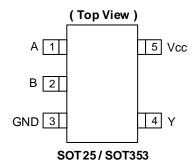
### **Description**

The 74AHC1G09Q is an automotive compliant single, two-input positive AND gate with an open drain output. The device is designed for operation with a power supply range of 2.0V to 5.5V. The gate performs the positive Boolean function:

$$Y = A \bullet B$$
 or  $Y = \overline{\overline{A} + \overline{B}}$ 

A pull-up resistor is required to achieve a high output state.

# Pin Assignments



#### **Features**

- Grade 1 Ambient Temperature Operation: -40°C to +125°C
- Supply Voltage Range from 2.0V to 5.5V
- 8mA Output Sink at V<sub>CC</sub> = 4.5V
- CMOS Low-Power Consumption
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time
- Inputs Not Limited by V<sub>CC</sub>
- Balanced Propagation Delays
- Balanced Drive Capability
- ESD Protection Tested per AEC-Q100
- Exceeds 2000-V Human Body Model (AEC-Q100-002)
- Exceeds 1000-V Charged Device Model (AEC-Q100-011)
- Latch-Up Exceeds 100mA (AEC-Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74AHC1G09Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

### **Applications**

- General Purpose Logic
- Wide Array of Products, such as:
  - Automotive Applications within Grade 1 Temperature Range
  - Industrial Computing/Controls/Automation
  - High Reliability Networking/Communications
  - Industrial/Agricultural Equipment

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

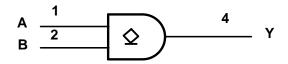
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### **Pin Descriptions**

Pin Name	Description
Α	Data Input
В	Data Input
GND	Ground
Y	Data Output
Vcc	Supply Voltage

### **Logic Diagram**



### **Function Table**

Inp	Output	
Α	В	Υ
Н	Н	Z
L	Х	L
Х	L	L

### **Absolute Maximum Ratings** (Notes 4 & 5)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to 6.5	V
lıĸ	Input Clamp Current (V <sub>I</sub> < 0)	-20	mA
lok	Output Clamp Current (Vo < 0)	-20	mA
lo	Continuous Output Current (Vo = 0 to Vcc)	+25	mA
Icc	Continuous Current Through Vcc	75	mA
IGND	Continuous Current Through GND	-75	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C
PD	Total Power Dissipation (Note 6)	250	mW

Notes:

<sup>4.</sup> Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

<sup>5.</sup> Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.

<sup>6.</sup> This will need to be derated at higher operating temperatures to prevent exceeding maximum T<sub>J</sub>. Refer to package thermal characteristics section.



# **Recommended Operating Conditions** (Note 7)

Symbol		Parameter	Min	Max	Unit
Vcc	Operating Voltage	_	2	5.5	V
		Vcc = 2V	1.5	_	
VIH	High-Level Input Voltage	Vcc = 3V	2.1	_	V
		Vcc = 5.5V	3.85	_	
		Vcc = 2V	_	0.5	
$V_{IL}$	V <sub>IL</sub> Low-Level Input Voltage	Vcc = 3V	_	0.9	V
		Vcc = 5.5V	_	1.65	
Vı	Input Voltage		0	5.5	V
Vo	Output Voltage		0	5.5	V
		Vcc = 2V	_	-50	μΑ
Іон	High-Level Output Current	$V_{CC} = 3.3V \pm 0.3V$	_	-4	
		$V_{CC} = 5V \pm 0.5V$	_	-8	mA
		Vcc = 2V	_	50	μA
loL	Low-Level Output Current	$V_{CC} = 3.3V \pm 0.3V$	_	4	
		$V_{CC} = 5V \pm 0.5V$	_	8	mA
	Input Transition Rise or Fall	$V_{CC} = 3.3V \pm 0.3V$	_	100	
Δt/ΔV	Rate	$V_{CC} = 5V \pm 0.5V$	_	20	ns/V
TA	Ambient Temperature	_	-40	+125	°C

Note:

### Electrical Characteristics (All typical values are at Vcc = 3.3V, TA = +25°C.)

			.,		+25°C		-40°C to	+85°C	-40°C to	+125°C	
Symbol	Parameter	Test Conditions	Vcc	Min	Тур	Max	Min	Max	Min	Max	Unit
		VI = V <sub>IL</sub> or VI <sub>H</sub>	2V	_	_	0.1	_	0.1	_	0.1	
			3V	_	_	0.1	_	0.1	-	0.1	
	Low Lovel Output	I <sub>OL</sub> = 50µA	4.5V	_	_	0.1	_	0.1	_	0.1	
Vol	Voltage Voltage	ow Level Output	3V	_	_	0.36	_	0.44	_	0.55	V
			4.5V	_	_	0.36	_	0.44	_	0.55	
II	Input Current	V <sub>I</sub> = 5.5V or GND	0 to 5.5V	_		±0.1	1	±1	1	±2	μΑ
Icc	Supply Current	V <sub>I</sub> = 5.5V or GND I <sub>O</sub> = 0	5.5V		ı	1	l	10	ı	40	μΑ
Icc	Supply Current	$V_1 = 5.5V$ or GND $I_0 = 0$	5.5V	_	-	1	-	10	1	40	μΑ
Cı	Input Capacitance	V <sub>I</sub> = V <sub>CC</sub> or GND	5.5V	_	2.0	10	_	10	_	10	pF

<sup>7.</sup> Unused inputs should be held at  $V_{\text{CC}}$  or Ground.



# **Package Characteristics**

Symbol	Parameter	Package	Test Conditions	Min	Тур	Max	Unit
0	Thermal Resistance	SOT25	Note 0		184	_	°C/W
θЈА	Junction-to-Ambient	SOT353	Note 8	_	385	_	
0.5	Thermal Resistance	SOT25	Nata 0	_	62	_	9000
θJC	Junction-to-Case	SOT353	Note 8	_	164	_	°C/W

Note: 8. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

# **Switching Characteristics**

 $Vcc = 3.3V \pm 0.3V$  (See Figure 1)

Parameter	From	То	Test		+25°C		-40°C to	o +85°C	-40°C to	+125°C	Unit		
(Input)	(Input)	(Output)	Conditions	Min	Тур	Max	Min	Max	Min	Max			
4	t <sub>PD</sub> A or B	A = = D	A or P	V	C <sub>L</sub> = 15pF	0.6	4.5	7.9	0.6	9.5	0.6	10.5	ns
IPD .		Y	C <sub>L</sub> = 50pF	0.6	6.5	11.4	0.6	13.0	0.6	14.5	ns		

 $Vcc = 5V \pm 0.5V$  (See Figure 1)

Parameter	From	То	Test		+25°C		-40°C to	o +85°C	-40°C to	+125°C	Unit	
(Input)	(Output)	Conditions	Min	Тур	Max	Min	Max	Min	Max			
4	A 5	ton A or B	V	C <sub>L</sub> = 15pF	0.6	3.5	5.5	0.6	6.5	0.6	7.0	ns
tpD A or B	Y Cı	C <sub>L</sub> = 50pF	0.6	4.9	7.5	0.6	8.5	0.6	9.5	ns		

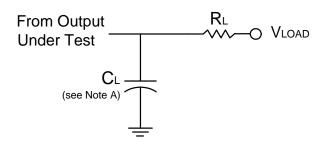
# **Operating Characteristics**

 $T_A = +25$ °C

Parameter		Test Conditions	Vcc = 5V Typ	Unit
C <sub>PD</sub>	Power Dissipation Capacitance	f = 1MHz No Load	10	pF



### **Measurement Information**



Test	Condition
t <sub>PLZ</sub> (See Notes D and F)	Vload
t <sub>PZL</sub> (See Notes D and E)	$V_{LOAD}$

· ·	Inputs			V	CL		
Vcc	Vı	t <sub>R</sub> /t <sub>F</sub>	Vм	/M VLOAD		R∟	<b>V</b> Δ
3.3V±0.3V	Vcc	≤3ns	V <sub>CC</sub> /2	Vcc	15pF	1kΩ	0.3V
3.3V±0.3V	Vcc	≤3ns	V <sub>CC</sub> /2	Vcc	50pF	1kΩ	0.3V
5V±0.5V	Vcc	≤3ns	V <sub>CC</sub> /2	Vcc	15pF	1kΩ	0.3V
5V±0.5V	Vcc	≤3ns	V <sub>CC</sub> /2	Vcc	50pF	1kΩ	0.3V

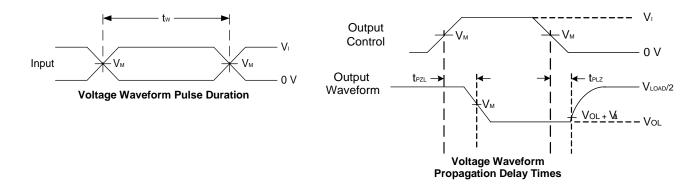


Figure 1. Load Circuit and Voltage Waveforms

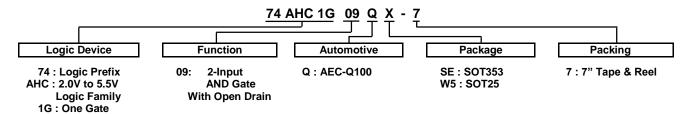
Notes:

- A. Includes test lead and test apparatus capacitance. B. All pulses are supplied at pulse repetition rate  $\leq$  1MHz.
- C. The inputs are measured one at a time with one transition per measurement.
- D. For the open drain device  $t_{PLZ}$  and  $t_{PZL}$  are the same as  $t_{PD}$ .
- E. t<sub>PZL</sub> is measured at V<sub>M</sub>.
- F.  $t_{PLZ}$  is measured at  $V_{OL}$  + $V_{\Delta}$ .

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### Ordering Information (Note 9)



Part Number	Package Package		Package Size	7" Tape and Reel			
r art Number	Code	(Notes 10 & 11)	i ackage Size	Quantity	Part Number Suffix		
74AHC1G09QSE-7	SE	SOT353	2.15mm × 2.1mm × 1.1mm 0.65mm lead pitch	3000/Tape & Reel	-7		
74AHC1G09QW5-7	W5	SOT25	3.0mm $ imes 2.8$ mm $ imes 1.2$ mm $0.95$ mm lead pitch	3000/Tape & Reel	-7		

9. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

10. Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.

11. The taping orientation is located on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.

### **Marking Information**

(Top View)

SOT 25 / SOT 353

XXX: Identification Code : Year 0~9

: Week: A~Z 1~26 week

a~z 27~52 week

z represents week 52 and 53

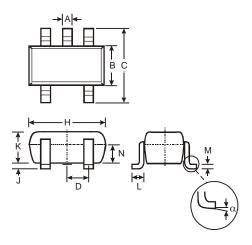
X : A~ Z: Internal Code

Part Number	Package	Identification Code
74AHC1G09QW5-7	SOT25	YNQ
74AHC1G09QSE-7	SOT353	YNQ



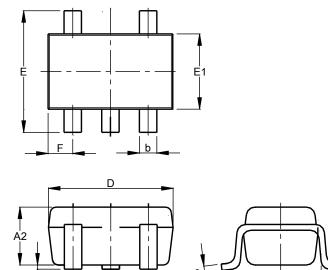
### **Package Outline Dimensions**

### (1) Package Type: SOT25



SOT25				
Dim	Min	Max	Тур	
Α	0.35	0.50	0.38	
В	1.50	1.70	1.60	
С	2.70	3.00	2.80	
D	-	-	0.95	
H	2.90	3.10	3.00	
J	0.013	0.10	0.05	
K	1.00	1.30	1.10	
L	0.35	0.55	0.40	
M	0.10	0.20	0.15	
N	0.70	0.80	0.75	
α	0°	8°	-	
All Dimensions in mm				

### (2) Package Type: SOT353



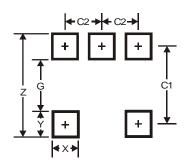
SOT353					
Dim	Min	Max	Тур		
A1	0.00	0.10	0.05		
A2	0.90	1.00	0.95		
b	0.10	0.30	0.25		
С	0.10	0.22	0.11		
D	1.80	2.20	2.15		
Е	2.00	2.20	2.10		
E1	1.15	1.35	1.30		
е	0.650 BSC				
F	0.40	0.45	0.425		
L	0.25	0.40	0.30		
а	0°	8°			
All Dimensions in mm					



### **Suggested Pad Layout**

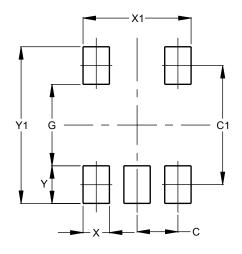
Please see http://www.diodes.com/package-outlines.html for the latest version.

#### (1) Package Type: SOT25



Dimensions	Value
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95

#### (2) Package Type: SOT353



Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Y	0.600
Y1	2.500

### **Mechanical Data**

#### SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208@3
- Weight: 0.0158 grams (Approximate)

#### **SOT353**

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.0064 grams (Approximate)



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