

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> T <sub>c</sub> = +25°C
80V	7.8mΩ @ V <sub>GS</sub> = 10V	91A
	11mΩ @ V <sub>GS</sub> = 4.5V	77A

## Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- DC-DC Converters
- Load Switch

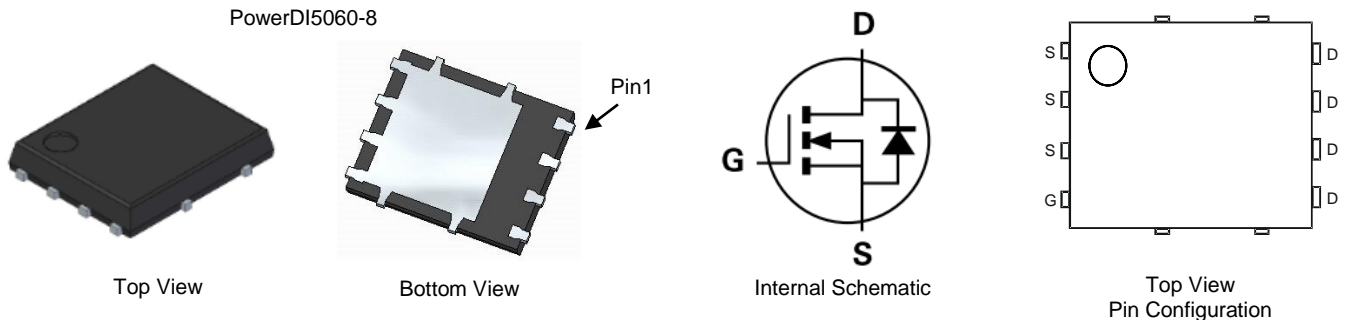
## Features

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R<sub>DS(ON)</sub> – Minimizes On-State Losses
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DMTH8008LPSQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

## Mechanical Data

- Case: PowerDI<sup>®</sup>5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 **Ⓔ3**
- Weight: 0.097 grams (Approximate)

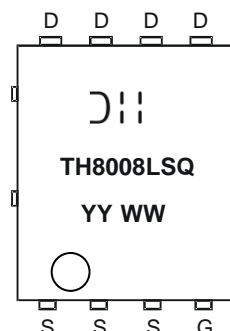


## Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH8008LPSQ-13	PowerDI5060-8	2,500/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



DII = Manufacturer's Marking  
 TH8008LSQ = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY = Year (ex: 19 = 2019)  
 WW = Week (01 to 53)

**Maximum Ratings** (@T<sub>C</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	80	V	
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 7)	Steady State	T <sub>C</sub> = +25°C	91	A
		T <sub>C</sub> = +100°C	64	
Maximum Continuous Body Diode Forward Current (Note 7)	I <sub>S</sub>	69	A	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	360	A	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)	I <sub>SM</sub>	360	A	
Avalanche Current, L = 0.1mH (Note 8)	I <sub>AS</sub>	23	A	
Avalanche Energy, L = 0.1mH (Note 8)	E <sub>AS</sub>	26.5	mJ	

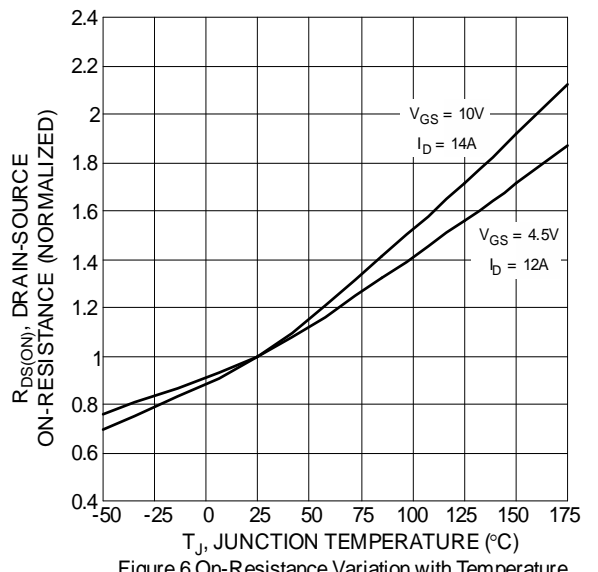
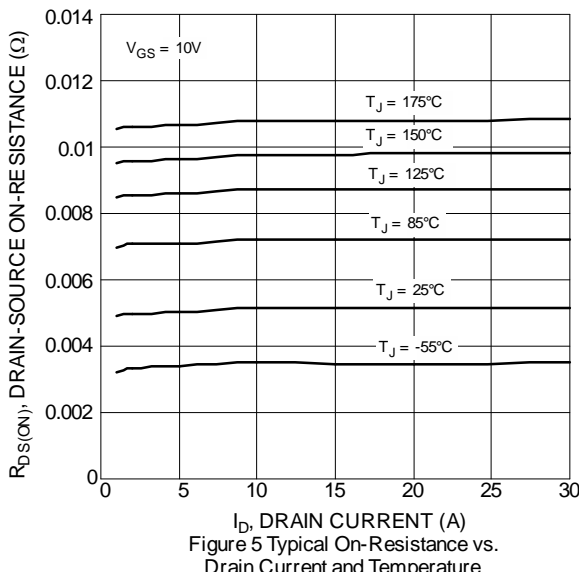
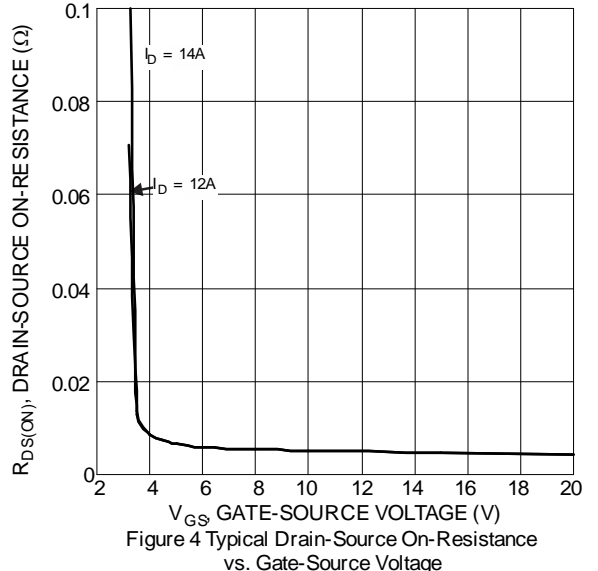
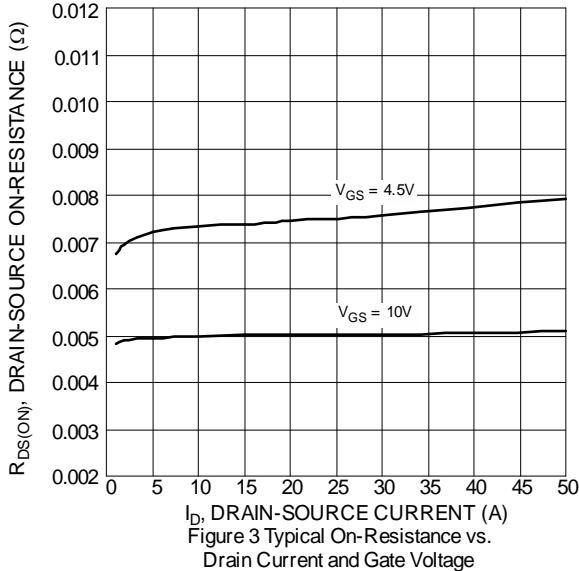
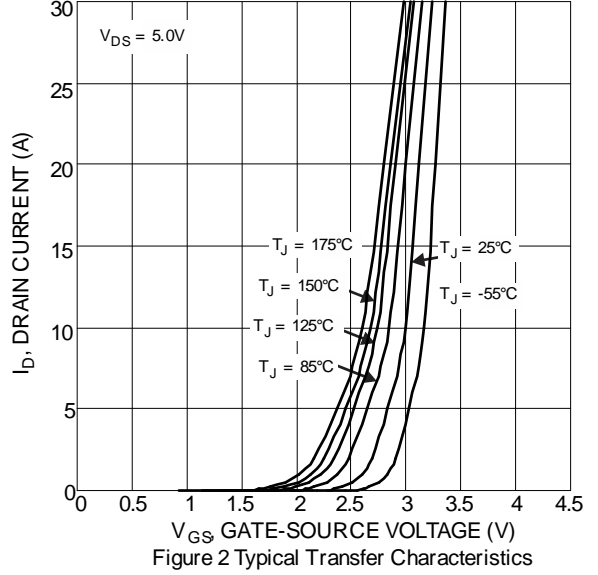
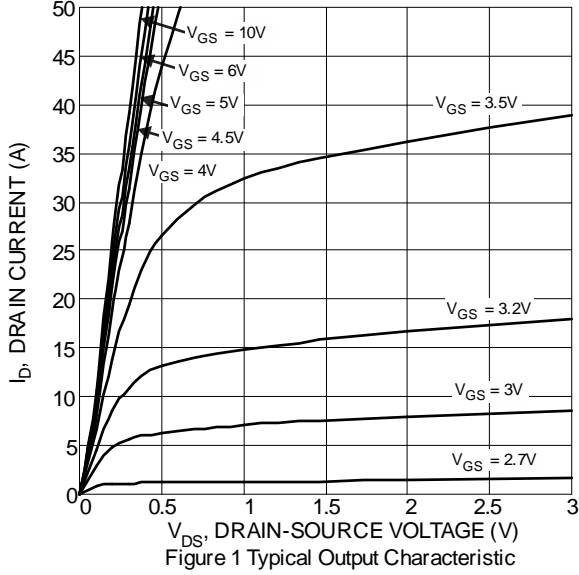
**Thermal Characteristics** (@T<sub>C</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	1.6	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	93	°C/W
Total Power Dissipation (Note 6)	P <sub>D</sub>	3.4	W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	44	°C/W
Total Power Dissipation (Note 7)	P <sub>D</sub>	100	W
Thermal Resistance, Junction to Case (Note 7)	R <sub>θJC</sub>	1.5	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C

**Electrical Characteristics** (@T<sub>C</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 9)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	80	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	µA	V <sub>DS</sub> = 64V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 9)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.3	—	2.8	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 1mA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	5	7.8	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 14A
		—	8	11		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 12A
Diode Forward Voltage	V <sub>SD</sub>	—	0.8	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 14A
<b>DYNAMIC CHARACTERISTICS (Note 10)</b>						
Input Capacitance	C <sub>ISS</sub>	—	2345	—	pF	V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V, f = 1MHz
Output Capacitance	C <sub>OSS</sub>	—	842	—		
Reverse Transfer Capacitance	C <sub>RSS</sub>	—	51.9	—		
Gate Resistance	R <sub>G</sub>	—	1.7	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>G</sub>	—	21.7	—	nC	V <sub>DD</sub> = 40V, I <sub>D</sub> = 2A
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>G</sub>	—	41.2	—		
Gate-Source Charge	Q <sub>GS</sub>	—	5.0	—		
Gate-Drain Charge	Q <sub>GD</sub>	—	10.6	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	5.8	—	ns	V <sub>DD</sub> = 40V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 2A, R <sub>G</sub> = 1.6Ω
Turn-On Rise Time	t <sub>R</sub>	—	5.4	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	24.5	—		
Turn-Off Fall Time	t <sub>F</sub>	—	43.2	—		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	—	61	—	ns	I <sub>F</sub> = 2A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	—	181	—	nC	

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
  - Thermal resistance from junction to soldering point (on the exposed drain pad).
  - I<sub>AS</sub> and E<sub>AS</sub> ratings are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.



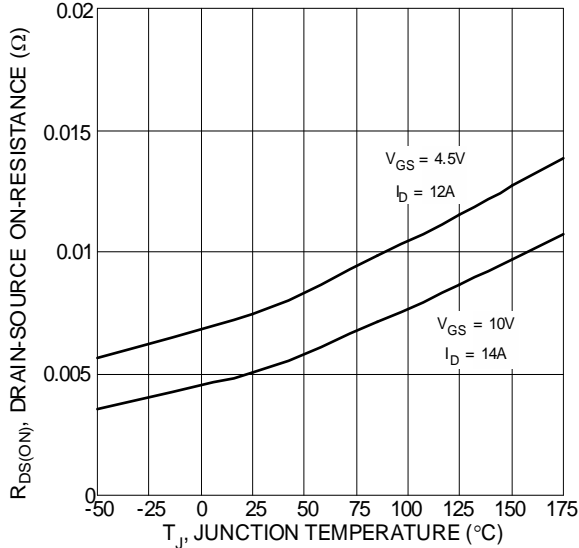


Figure 7 On-Resistance Variation with Temperature

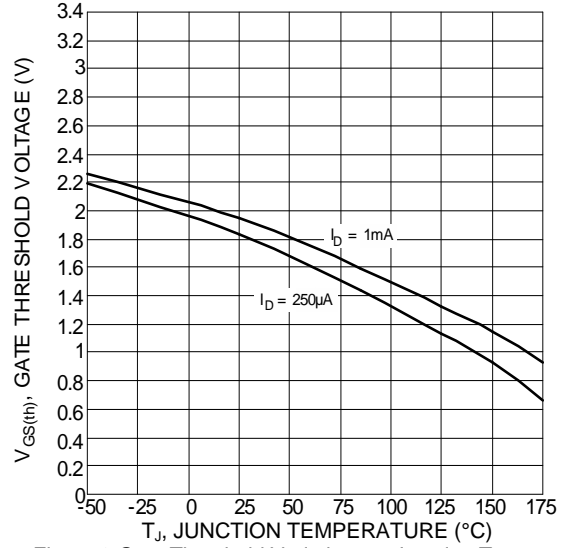


Figure 8 Gate Threshold Variation vs. Junction Temperature

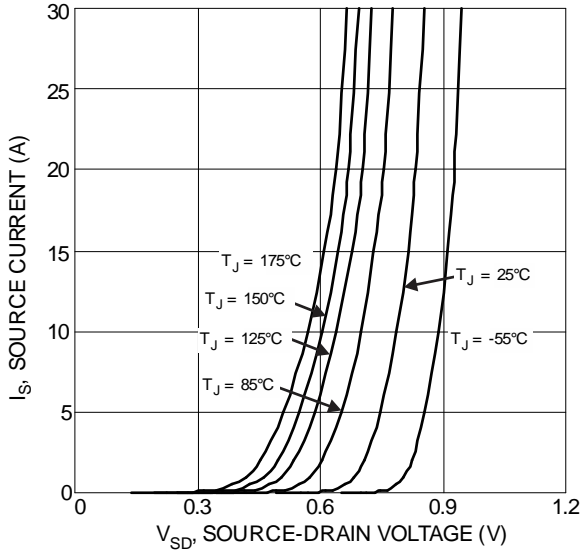


Figure 9 Diode Forward Voltage vs. Current

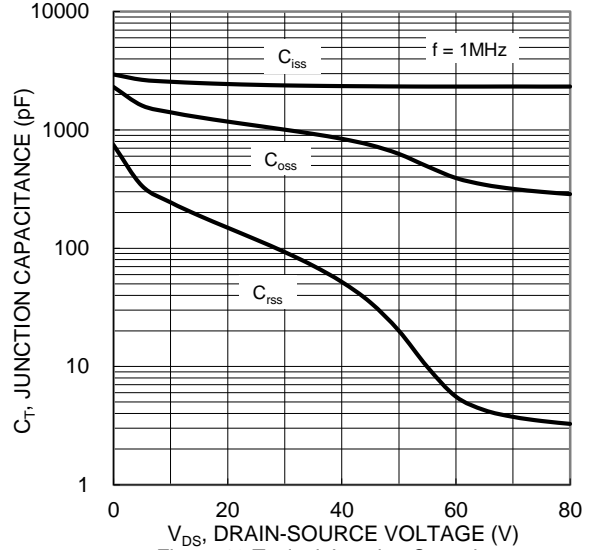


Figure 10 Typical Junction Capacitance

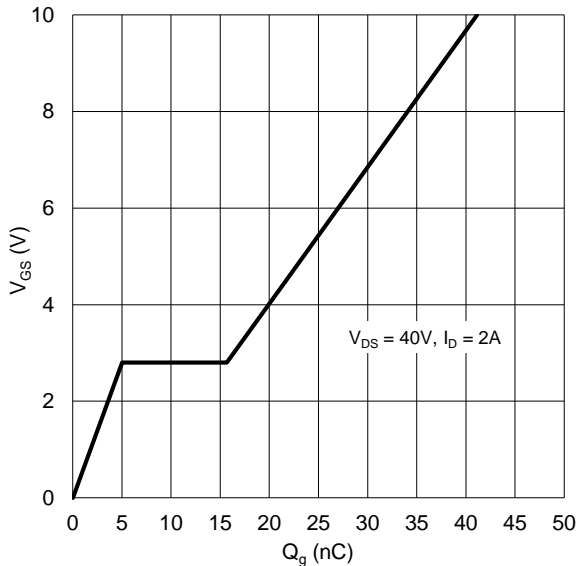


Figure 11 Gate Charge

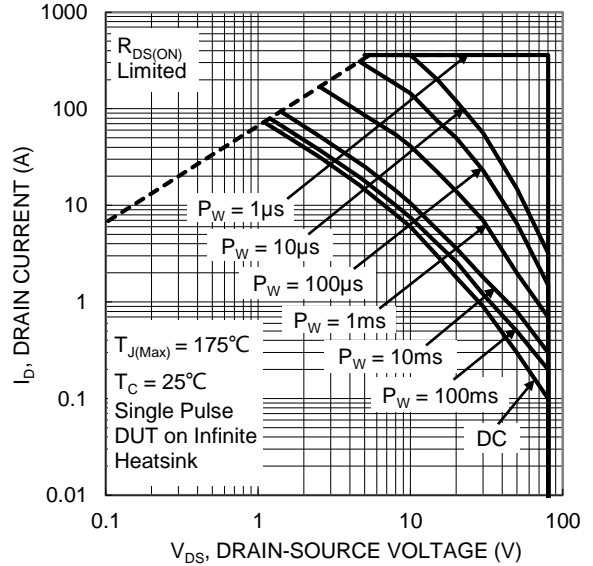
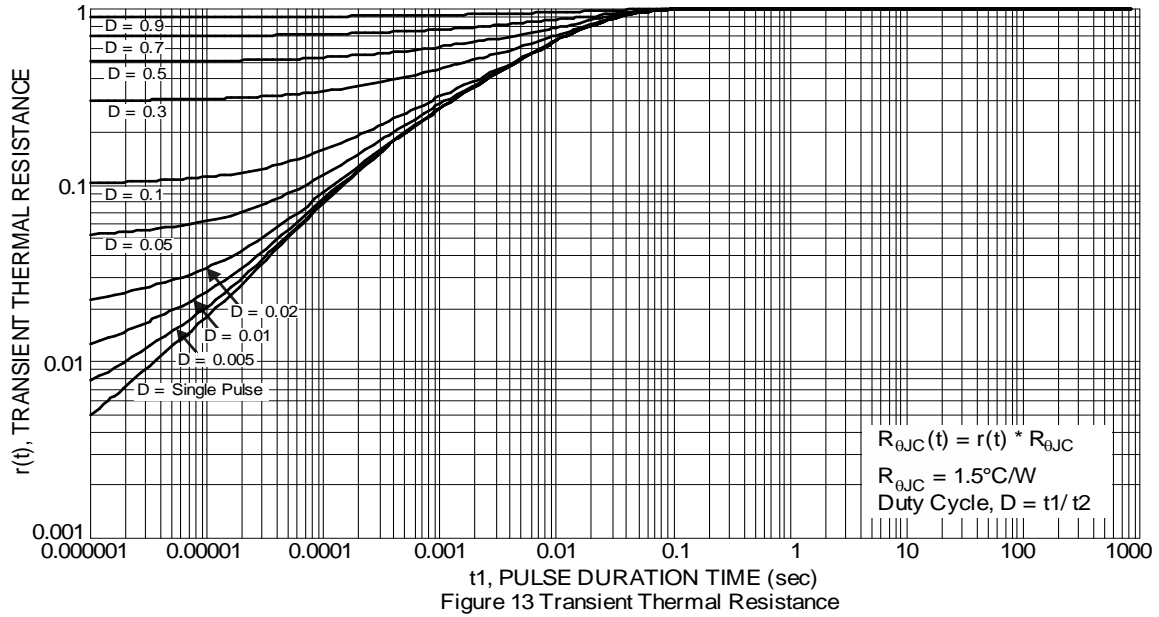


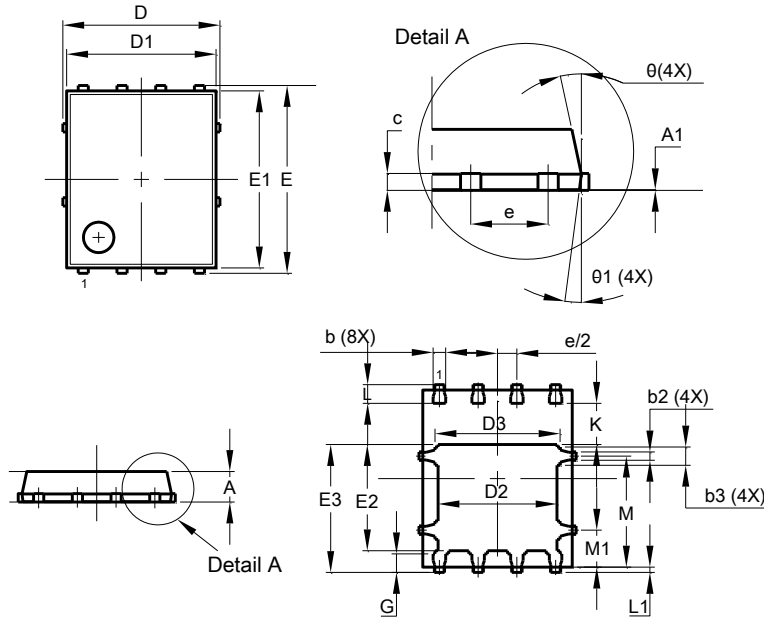
Figure 12. SOA, Safe Operation Area



**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI5060-8**

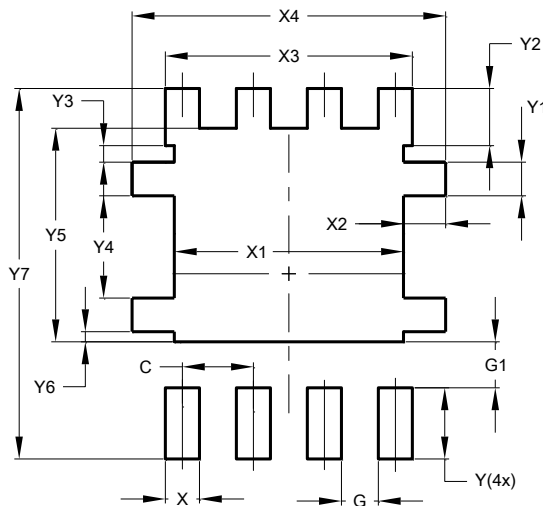


PowerDI5060-8			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0.00	0.05	-
b	0.33	0.51	0.41
b2	0.200	0.350	0.273
b3	0.40	0.80	0.60
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.70	4.10	3.90
D3	3.90	4.30	4.10
E	6.15 BSC		
E1	5.60	6.00	5.80
E2	3.28	3.68	3.48
E3	3.99	4.39	4.19
e	1.27 BSC		
G	0.51	0.71	0.61
K	0.51	-	-
L	0.51	0.71	0.61
L1	0.100	0.200	0.175
M	3.235	4.035	3.635
M1	1.00	1.40	1.21
$\Theta$	10°	12°	11°
$\Theta1$	6°	8°	7°
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI5060-8**



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

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