



**FCX605** 

#### 120V NPN HIGH VOLTAGE DARLINGTON TRANSISTOR

#### **Features**

- BV<sub>CEO</sub> > 120V
- Low Saturation Voltage < 1.5V @ 1A</li>
- Darlington Transistor h<sub>FE</sub> > 2k @ 1A
- I<sub>C</sub> = 1A High Continuous Collector Current
- Specification is Also Available in Eline and SOT223 Package Outlines
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

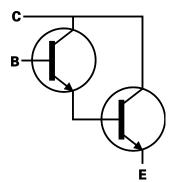
- Case: SOT89
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.052 grams (Approximate)

#### **Applications**

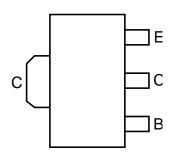
- · Various Driving Functions
  - Lamps
  - Motors
  - Relays and Solenoids
- High Output Current Switches







Device Symbol



Top View Pin-Out

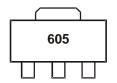
## **Ordering Information** (Note 4)

Ī	Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
	FCX605TA	AEC-Q101	605	7	12	1,000

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

#### **Marking Information**



605 = Product Type Marking Code



## Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	140	V
Collector-Emitter Voltage	V <sub>CEO</sub>	120	V
Emitter-Base Voltage	V <sub>EBO</sub>	10	V
Continuous Collector Current	Ic	1	Α
Peak Pulse Current	I <sub>CM</sub>	4	Α

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Power Dissipation	(Note 5)	ь	0.9		
Power Dissipation	(Note 6)	$P_{D}$	1	W	
Thermal Desigtance Junction to Ambient	(Note 5)		139	0000	
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\theta JA}$	125	°C/W	
Thermal Resistance, Junction to Leads (Note 7)		$R_{ heta JL}$	5.2	°C/W	
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-55 to +150	°C		

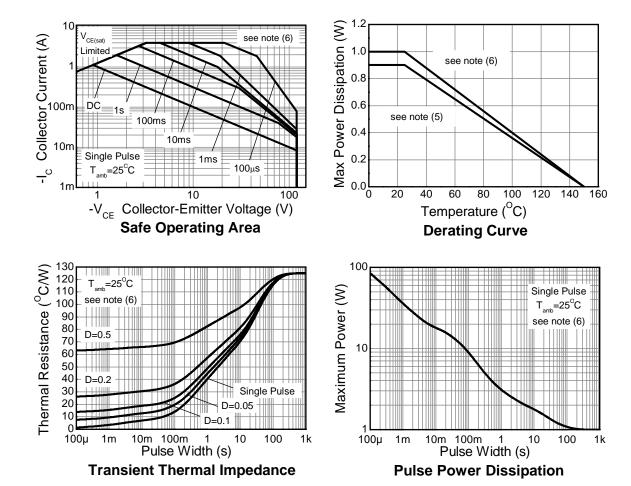
## ESD Ratings (Note 8)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	2,000	V	2
Electrostatic Discharge - Machine Model	ESD MM	200	V	В

5. For a device mounted with the exposed collector pad on 15mm x 15mm 1oz copper that is on a single-sided 1.6mm FR-4 PCB; device is measured under still air conditions whilst operating in a steady-state.
6. Same as Note 5, except the device is mounted on 25mm x 25mm 1oz copper.
7. Thermal resistance from junction to solder-point (at the end of the leads).
8. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



## **Thermal Characteristics and Derating Information**





## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	140	_	_	V	$I_C = 100\mu A$
Collector-Emitter Breakdown Voltage (Note 9)	BV <sub>CEO</sub>	120	_	_	V	I <sub>CEO</sub> = 10mA
Emitter-Base Breakdown Voltage	$BV_{EBO}$	10	_	_	V	$I_{EBO} = 100\mu A$
Collector Cut-off Current	I <sub>CBO</sub>		_	100 10	nΑ μΑ	V <sub>CB</sub> = 10V V <sub>CB</sub> = 120V, T <sub>A</sub> = +100°C
Emitter-base Cut-off Current	I <sub>EBO</sub>	_	_	0.1	μΑ	V <sub>EB</sub> = 8V
Collector Emitter Cut-Off Current	ICES	_	_	10	μΑ	V <sub>CES</sub> = 120V
ON CHARACTERISTICS (Note 9)						
Static Forward Current Transfer Ratio	h <sub>FE</sub>	2k 5k 2k 0.5	_ _ _ _	— 100k —	_	$\begin{split} & I_{C} = 50 \text{mA}, \ V_{CE} = 5 \text{V} \\ & I_{C} = 500 \text{mA}, \ V_{CE} = 5 \text{V} \\ & I_{C} = 1 \text{A}, \ V_{CE} = 5 \text{V} \\ & I_{C} = 2 \text{A}, \ V_{CE} = 5 \text{V} \end{split}$
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	_	1 1.5	V	$I_C = 250 \text{mA}, I_B = 0.25 \text{mA}$ $I_C = 1 \text{A}, I_B = 1 \text{mA}$
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	_	_	1.8	V	$I_C = 1A$ , $I_B = 1mA$
Base-Emitter Turn-On Voltage	V <sub>BE(ON)</sub>	_	_	1.7	V	$I_{C} = 1A, V_{CE} = 5V$
SMALL SIGNAL CHARACTERISTICS (Note 9)						
Transition Frequency	f⊤	150	_	_	MHz	$I_C = 100 \text{mA}, V_{CE} = 10 \text{V}$ f = 20MHz
Input Capacitance	$C_ibo$	_	90	_	pF	$V_{CB} = 500 \text{mV}, f = 1 \text{MHz}$
Output Capacitance	$C_{obo}$	_	15	_	pF	$V_{CB} = 10V, f = 1MHz$
Turn-On Time	ton	_	0.5	_	μs	$I_C = 500$ mA, $V_{CE} = 10$ V $I_{B1} = -I_{B2} = 0.5$ mA
Turn-Off Time	toff	_	1.6	_	μs	$I_C = 500$ mA, $V_{CE} = 10$ V $I_{B1} = -I_{B2} = 0.5$ mA

Note: 9. Me

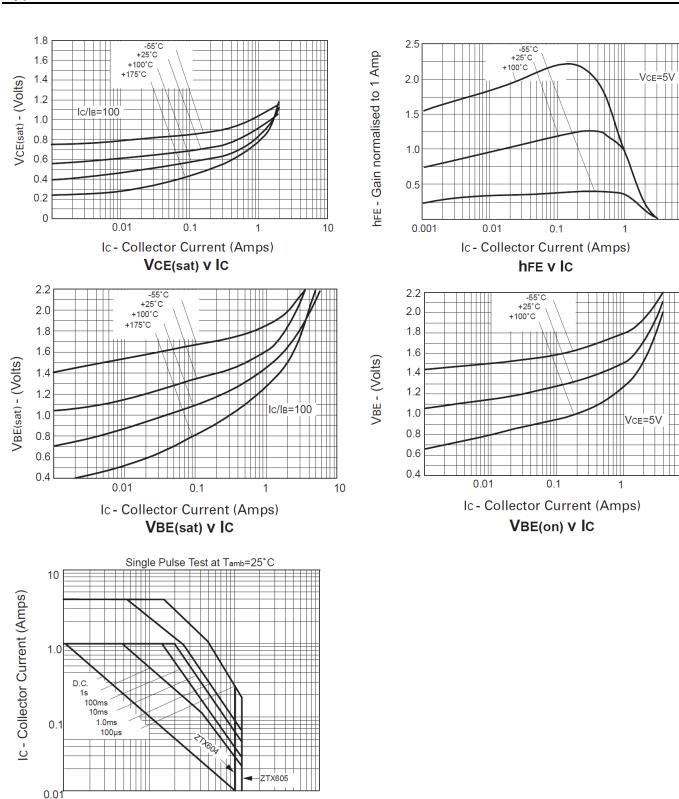
9. Measured under pulsed conditions. Pulse width ≤ 300µs. Duty cycle ≤ 2%.

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## Typical Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)



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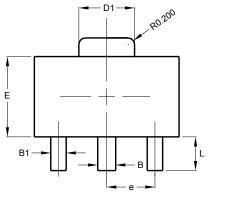
Vce - Collector Voltage (Volts)

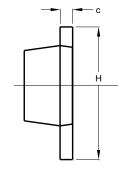
Safe Operating Area

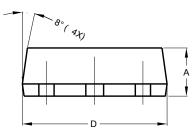


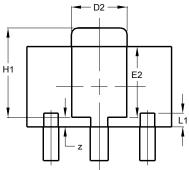
## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.





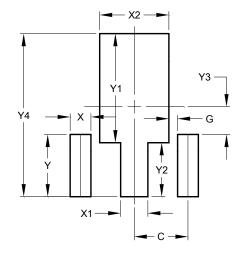




SOT89					
Dim	Min	Max	Тур		
Α	1.40	1.60	1.50		
В	0.50	0.62	0.56		
B1	0.42	0.54	0.48		
С	0.35	0.43	0.38		
D	4.40	4.60	4.50		
D1	1.62	1.83	1.733		
D2	1.61	1.81	1.71		
Е	2.40	2.60	2.50		
E2	2.05	2.35	2.20		
е	-	-	1.50		
Н	3.95	4.25	4.10		
H1	2.63	2.93	2.78		
L	0.90	1.20	1.05		
L1	0.327	0.527	0.427		
Z	0.20	0.40	0.30		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.500
G	0.244
Х	0.580
X1	0.760
X2	1.933
Υ	1.730
Y1	3.030
Y2	1.500
Y3	0.770
Y4	4.530

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.



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