



1 CHANNEL UNIDIRECTIONAL TVS

Product Summary

VBR (Min)	IPP (Max)	Ст (Тур)
6.5V	50A	255pF

Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD and Surge. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Applications

- Cellular Handsets
- Portable Electronics
- · Computers and Peripheral

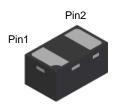
Features

- Low Profile Package (0.50mm Typical) and Ultra-Small PCB Footprint Area (1.1mm x 0.7mm Max) Suitable for Compact Portable Electronics
- One Channel of ESD and Surge Protection
- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- Provides Surge and Lightning Protection per IEC 61000-4-5 Standard: IPP Max 50A
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: X1-DFN1006-2
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.001 grams (Approximate)

X1-DFN1006-2



Bottom View



Device Schematic

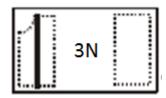
Ordering Information (Note 4)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity
D6V3S1U2LP-7B	Standard	3N	7	8	10.000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



3N = Product Type Marking Code Bar Denotes Pin 1 or Cathode Side

D6V3S1U2LP
Document number: DS42431 Rev. 2 - 2





Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	P _{PP}	575	W	8/20µs, per Figure 3
Peak Pulse Current	IPP	50	Α	8/20µs, per Figure 3
ESD Protection – Contact Discharge	Vesd_contact	±30	kV	IEC 61000-4-2 Standard
ESD Protection – Air Discharge	V _{ESD_AIR}	±30	kV	IEC 61000-4-2 Standard

Thermal Characteristics

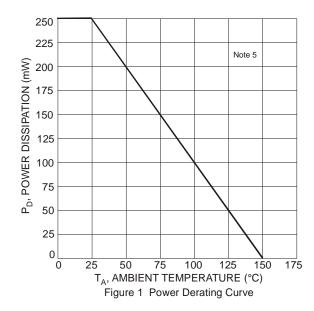
Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	PD	250	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	500	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

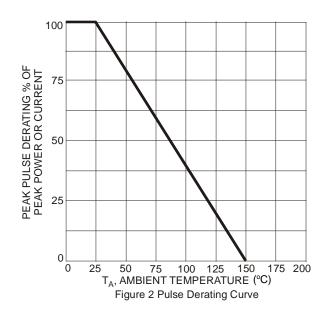
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	V_{RWM}	_	_	6.3	V	_
Reverse Current (Note 6)	I _R	_	_	1.0	μА	$V_R = V_{RWM} = 6.3V$
Reverse Breakdown Voltage	V _{BR}	6.5	_	9	V	I _R = 1mA
Reverse Clamping Voltage (Note 7)	VcL	_	8.0	_	V	$I_{PP} = 10A$, $t_P = 8/20 \mu s$
		_	11.5	_		$I_{PP} = 50A$, $t_P = 8/20\mu s$
ESD Clamping Voltage (Note 8)	\/-	_	7.3	_	V	IPP = 4A, tP = 10/100ns
	Vc	_	8.0	_		IPP = 16A, tp = 10/100ns
Capacitance	Ст	_	255	_	pF	V _R = 0V, f = 1MHz

Notes:

- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Clamping voltage value is based on an 8x20µs peak pulse current (IPP) waveform.
- 8. Transmission Line Pulse Test (TLP) settings: $t_P = 100$ ns, $t_R = 10$ ns, t_{TLP} and V_{TLP} averaging window is from 70ns to 90ns.







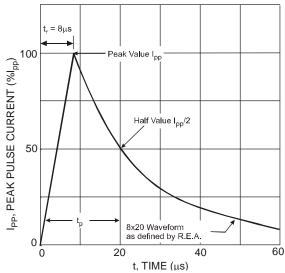
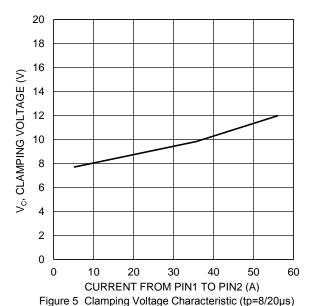


Figure 3 Typical 8 x 20µs Pulse Waveform



300
250
250
250
250
200
150
0
0
1 2 3 4 5 6 7
V_R, REVERSE VOLTAGE (V)
Figure 4 Typical Pin1 to Pin2 Capacitance

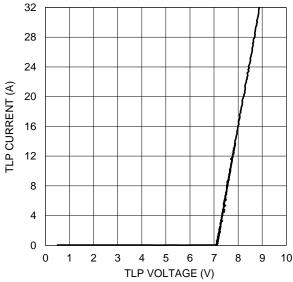


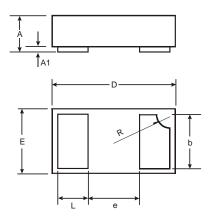
Figure 6 Pin1 to Pin2 TLP Curve (tp=100ns)



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-2

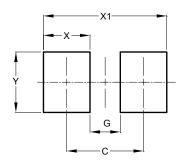


X1-DFN1006-2				
Dim	Min	Max	Тур	
Α	0.47	0.53	0.50	
A1	0	0.05	0.03	
b	0.45	0.55	0.50	
D	0.95	1.075	1.00	
Е	0.55	0.675	0.60	
е	-	-	0.40	
L	0.20	0.30	0.25	
R	0.05	0.15	0.10	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-2



Dimensions	Value		
Difficitions	(in mm)		
С	0.70		
G	0.30		
Х	0.40		
X1	1.10		
Y	0.70		



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