PERICOM<sup>®</sup>

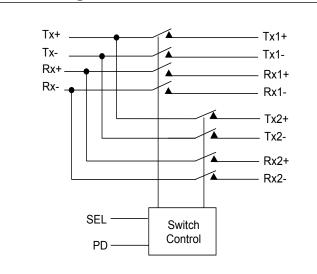
# PI3USB302-A

## 3.3V, USB 3.0, 1-Port, 2:1 Mux/DeMux Switch

#### **Features**

- → 2 Differential Channel, 2:1 Mux/DeMux
- → USB 3.0 SuperSpeed switch
- ➔ Bi-directional operation
- → Low Bit-to-Bit Skew, 10 ps max
- → 3dB Bandwidth = 8.1GHz
- → Low channel-to-channel skew: 20 ps max
- → Low insertion loss: -1dB @ 2.5 GHz (5.0 Gbps)
- → Low Crosstalk: -33dB @ 2.5 GHz (5.0 Gbps)
- → Low Off Isolation: -27dB @ 2.5 GHz (5.0 Gbps)
- → Low Return Loss: -23.3dB @ 2.5GHz (5.0Gbps)
- → V<sub>DD</sub> Operating Range: 3.3 V +/-10%
- → ESD Tolerance: 2 kV HBM
- → Low current: 0.2mA Typ.
- → Packaging (Pb-free & Green):
  - □ 20-contact TQFN (2.5 × 4.5mm)

#### **Block Diagram**



#### **Truth Table**

Function	SEL	PD
port 1 is active	L	L
port 2 is active	Н	L
Both Ports Hi-z, IC power down	x	Н

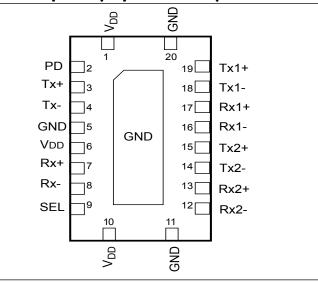
#### Description

Pericom Semiconductor's PI3USB302-A is a 2-differential channel bi-directional multiplexer/demultiplexer switch. Due to its low bit-to-bit skew, high channel-to-channel noise isolation and bandwidth, this product is ideal for USB 3.0 signal switching at 5.0 Gbps.

#### Application

Routing USB 3.0 SuperSpeed signals

# Pin Description (Top-side view)



15-0076

# **Pin Description**

Pin #	Pin Name	I/O	Description			
2	PD	Ι	Power Down input. see truth table on page 1 for functionality			
3	Tx+	I/O	Signal I/O Transmit differential nair from common next			
4	Tx-	I/O	Signal I/O, Transmit differential pair from common port			
7	Rx+	1/0	Signal I/O Dessive differential nain from common part			
8	Rx-	I/O	Signal I/O, Receive differential pair from common port			
9	SEL	Ι	Operation mode Select (when SEL=0: port 1 is active, when SEL=1: port 2 is active			
14	Tx2-	I/O	Signal I/O Transmit differential pair part 2			
15	Tx2+	1/0	Signal I/O, Transmit differential pair, port 2			
12	Rx2-	I/O	Signal I/O Bassiva differential pair part 2			
13	Rx2+	I/O	Signal I/O, Receive differential pair, port 2			
17	Rx1+	I/O	Signal I/O, Receive differential pair, port 1			
16	Rx1-	1/0	Signal 1/0, Receive differential pail, port 1			
19	Tx1+	I/O	Signal I/O, Transmit differential pair, port 1			
18	Tx1-	1/0	Signal 1/O, Transmit unierential pail, port 1			
6, 10, 1	V <sub>DD</sub>	Pwr	3.3V ±10% Positive Supply Voltage			
5, 11, 20, center pad	GND	Pwr	Power ground			

### **Maximum Ratings**

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	65°C to +150°C
Supply Voltage to Ground Potential	0.5V to +4.6V
Channel DC Input Voltage	–0.5V to 1.5V
SEL/PD DC Input Voltage	–0.5V to 4.6V
DC Output Current	
Power Dissipation	0.5W
•	

**Note:** Stresses greater than those listed under MAXI-MUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## **Electrical Characteristics**

**Recommended Operating Conditions** 

Symbol	Parameter	Conditions	Min	Тур	Max	Units
V <sub>DD</sub>	3.3V Power Supply		3.0	3.3	3.6	V
I <sub>DD</sub>	Total current from V <sub>DD</sub> 3.3V supply	SEL = 0V or $V_{DD}$ ,		0.2	1	mA
I <sub>DD_PD</sub>	Power down current	PD = 1		20	40	uA
T <sub>CASE</sub>	Case temperature range for operation within spec.		-40		85	Celsius

### DC Electrical Characteristics for Switching over Operating Range

Parameters	Description	Test Conditions <sup>(1)</sup>	Min	Typ <sup>(1)</sup>	Max	Units
VIH-SEL, PD	Input HIGH Voltage, SEL, PD Input		2		3.6	
VIL-SEL, PD	Input LOW Voltage, SEL, PD Input		0		0.8	V
V <sub>IK</sub>	Clamp Diode Voltage	$V_{DD}$ = Max, $I_{IN}$ = -18mA		-0.7	-1.2	
IIH	Input HIGH Current SEL, PD Input	$V_{DD}$ = Max, $V_{IN}$ = $V_{DD}$	-5		+5	
I <sub>IL</sub>	Input LOW Current, SEL, PD Input	$V_{DD} = Max, V_{IN} = 0V$	-5		+5	
I <sub>IH</sub>	Input High Current, T <sub>X</sub> , R <sub>X</sub>	$V_{DD}$ = Max, $V_{IN}$ = 1.5V	-10		+10	- μΑ
I <sub>IL</sub>	Input LOW Current, T <sub>X</sub> , R <sub>X</sub>	$V_{DD} = Max, V_{IN} = 0V$	-10		+10	
I <sub>OZH</sub>	High Z HIGH Current, T <sub>X</sub> , R <sub>X</sub>	$V_{DD} = Max, V_{IN} = 1.5V$	-10		+10	μA
I <sub>OZL</sub>	High Z LOW Current, T <sub>X</sub> , R <sub>X</sub>	$V_{DD} = Max, V_{IN} = 0V$	-10		+10	μΑ

Note:

1. Typical values are at  $\rm V_{DD}$  = 3.3V,  $\rm T_A$  = 25°C ambient and maximum loading.

#### **Switching Characteristics**

Parameters	Description	Test Conditions	Min.	Тур.	Max.	Units
t <sub>PZH</sub> , t <sub>PZL</sub>	Line Enable Time			25	30	
tpHZ, tPLZ	Line Disable Time			5	25 ns	
t <sub>b-b</sub>	Bit-to-bit skew within the same differential pair			5	10	
t <sub>ch-ch</sub>	Channel-to-channel skew				20 ps	

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### **Dynamic Electrical Characteristics**

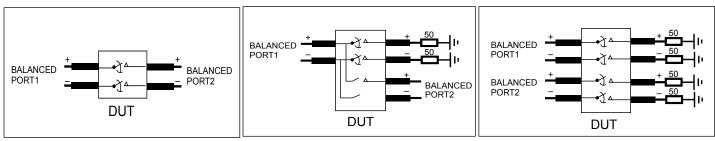
Parameter	Description	Test Conditions	Min.	Typ. <sup>(1)</sup>	Max.	Units
		f= 100MHz		-0.4		
DD11 2 3	Differential Insertion Loss	f= 1.25GHz		-0.6		15
DDIL <sup>2,3</sup>	$(V_{IN} = -10 dBm, DC = 0V)$	f= 2.5GHz		-1.0		dB
		f=4.0GHz		-1.7		
		f= 100MHz		-59		
DDIL <sub>OFF</sub> <sup>2,3</sup>	Differential Off Isolation	f= 1.25GHz		-37		dB
DDIL <sub>OFF</sub> -"	Differential Off Isolation	f= 2.5GHz		-27		ab
		f=4.0GHz		-21		
	Differential Return Loss	f= 100MHz		-27		
DDRL <sup>2</sup>		f= 1.25GHz		-23.3		dB
DDKL		f= 2.5GHz		-23.3		uD
		f= 4.0GHz		-13.5		
	Near End Crosstalk	f= 100MHz		-57		
DDNEXT <sup>2,3</sup>		f= 1.25GHz		-38		σL
		f= 2.5GHz		-33		dB
		f= 4.0GHz		-32		
BW	-3dB Bandwidth			8.1		GHz

#### Notes:

1. Guaranteed by design. Typical values are at  $V_{DD}$  = 3.3V ,  $T_A$  = 25°C ambient and maximum loading.

2. S parameters are measured with our evaluation board made with Rogers (R04350) material. Trace width is 30 mil, length 540 mil, trace impedence is 50 Ohm (+/- 5%) and total insertion loss of the trace is 0.5dB@4GHz.

3. Measurement done with fixture deembedding.



**Diff. Insertion Loss and Return Test** Circuit

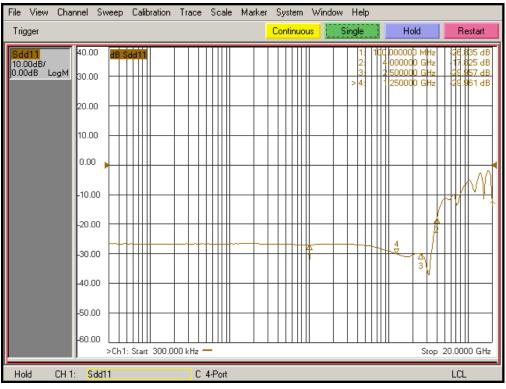
**Diff. Off Isolation Test Circuit** 

**Diff. Near End Xtalk Test Circuit** 

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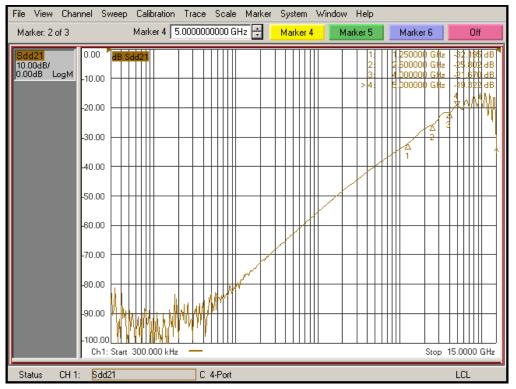


#### **Differential Insertion Loss**

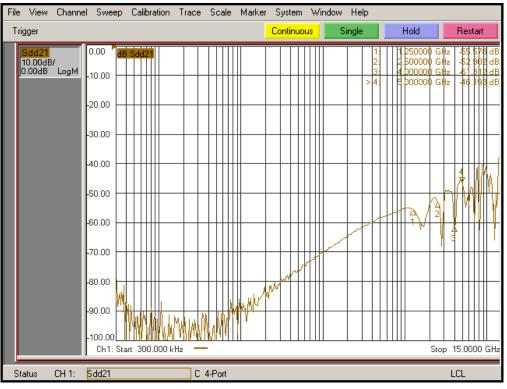


#### **Differential Return Loss**

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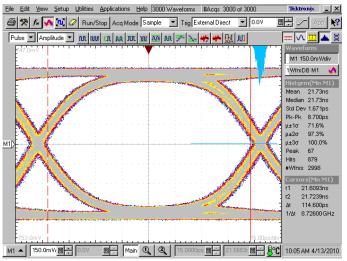


#### **Differential Off Isolation**

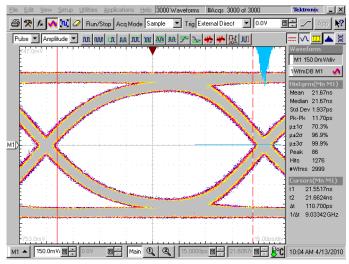


#### **Differential Crosstalk**

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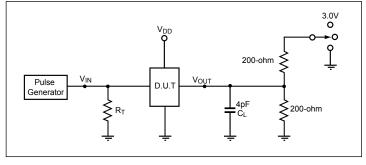


5.0 Gbps RX signal eye without PI3USB302-A



5.0 Gbps RX signal eye with PI3USB302-A

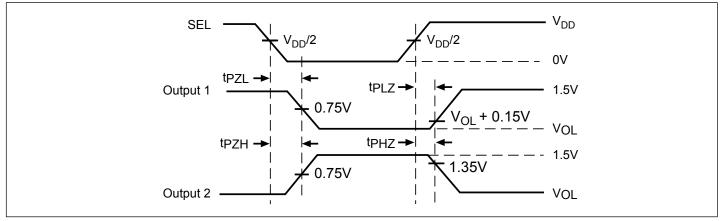
# Test Circuit for Electrical Characteristics<sup>(1-5)</sup>



#### **Switch Positions**

Test	Switch
t <sub>PLZ</sub> , t <sub>PZL</sub>	3.0V
t <sub>PHZ</sub> , t <sub>PZH</sub>	GND
Prop Delay	Open

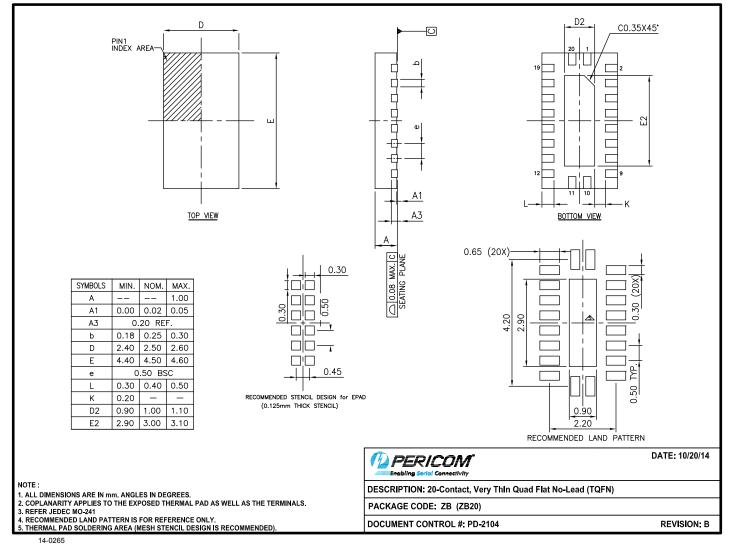
#### **Switching Waveforms**



#### Voltage Waveforms Enable and Disable Times

### PI3USB302-A 3.3V, USB 3.0, 1-Port, 2:1 Mux/DeMux Switch

### Packaging Mechanical: 20-TQFN (ZB)



#### Note:

1. For latest package info, please check: http://www.pericom.com/products/packaging/mechanicals.php

#### **Ordering Information**

Ordering Code	Package Code	Package Type
PI3USB302-AZBE	ZB	20-Contactm Very Think Quad Flat No-Lead (TQFN)
PI3USB302-AZBEX	ZB	20-Contactm Very Think Quad Flat No-Lead (TQFN), Tape & Reel

#### Notes:

Thermal characteristics can be found on the company web site at www.pericom.com/packaging/

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• E = Pb-free and Green

• Adding an X suffix = Tape/Reel

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