



**DMN2026UVT** 

#### **Product Summary**

V <sub>(BR)DSS</sub>	RDS(ON) Max	Ι <sub>D</sub> T <sub>A</sub> = +25°C
20V	$24m\Omega @ V_{GS} = 4.5V$	6.2A
200	$32m\Omega @ V_{GS} = 2.5V$	0.27

## **Description and Applications**

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power Management Functions
- Backlighting

#### **Features and Benefits**

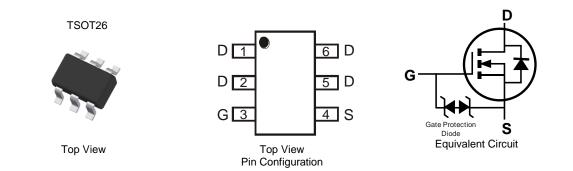
- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)

N-CHANNEL ENHANCEMENT MODE MOSFET

Halogen and Antimony Free. "Green" Device (Note 3)

#### **Mechanical Data**

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.013 grams (Approximate)



#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2026UVT-7	TSOT26	3,000/Tape & Reel
DMN2026UVT-13	TSOT26	10,000/Tape & Reel

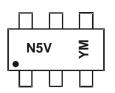
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



N5V = Product Type Marking CodeYM = Date Code MarkingY = Year (ex: B = 2014)M = Month (ex: 9 = September)

Date Code Kev

Date Code Key												
Year	201	4	2015		2016	20	17	2018		2019	2	2020
Code	В		С		D	I	E	F		G		Н
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	20	V	
Gate-Source Voltage	V <sub>GSS</sub>	±10	V	
Continuous Drain Current (Note 6) $V_{GS} = 4.5V$	ID	6.2	А	
Maximum Body Diode Forward Current (Note 6)		Is	2	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	20	A	

### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	1.15	W	
Thermal Desistence, lunction to Archient (Note 5)	Steady state	5	107	90 AA	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s R <sub>0JA</sub>		76	°C/W	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	PD	1.75	W	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	P	75		
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{ extsf{ heta}JA}$	50	°C/W	
Thermal Resistance, Junction to Case (Note 6)		R <sub>θ</sub> JC	16		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

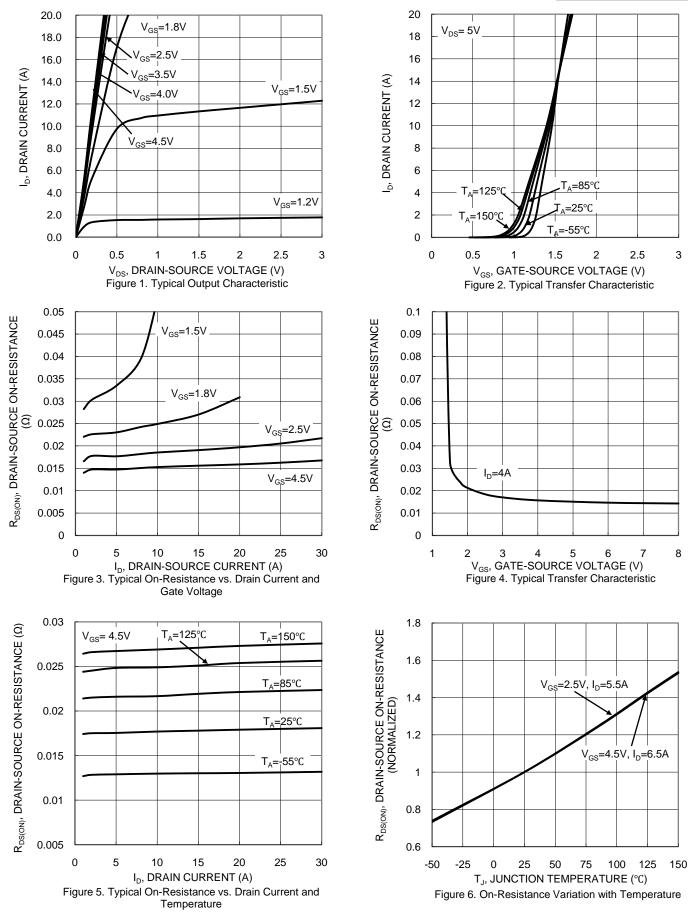
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)						·	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	_	_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.4	—	1.5	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance			18	24	mΩ	$V_{GS} = 4.5V, I_D = 6.2A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	21	32	11122	V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 5.2A	
Diode Forward Voltage	V <sub>SD</sub>		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1.3A$	
DYNAMIC CHARACTERISTICS (Note 8)						·	
Input Capacitance	Ciss		887	_			
Output Capacitance	Coss		91	_	pF	$V_{DS} = 10V, V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss		37	_			
Gate Resistance	Rg	_	191	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg		10	-			
Total Gate Charge (V <sub>GS</sub> = 8V)	Qg	_	18.4	—	nC		
Gate-Source Charge	Q <sub>gs</sub>	—	1.3	—	nc	$V_{DS} = 10V, I_D = 6.5A$	
Gate-Drain Charge	Q <sub>gd</sub>	_	1.8	_			
Turn-On Delay Time	t <sub>D(ON)</sub>		53	_			
Turn-On Rise Time	t <sub>R</sub>	_	66	_		$V_{DS} = 10V$ , $V_{GS} = 4.5V$ .	
Turn-Off Delay Time	t <sub>D(OFF)</sub>		619	—	ns	$R_G = 6\Omega$ , $R_L = 10\Omega$ , $I_D = 1A$	
Turn-Off Fall Time	tF		197	—			
Reverse Recovery Time	t <sub>RR</sub>		119	_	ns	I <sub>F</sub> = 4A, di/dt = 100A/µs	
Reverse Recovery Charge	Q <sub>RR</sub>		96	_	nC	$I_{F} = 4A$ , di/dt = 100A/µs	

Notes:

Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.



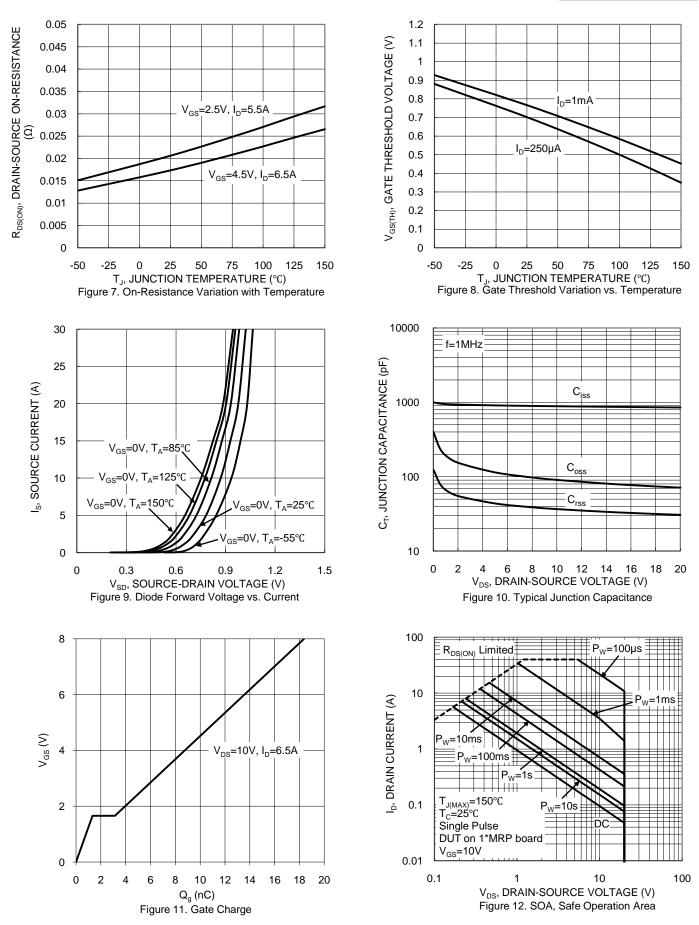
#### **DMN2026UVT**



NEW PRODUCT

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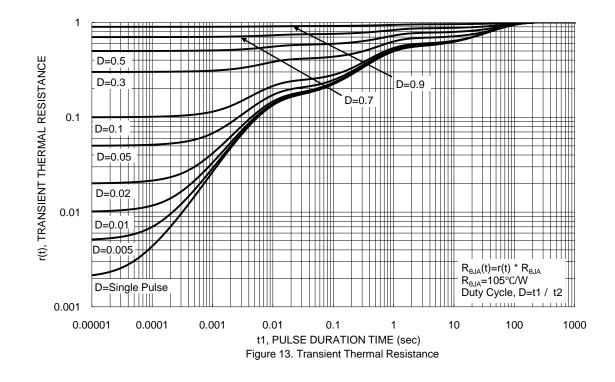


NEW PRODUCT

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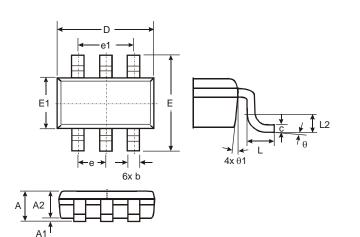
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# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

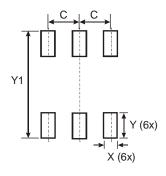


TSOT26						
Dim	Min	Max	Тур			
Α		1.00	_			
A1	0.01	0.10	_			
A2	0.84	0.90	—			
D	-	-	2.90			
Е	-	-	2.80			
E1	-	-	1.60			
b	0.30	0.45	—			
С	0.12	0.20	—			
е	-	-	0.95			
e1			1.90			
1	0.30	0.50	_			
L2			0.25			
θ	0°	8°	4°			
θ1	4°	12°	_			
All D	All Dimensions in mm					



## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
Y1	3.199

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