





### **DUAL SURFACE MOUNT SWITCHING DIODE**

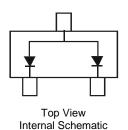
### **Features**

- Fast Switching Speed
- Small Surface Mount Package
- For General Purpose Switching Applications
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

### **Mechanical Data**

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound, (Note 5); UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating) Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Weight: 0.006 grams (Approximate)





### Ordering Information (Notes 5 & 6)

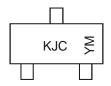
Part Number	Qualification	Case	Packaging
BAW56W-7-F	Commercial	SOT323	3000/Tape & Reel
BAW56WQ-7-F	Automotive	SOT323	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to https://www.diodes.com/quality/.

  5. Product manufactured with Date Code 0627 (week 27, 2006) and newer are built with Green Molding Compound. Product manufactured prior to Date
- Code 0627 are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.
- 6. For packaging details, go to our website at http://www.diodes.com.

## **Marking Information**



KJC = Product Type Marking Code YM = Date Code Marking Y = Year (ex: G = 2019)M = Month (ex: 9 = September)

Date Code Key

Year	2005	2006	2007	2008	2009	2010		2019	2020	2021	2022	2023	2024
Code	S	Т	U	V	W	Χ		G	Н	- 1	J	K	L
Month	Jan	Feb	Mar	Apr	Ma	v Ju	ın ,	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	. 6	3	7	8	9	0	N	D

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# **Maximum Ratings** $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic	Symbol	Value	Unit	
Non-Repetitive Peak Reverse Voltage	$V_{RM}$	100	V	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	75	V	
RMS Reverse Voltage	V <sub>R(RMS)</sub>	53	V	
Forward Continuous Current (Note 7)	I <sub>FM</sub>	300	mA	
Average Rectified Output Current (Note 7)	Io	150	mA	
Non-Repetitive Peak Forward Surge Current @ t = 1.0µs (Note 7) @ t = 1.0s	I <sub>FSM</sub>	2.0 1.0	A	

### **Thermal Characteristics**

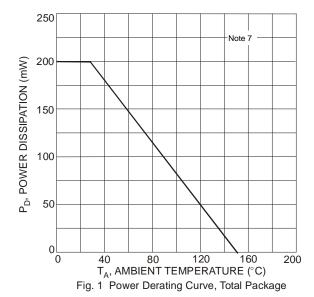
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	$P_{D}$	200	mW
Thermal Resistance Junction to Ambient Air (Note 7)	R <sub>OJA</sub>	625	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

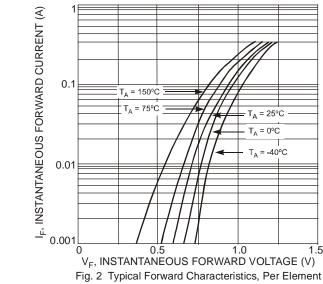
## Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	$V_{(BR)R}$	75	_	V	$I_R = 2.5 \mu A$
Forward Voltage	V <sub>F</sub>	_	0.715 0.855 1.0 1.25	V	$I_F = 1.0 \text{mA}$ $I_F = 10 \text{mA}$ $I_F = 50 \text{mA}$ $I_F = 150 \text{mA}$
Reverse Current (Note 8)	I <sub>R</sub>	_	2.5 50 30 25	μΑ μΑ μΑ nA	$V_R = 75V$ $V_R = 75V$ , $T_J = 150$ °C $V_R = 25V$ , $T_J = 150$ °C $V_R = 20V$
Total Capacitance	C <sub>T</sub>	_	2.0	pF	$V_R = 0, f = 1.0MHz$
Reverse Recovery Time	t <sub>rr</sub>	_	4.0	ns	$I_F = I_R = 10 \text{mA},$ $I_{rr} = 0.1 \times I_R, R_L = 100 \Omega$

Notes:

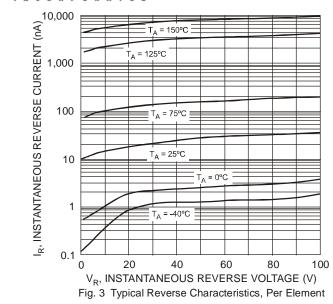
- 7. Part mounted on FR-4, 2oz 1inch squared copper pad PC board.
- 8. Short duration pulse test used to minimize self-heating effect.





May 2019





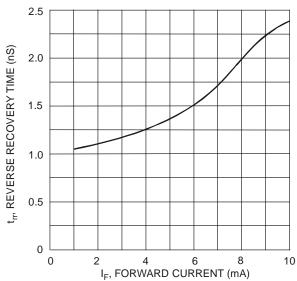


Fig. 5 Reverse Recovery Time vs. Forward Current,
Per Element

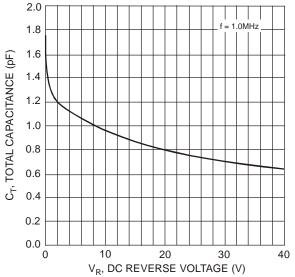


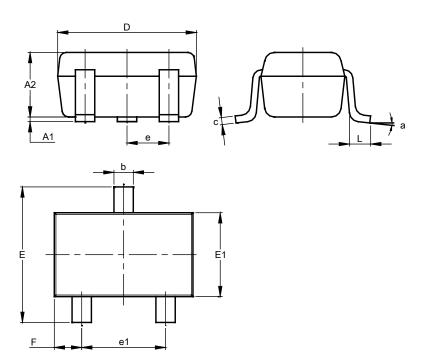
Fig. 4 Total Capacitance vs. Reverse Voltage, Per Element



# **Package Outline Dimensions**

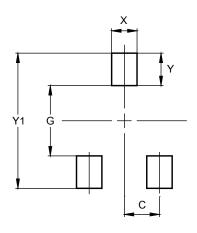
Please see http://www.diodes.com/package-outlines.html for the latest version.

### **SOT323**



SOT323							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.25	0.40	0.30				
С	0.10	0.18	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	C	0.650 BSC					
e1	1.20	1.40	1.30				
F	0.375	0.475	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All	Dimen	sions	in mm				

# **Suggested Pad Layout**



### **SOT323**

Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



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