#### 100V N-CHANNEL ENHANCEMENT MODE MOSFET

**SUMMARY** 

 $V_{(BR)DSS} = 100V; R_{DS(ON)} = 0.25\Omega I_D = 2.1A$ 

#### **DESCRIPTION**

This new generation of TRENCH MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.



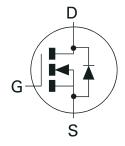
SO8

#### **FEATURES**

- Low on-resistance
- · Fast switching speed
- Low threshold
- · Low gate drive
- Low profile SOIC package

#### **APPLICATIONS**

- DC DC converters
- Power management functions
- Disconnect switches
- Motor control



#### **PINOUT**

# \$1 D1 G1 Dual D1 \$2 Device D2 G2 D2

Top View

#### **ORDERING INFORMATION**

DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMN10A08DN8TA	7″	12mm	500 units
ZXMN10A08DN8TC	13″	12mm	2,500 units

#### **DEVICE MARKING**

 ZXMN 10A08D

**ZETEX** 

#### **ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V <sub>DSS</sub>	100	V
Gate source voltage	$V_{GS}$	±20	V
Continuous drain current $V_{GS}=10V; T_A=25^{\circ}C^{(b)}$ $V_{GS}=10V; T_A=70^{\circ}C^{(b)}$ $V_{GS}=10V; T_A=25^{\circ}C^{(a)}$	I <sub>D</sub>	2.1 1.7 1.6	А
Pulsed drain current (c)	I <sub>DM</sub>	9	А
Continuous source current (body diode) (b)	I <sub>S</sub>	2.6	А
Pulsed source current (body diode) (c)	I <sub>SM</sub>	9	А
Power dissipation at T <sub>A</sub> =25°C <sup>(a)</sup> Linear derating factor	P <sub>D</sub>	1.25 10	W mW/°C
Power dissipation at T <sub>A</sub> =25°C <sup>(b)</sup> Linear derating factor	P <sub>D</sub>	1.8 14.5	W mW/°C
Operating and storage temperature range	T <sub>j</sub> :T <sub>stg</sub>	-55 to +150	°C

#### THERMAL RESISTANCE

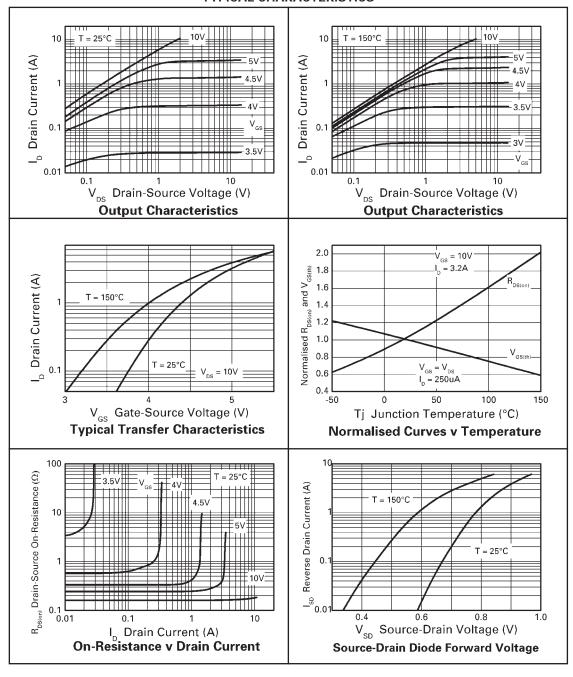
PARAMETER	SYMBOL	VALUE	UNIT	
Junction to ambient (a)	$R_{\theta JA}$	100	°C/W	
Junction to ambient (b)	$R_{\theta JA}$	69	°C/W	

#### NOTES

- (a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions
- (b) For a device surface mounted on FR4 PCB measured at t  $\! \leqslant \! 5$  secs.
- (c) Repetitive rating 25mm x 25mm FR4 PCB, D = 0.02, pulse width  $300\mu s$  pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph



#### TYPICAL CHARACTERISTICS





# **ELECTRICAL CHARACTERISTICS** (at $T_A = 25$ °C unless otherwise stated).

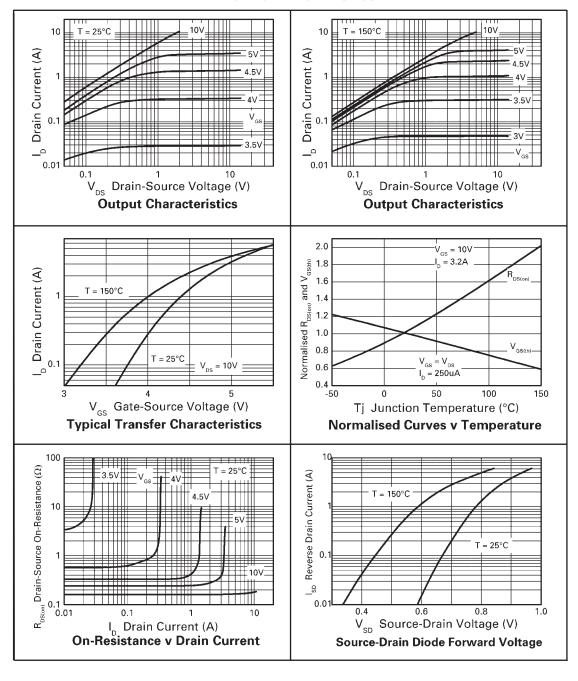
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS.	
STATIC	•	•					
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	100			V	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	
Zero gate voltage drain current	I <sub>DSS</sub>			0.5	μΑ	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V	
Gate-body leakage	I <sub>GSS</sub>			100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
Gate-source threshold voltage	V <sub>GS(th)</sub>	2.0			V	I <sub>D</sub> =250μA, V <sub>DS</sub> = V <sub>GS</sub>	
Static drain-source on-state resistance (1)	R <sub>DS(on)</sub>			0.25 0.30	$\Omega$	V <sub>GS</sub> =10V, I <sub>D</sub> =3.2A V <sub>GS</sub> =6V, I <sub>D</sub> =2.6A	
Forward transconductance (1)(3)	9 <sub>fs</sub>		5.0		S	V <sub>DS</sub> =15V,I <sub>D</sub> =3.2A	
DYNAMIC (3)		•			•		
Input capacitance	C <sub>iss</sub>		405		pF		
Output capacitance	C <sub>oss</sub>		28.2		pF	V <sub>DS</sub> =50 V, V <sub>GS</sub> =0V, f=1MHz	
Reverse transfer capacitance	C <sub>rss</sub>		14.2		pF	1-11/11/2	
SWITCHING <sup>(2) (3)</sup>	•	•	•		•		
Turn-on delay time	t <sub>d(on)</sub>		3.4		ns		
Rise time	t <sub>r</sub>		2.2		ns	V <sub>DD</sub> =30V, I <sub>D</sub> =1.2A	
Turn-off delay time	t <sub>d(off)</sub>		8		ns	$R_G \approx 6.0\Omega$ , $V_{GS} = 10V$	
Fall time	t <sub>f</sub>		3.2		ns		
Gate charge	Qg		4.2		nC	V <sub>DS</sub> =50V,V <sub>GS</sub> =5V, I <sub>D</sub> =1.2A	
Total gate charge	Qg		7.7		nC		
Gate-source charge	Q <sub>gs</sub>		1.8		nC	V <sub>DS</sub> =50V,V <sub>GS</sub> =10V, I <sub>D</sub> =1.2A	
Gate-drain charge	Q <sub>gd</sub>		2.1		nC		
SOURCE-DRAIN DIODE							
Diode forward voltage <sup>(1)</sup>	V <sub>SD</sub>		0.87	0.95	V	T <sub>J</sub> =25°C, I <sub>S</sub> =3.2A, V <sub>GS</sub> =0V	
Reverse recovery time <sup>(3)</sup>	t <sub>rr</sub>		27		ns	T <sub>J</sub> =25°C, I <sub>F</sub> =1.2A,	
Reverse recovery charge <sup>(3)</sup>	Q <sub>rr</sub>		32		nC	di/dt= 100A/μs	

#### NOTES

- (1) Measured under pulsed conditions. Width = 300 $\mu s$ . Duty cycle  $\leq~2\%$  .
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.

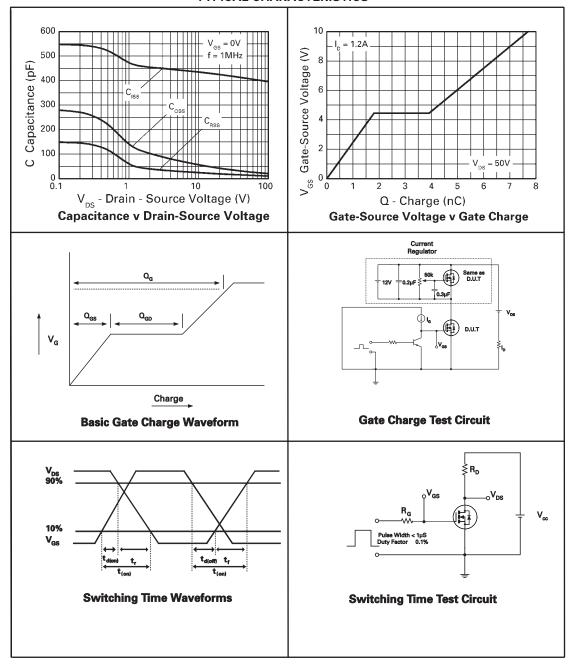


#### TYPICAL CHARACTERISTICS



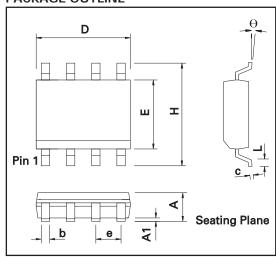


#### TYPICAL CHARACTERISTICS





#### **PACKAGE OUTLINE**



CONTROLLING DIMENSIONS IN MILLIMETERS APPROX CONVERSIONS INCHES

#### **PACKAGE DIMENSIONS**

DIM	Millin	neters	Inc	hes	DIM -	Millim		neters	Inches	
DIIVI	Min	Max	Min	Max	DIIVI	Min	Max	Min	Max	
А	1.35	1.75	0.053	0.069	е	1.27	BSC	0.050	BSC	
A1	0.10	0.25	0.004	0.010	b	0.33	0.51	0.013	0.020	
D	4.80	5.00	0.189	0.197	С	0.19	0.25	0.008	0.010	
Н	5.80	6.20	0.228	0.244	θ	0°	8°	0°	8°	
Е	3.80	4.00	0.150	0.157	h	0.25	0.50	0.010	0.020	
L	0.40	1.27	0.016	0.050	-	-	-	-	-	

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