



**DMP3007SFG** 

POWERDI

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C		
-30V	$6m\Omega @ V_{GS} = -10V$	-70A		
	13mΩ @ V <sub>GS</sub> = -4.5V	-45A		

### Description

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### Applications

- Backlighting
- **Power Management Functions**
- **DC-DC** Converters

### **Features and Benefits**

- Low R<sub>DS(ON)</sub> Ensures on State Losses are Minimized
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Occupies Just 33% of the Board Area Occupied By SO-8 Enabling Smaller End Product

**30V P-CHANNEL ENHANCEMENT MODE MOSFET** 

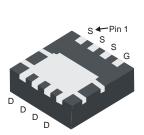
- **ESD** Protected Gate
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

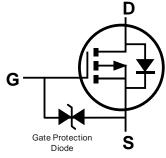
### **Mechanical Data**

- Case: PowerDI3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed Over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.030 grams (Approximate)



Top View





Bottom View

Equivalent Circuit

### Ordering Information (Note 4)

n		
Part Number	Case	Packaging
DMP3007SFG-7	PowerDI3333-8	2,000/Tape & Reel
DMP3007SFG-13	PowerDI3333-8	3,000/Tape & Reel

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied. 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### Marking Information

Notes:



V08= Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 16 = 2016) WW = Week Code (01 to 53)



## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	-30	V
Gate-Source Voltage			V <sub>GSS</sub>	±25	V
Continuous Drain Current (Note 7) $V_{GS}$ = -10V	Steady State	$T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$	ID	-70 -55	А
Maximum Continuous Body Diode Forward Current (Note 6)			Is	-3.0	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	-120	А
Avalanche Current (Notes 8) L = 1mH			I <sub>AS</sub>	-16	А
Avalanche Energy (Notes 8) L = 1mH			E <sub>AS</sub>	130	mJ

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	PD	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R <sub>0JA</sub>	105	°C/W
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	PD	2.8	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0JA</sub>	45	°C/W
Thermal Resistance, Junction to Case (Note 7)	R <sub>θJC</sub>	3.0	°C/W	
Operating and Storage Temperature Range		$T_{J,} T_{STG}$	-55 to +150	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)	0,		. 76		•		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	_	_	V	$V_{GS} = 0V, I_D = -250 \mu A$	
Zero Gate Voltage Drain Current	IDSS	_	—	-1	μA	$V_{DS} = -24V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>		—	±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-1.0	_	-3.0	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
Static Drain-Source On-Resistance	P	_	4.3	6	mΩ	$V_{GS} = -10V, I_D = -11.5A$	
	R <sub>DS(ON)</sub>	—	6.6	13		$V_{GS} = -4.5V, I_D = -8.5A$	
Diode Forward Voltage	V <sub>SD</sub>	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	C <sub>iss</sub>	_	2826		pF		
Output Capacitance	C <sub>oss</sub>	_	606		pF	V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V, −f = 1.0MHz	
Reverse Transfer Capacitance	Crss	—	305	—	pF		
Gate Resistance	R <sub>g</sub>	—	23	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	_	31.2	_	nC		
Total Gate Charge (V <sub>GS</sub> = -10V)	Qg	_	64.2	_	nC		
Gate-Source Charge	Q <sub>gs</sub>	_	10.6	_	nC	V <sub>DS</sub> = -15V, I <sub>D</sub> = -11.5A	
Gate-Drain Charge	Q <sub>gd</sub>	_	11.6		nC	1	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	4.8		ns		
Turn-On Rise Time	t <sub>R</sub>	_	4.3		ns	$V_{DD} = -15V, V_{GS} = -10V,$ $R_g = 6\Omega, I_D = -11.5A$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	306	_	ns		
Turn-Off Fall Time	t <sub>F</sub>		125		ns		
Reverse Recovery Time	t <sub>RR</sub>		19	_	ns		
Reverse Recovery Charge	Q <sub>RR</sub>	_	9.8	_	nC	I <sub>S</sub> = -11.5A, dl/dt = 100A/µs	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

7. Thermal resistance from junction to soldering point (on the exposed drain pad).

8. IAs and EAs rating are based on low frequency and duty cycles to keep  $T_J = +25^{\circ}C$ .

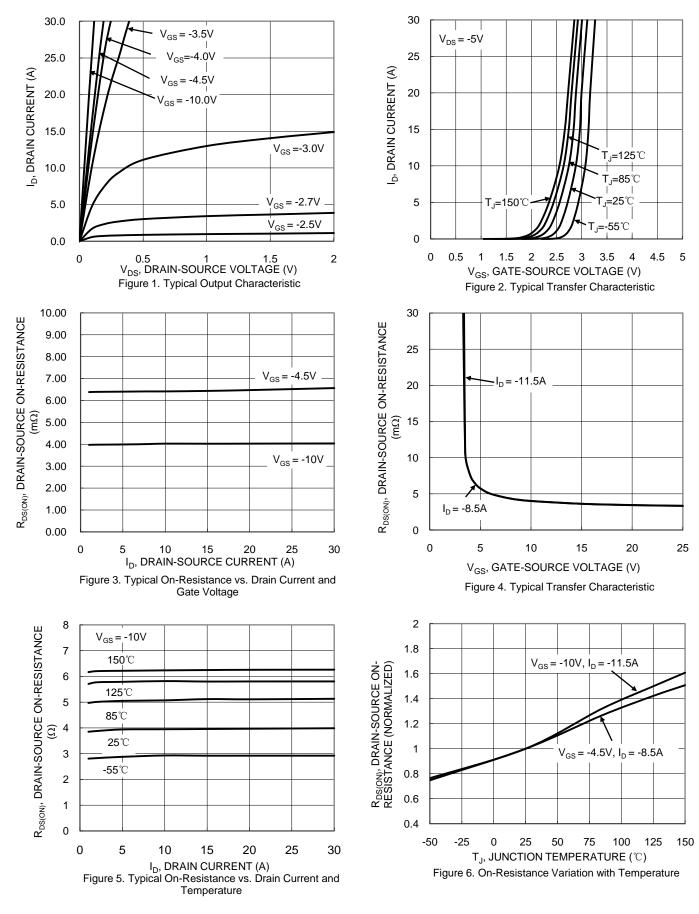
9. Short duration pulse test used to minimize self-heating effect.

10. Guaranteed by design. Not subject to product testing.

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### DMP3007SFG

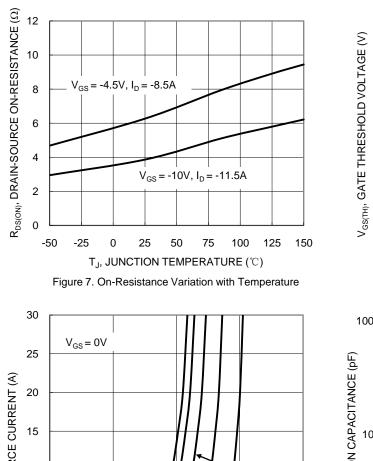


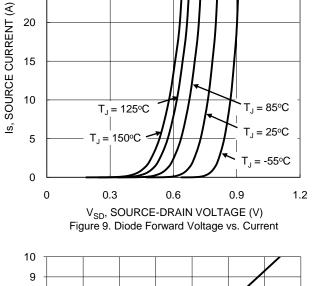
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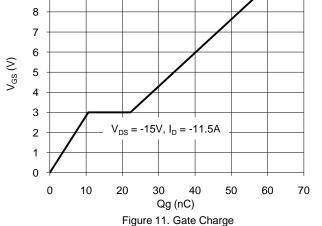
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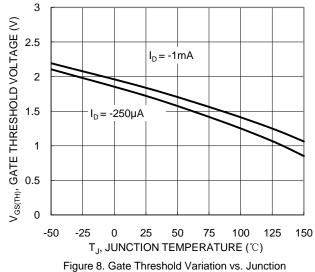


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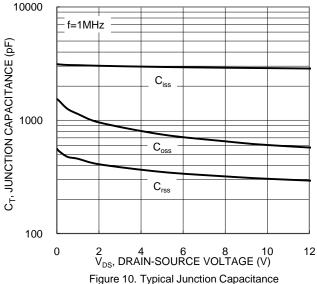


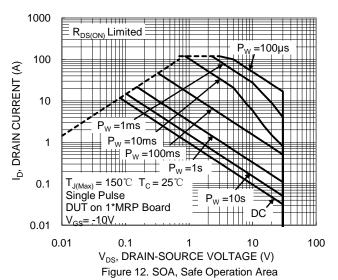






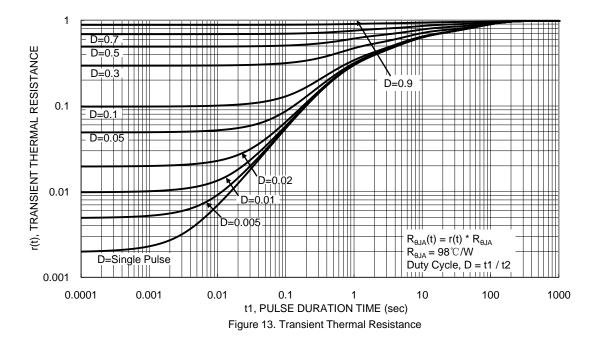
Temperature





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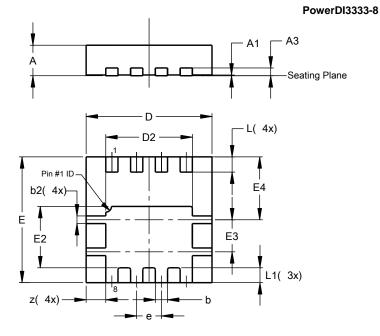






### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

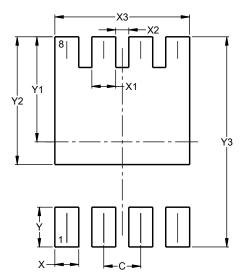


			-			
PowerDI3333-8						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05	0.02			
A3	-	-	0.203			
b	0.27	0.37	0.32			
b2	0.15	0.25	0.20			
D	3.25	3.35	3.30			
D2	2.22	2.32	2.27			
Е	3.25	3.35	3.30			
E2	1.56	1.66	1.61			
E3	0.79	0.89	0.84			
E4	1.60	1.70	1.65			
е	_	_	0.65			
L	0.35	0.45	0.40			
L1	_	_	0.39			
z	_	_	0.515			
All Dimensions in mm						

### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8



Dimensions	Value (in mm)		
С	0.650		
Х	0.420		
X1	0.420		
X2	0.230		
X3	2.370		
Y	0.700		
Y1	1.850		
Y2	2.250		
Y3	3.700		



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