

Product Summary

BV _{DSS}	R _{D1D2 max}	I _D T _A = +25°C
-20V	90mΩ @ V _{GS} = -4.5V	-3.2A
	120mΩ @ V _{GS} = -2.5V	-2.7A

Features

- PCB Footprint of 4mm²
- Low On-Resistance
- Low Input Capacitance
- Low Profile, 0.6mm Maximum Height
- **ESD Protected Gate**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](https://www.diodes.com/quality/product-definitions/) or your local Diodes representative.**
<https://www.diodes.com/quality/product-definitions/>

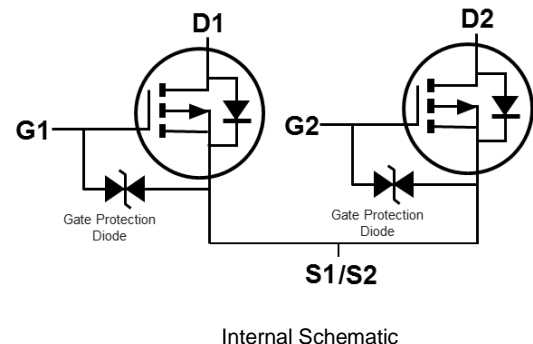
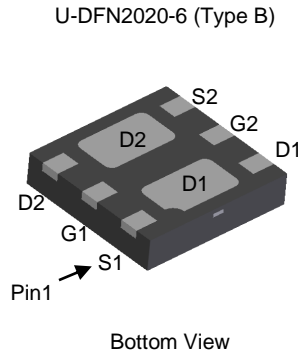
Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, which makes it ideal for high-efficiency power management applications.

- Load Switch
- Power Management Functions
- Portable Power Adaptors

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Weight: 0.0065 grams (Approximate)



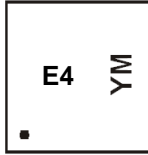
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2090UFDB-7	U-DFN2020-6 (Type B)	3000/Tape & Reel
DMP2090UFDB-13	U-DFN2020-6 (Type B)	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information

Site 1:

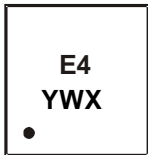


E4 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: G = 2019)
 M = Month (ex: 9 = September)

Date Code Key

Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G	H	I	J	K	L	M	N	O	P	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Site 2:



E4 = Product Type Marking Code
 YWX = Date Code Marking
 Y = Year (ex: 9 = 2019)
 W = Week (ex: a = week 27; z represents week 52 and 53)
 X = Internal Code (ex: U = Monday)

Date Code Key

Year	2019	2020	2021	2022	2023	2024	2025	2026	2027
Code	9	0	1	2	3	4	5	6	7
Week	1-26			27-52			53		
Code	A-Z			a-z			z		
Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
Code	T	U	V	W	X	Y	Z		

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	-20	V
Gate-Source Voltage	V _{GSS}	±8	V
Continuous Drain Current (Note 6) V _{GS} = -4.5V	I _D	-3.2	A
Steady State T _A = +25°C T _A = +70°C		-2.5	
Maximum Continuous Body Diode Forward Current (Note 6)	I _S	-1.9	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	-22	A
Avalanche Current (Note 7) L = 0.1mH	I _{AS}	-11	A
Avalanche Energy (Note 7) L = 0.1mH	E _{AS}	7	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	0.79	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	159	°C/W
Total Power Dissipation (Note 6)	P _D	1.39	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{θJA}	90	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	—	V	V _{GS} = 0V, I _D = -250µA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	-1.0	µA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	µA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	-0.3	—	-1.0	V	V _{DS} = V _{GS} , I _D = -250µA
Static Drain1-Drain2 On-Resistance	R _{D1D2}	—	37	90	mΩ	V _{GS} = -4.5V, I _D = -4A
		—	50	120		V _{GS} = -2.5V, I _D = -3.5A
Diode Forward Voltage	V _{SD}	—	-0.7	-1.2	V	V _{GS} = 0V, I _S = -1.0A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iSS}	—	634	—	pF	V _{DS} = -10V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oSS}	—	81	—	pF	
Reverse Transfer Capacitance	C _{rSS}	—	66	—	pF	
Gate Resistance	R _g	—	20	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = -4.5V)	Q _g	—	6.8	—	nC	V _{DS} = -4.5V, I _D = -4A, V _{DS} = -10V
Gate-Source Charge	Q _{gs}	—	0.7	—	nC	
Gate-Drain Charge	Q _{gd}	—	1.6	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	4.2	—	ns	V _{DS} = -10V, V _{GS} = -4.5V, R _L = 3.3Ω, R _g = 1Ω
Turn-On Rise Time	t _r	—	3.4	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	23	—	ns	
Turn-Off Fall Time	t _f	—	9.6	—	ns	
Body Diode Reverse Recovery Time	t _{RR}	—	1.8	—	ns	I _S = -1.0A, dI/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q _{RR}	—	9.4	—	nC	I _S = -1.0A, dI/dt = 100A/µs

- Notes:
- Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
 - Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.
 - I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

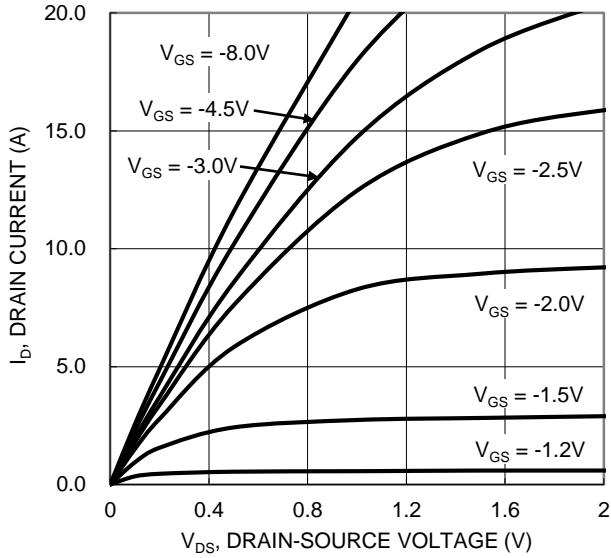


Figure 1. Typical Output Characteristic

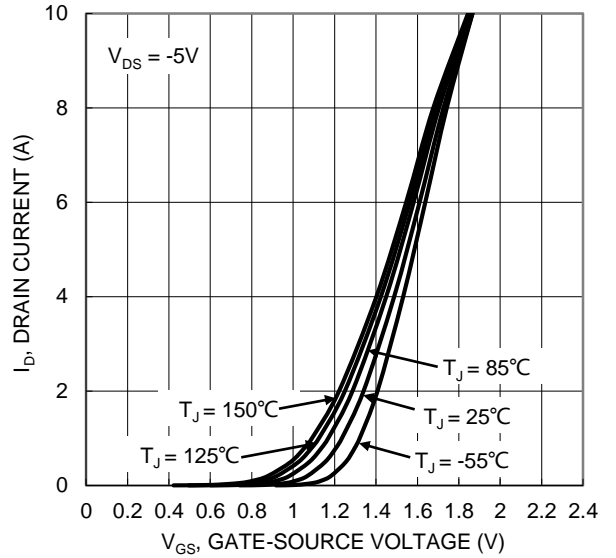


Figure 2. Typical Transfer Characteristic

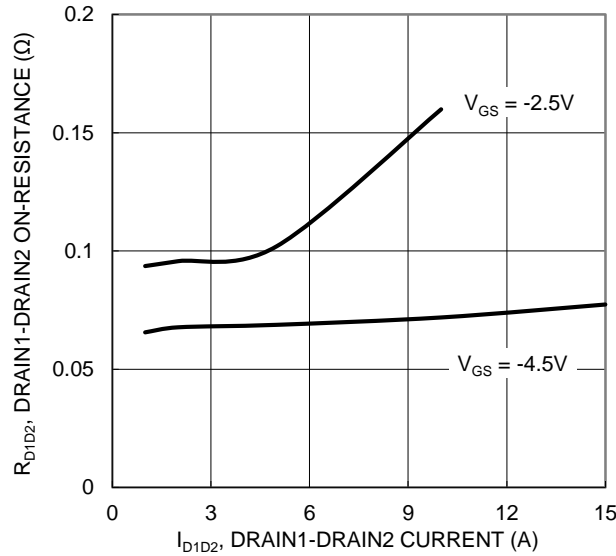


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

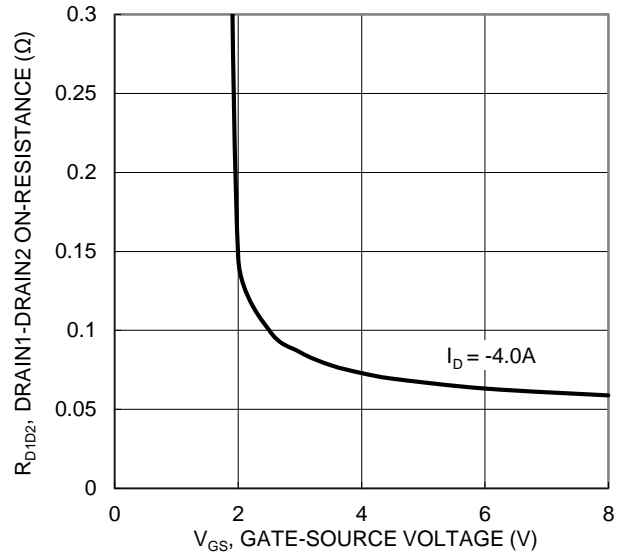


Figure 4. Typical Transfer Characteristic

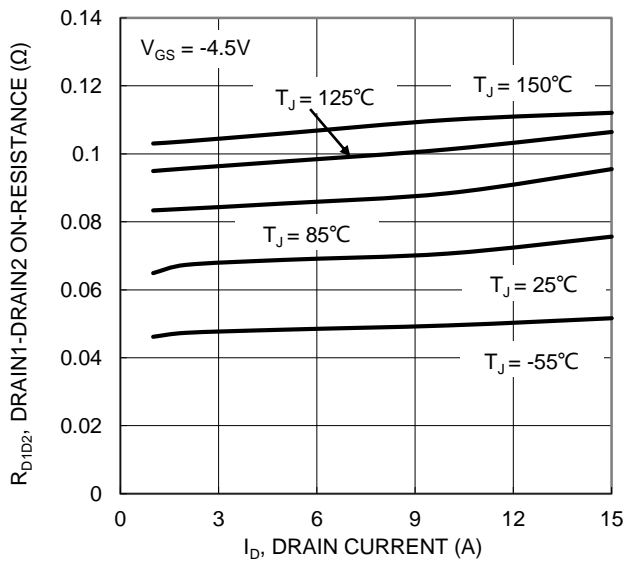


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

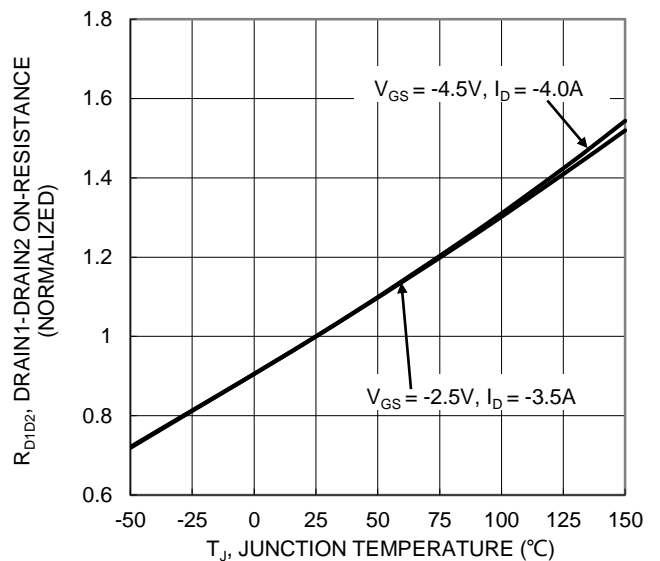


Figure 6. On-Resistance Variation with Junction Temperature

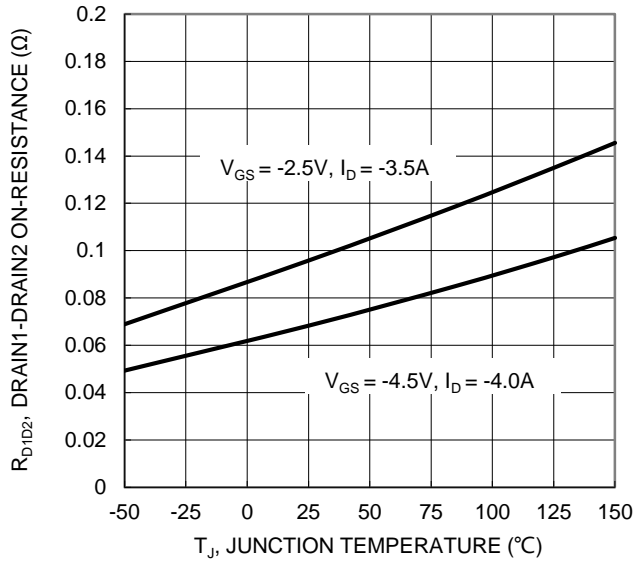


Figure 7. On-Resistance Variation with Junction Temperature

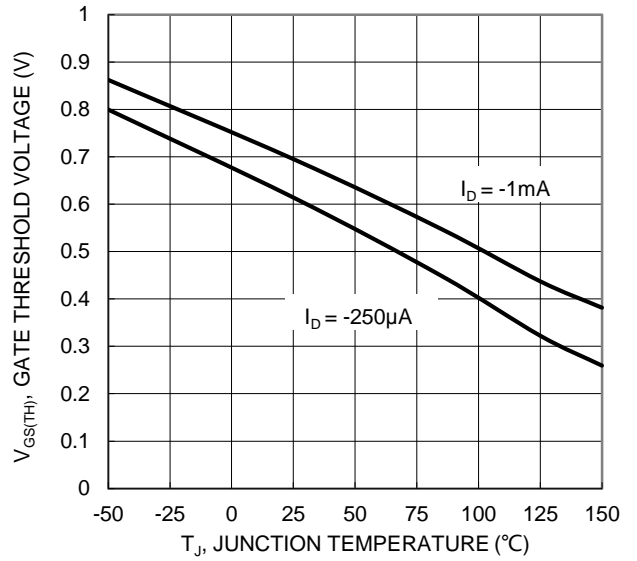


Figure 8. Gate Threshold Variation vs. Junction Temperature

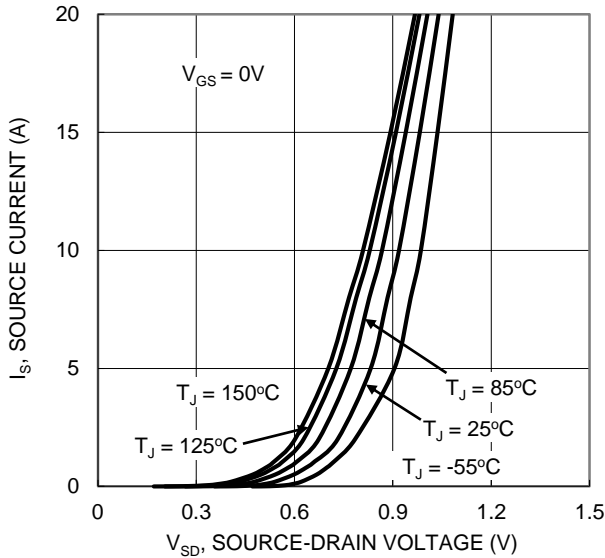


Figure 9. Diode Forward Voltage vs. Current

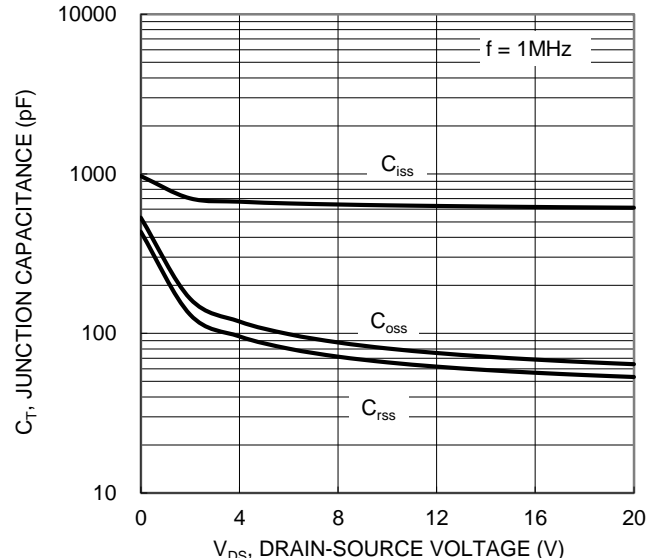


Figure 10. Typical Junction Capacitance

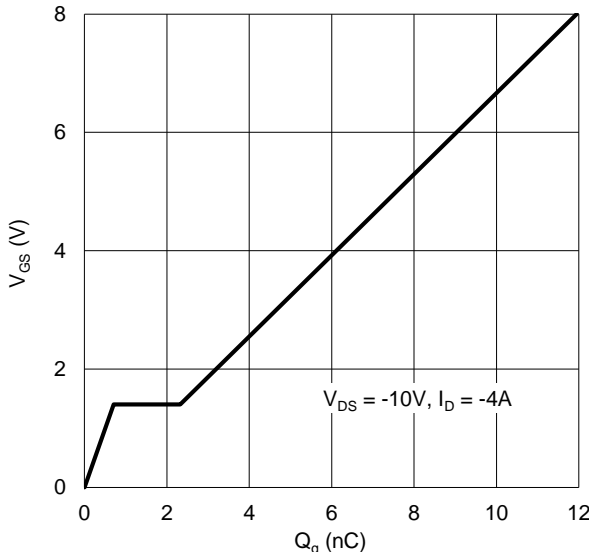


Figure 11. Gate Charge

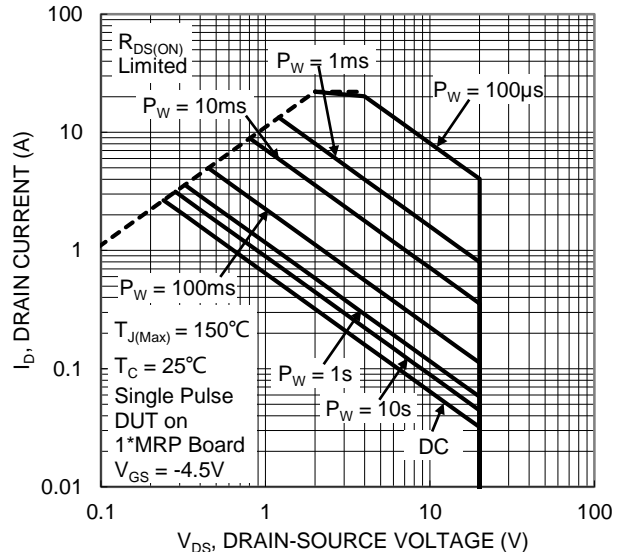


Figure 12. SOA, Safe Operation Area

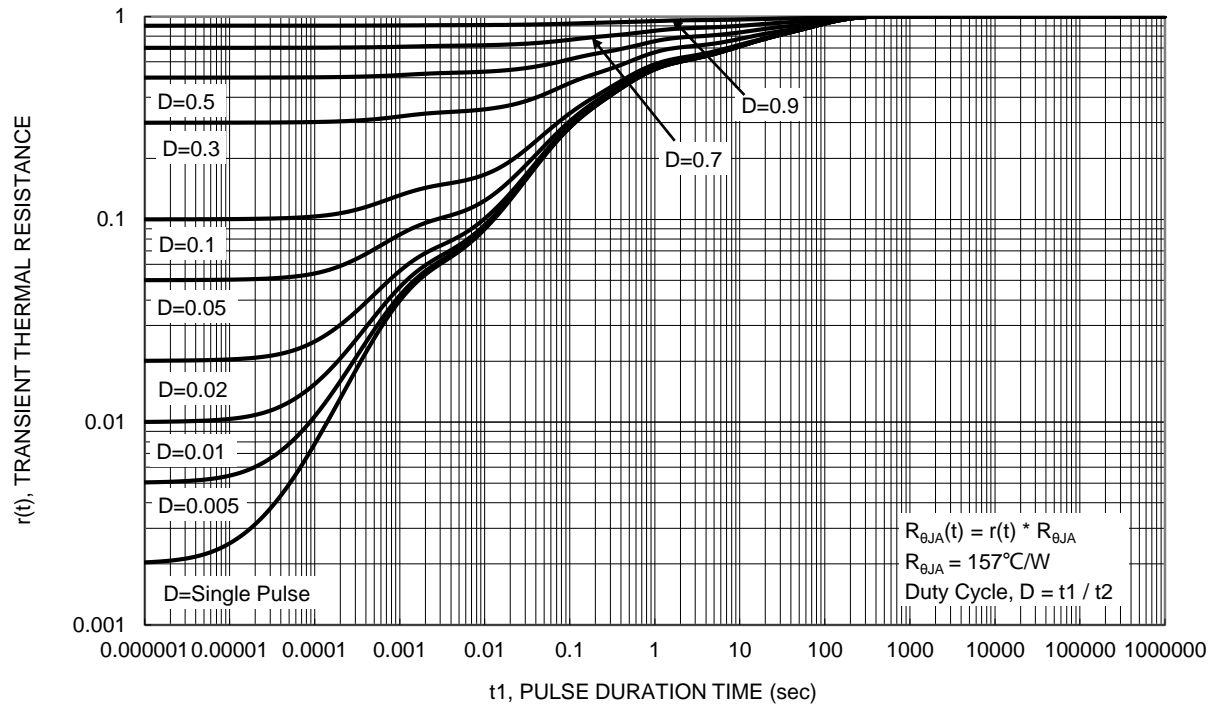
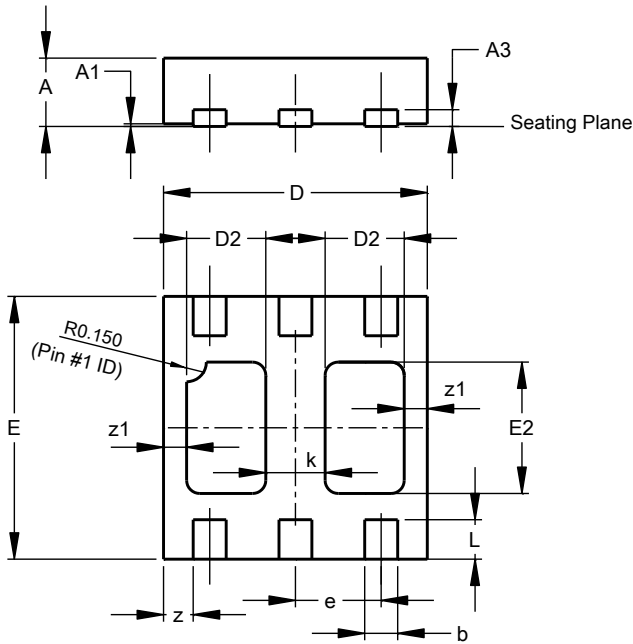


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-6 (Type B)

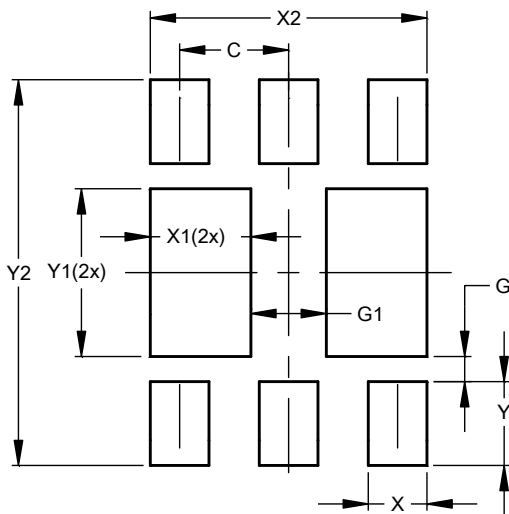


U-DFN2020-6 Type B			
Dim	Min	Max	Typ
A	0.545	0.605	0.575
A1	0.00	0.05	0.02
A3	-	-	0.13
b	0.20	0.30	0.25
D	1.95	2.075	2.00
D2	0.50	0.70	0.60
e	-	-	0.65
E	1.95	2.075	2.00
E2	0.90	1.10	1.00
k	-	-	0.45
L	0.25	0.35	0.30
z	-	-	0.225
z1	-	-	0.175
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-6 (Type B)



Dimensions	Value (in mm)
C	0.650
G	0.150
G1	0.450
X	0.350
X1	0.600
X2	1.650
Y	0.500
Y1	1.000
Y2	2.300

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