

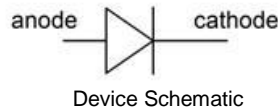
Product Summary

V_{RRM} (V)	I_o (A)	V_F Max (V)	I_R Max (μA)
40	2.0	0.52	150

Description and Applications

The SDT2L40CP3 is a 40-volt 2A Trench Schottky Barrier Rectifier that is optimized for low forward voltage drop and low leakage current, housed in a compact chip scale package (CSP) that occupies only 1.28mm² board space with low profile. The low thermal resistance enables designers to meet design challenges of increasing efficiency whilst at the same time reducing board space. It is ideally suited for use in portable applications as a:

- Blocking Diode
- Boost Diode
- Switching Diode
- Reverse Protection Diode

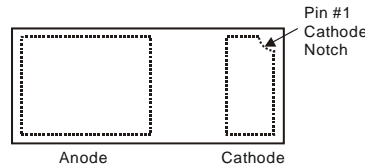


Features and Benefits

- Low Forward Voltage (V_F) Minimizes Conduction Losses and Improves Efficiency.
- Reduced High Temperature Reverse Leakage; Increased Reliability Against Thermal Runaway Failure in High Temperature Operation.
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](#) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

Mechanical Data

- Package: X3-DSN1608-2
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiAu Bump. Solderable per MIL-STD-202, Method 208 (e4)
- Polarity: Cathode Dot
- Weight: 0.001 grams (Approximate)

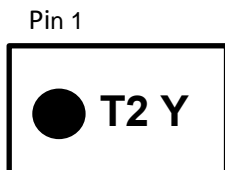


Ordering Information (Note 4)

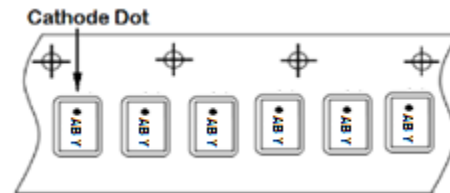
Part Number	Package	Packing	
		Qty.	Carrier
SDT2L40CP3-7B	X3-DSN1608-2	10,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



T2 = Product Type Marking Code
Y = Date Code Marking
Y or Y = Year (ex: I = 2021)
Dot Denotes Cathode Pin



Date Code Key

Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	H	I	J	K	L	M	N	O	P	R	S	T

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	40	V
Average Rectified Output Current	I _o	2.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	28	A
ESD (Human Body Model)	ESD	8	kV
ESD (Machine Model)		0.4	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	R _{θJA}	150	°C/W
Typical Thermal Resistance Junction to Ambient (Note 6)	R _{θJA}	50	°C/W
Typical Thermal Resistance Junction to Case (Note 5)	R _{θJC}	35	°C/W
Typical Thermal Resistance Junction to Case (Note 6)	R _{θJC}	10	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop	V _F	—	0.45	0.52	V	I _F = 2.0A, T _J = +25°C
Reverse Current (Note 7)	I _R	—	40	150	μA	V _R = 40V, T _J = +25°C
Junction Capacitance	C _T	—	285	—	pF	V _R = 4V, f = 1.0MHz

Notes: 5. Device mounted on FR-4 PCB, 2oz. copper, minimum recommended pad layout per <http://www.diodes.com/package-outlines.html>.
6. Device mounted on 1inch sq. copper pad, 2oz.
7. Short duration pulse test used to minimize self-heating effect.

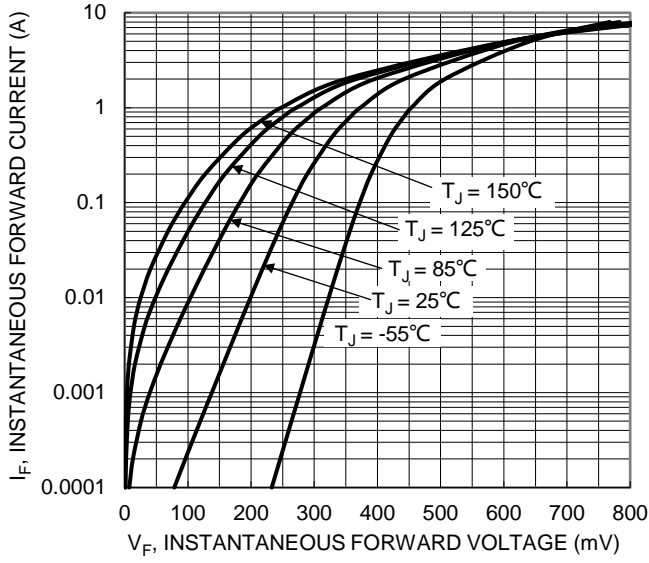


Figure 1. Typical Forward Characteristics

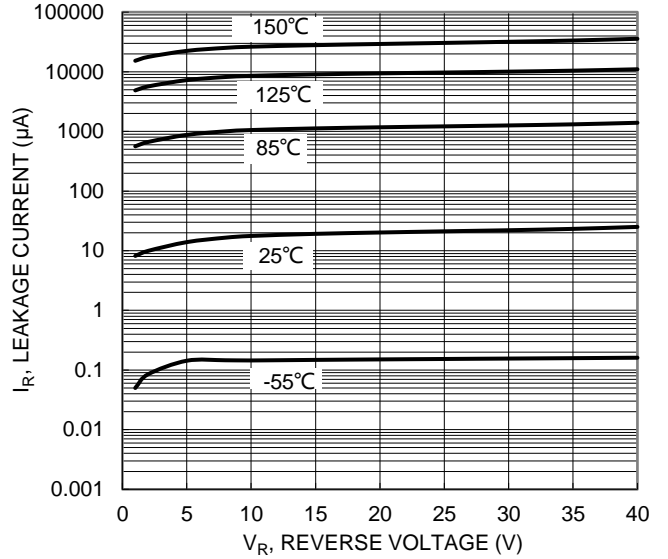


Figure 2. Typical Reverse Characteristics

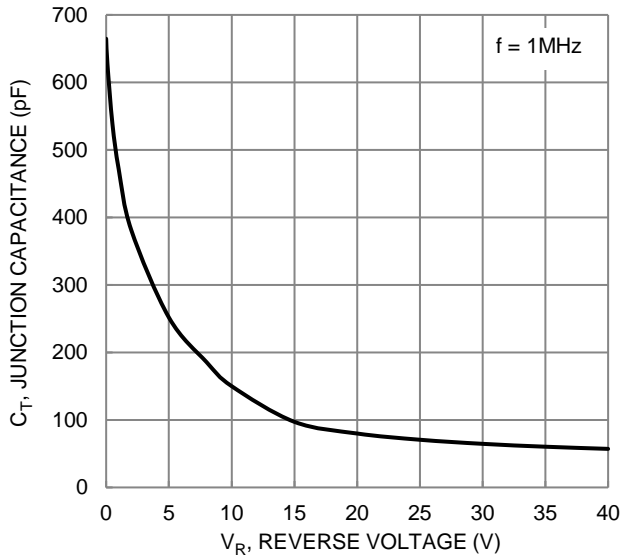


Figure 3. Typical Junction Capacitance

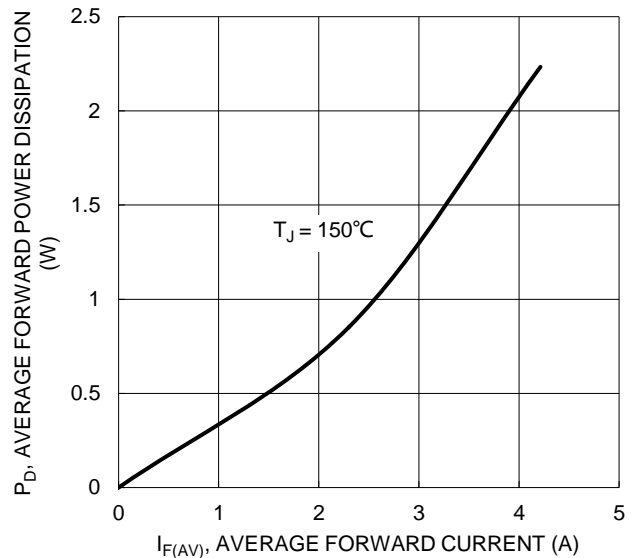
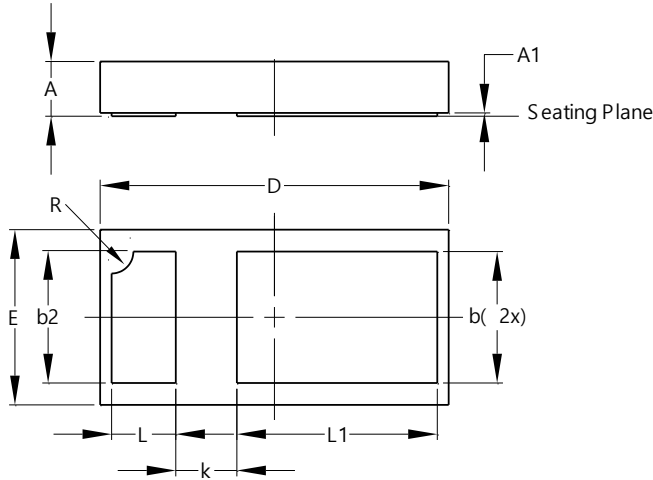


Figure 4. Forward Power Dissipation

Package Outline Dimensions (Note 8)

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X3-DSN1608-2



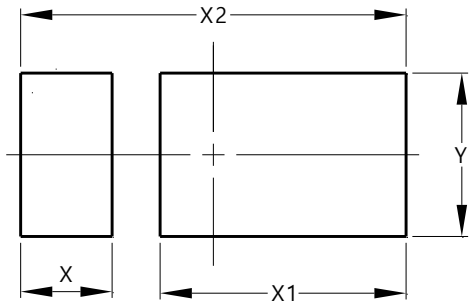
X3-DSN1608-2			
Dim	Min	Max	Typ
A	0.20	0.30	0.25
A1	--	0.02	--
b	0.55	0.65	0.60
b2	0.55	0.65	0.60
D	1.56	1.64	1.60
E	0.76	0.84	0.80
k	--	--	0.282
L	0.25	0.35	0.3
L1	0.90	1.00	0.95
R	--	--	0.10
All Dimensions in mm			

Note 8: Device side walls are electrically active bare silicon. Avoid contact of solder or flux on the side walls during the PCB assembly process.

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X3-DSN1608-2



Dimensions	Value (in mm)
X	0.385
X1	1.035
X2	1.622
Y	0.690

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