



DMT4004LPS

Product Summary

BV _{DSS}	R _{DS(ON)} Max	Ι _D T _C = +25°C
40V	2.5mΩ @ V _{GS} = 10V	90A
	4mΩ @ V _{GS} = 4.5V	90A

Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$, yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Engine Management Systems
- Body Control Electronics
- DC-DC Converters

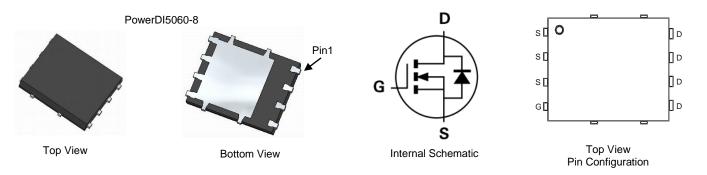
40V N-CHANNEL ENHANCEMENT MODE MOSFET POWERDI

Features

- 100% Unclamped Inductive Switching ensures more reliable and robust end application
- Low R_{DS(ON)} minimizes power losses
- Low Qg minimizes switching losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDI5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Weight: 0.097 grams (Approximate)



Ordering Information (Note 4)

h		
Part Number	Case	Packaging
DMT4004LPS-13	PowerDI5060-8	2,500 / Tape & Reel

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

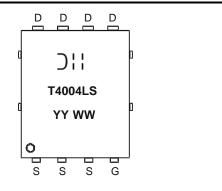
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:



) || = Manufacturer's Marking T4004LS = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 16 = 2016) WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	40	V	
Gate-Source Voltage	V _{GSS}	±20	V	
Continuous Drain Current (Note 5)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	26 21	А
	$T_{C} = +25^{\circ}C$	I _D	90	A
Continuous Drain Current (Note 6)	T _C = +70°C (Note 8)		90	
Maximum Continuous Body Diode Forward Current (Note 6)	Is	70	A	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	100	A	
Avalanche Current, L=0.2mH		I _{AS}	33.3	A
Avalanche Energy, L=0.2mH	E _{AS}	110	mJ	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	2.6	W
Thermal Resistance, Junction to Ambient (Note 5)		R _{0JA}	47	°C/W
Total Power Dissipation (Note 6)	T _C = +25°C	PD	138	W
Thermal Resistance, Junction to Case (Note 6)		R _{0JC}	0.9	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	—	-	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}		_	1	μA	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1	—	3	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance			_	2.5	mΩ	$V_{GS} = 10V, I_D = 50A$	
	R _{DS(ON)}		—	4	mΩ	$V_{GS} = 4.5V, I_D = 50A$	
Diode Forward Voltage	V _{SD}		0.9	1.2	V	$V_{GS} = 0V, I_{S} = 50A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		4508	—		$V_{DS} = 20V, V_{GS} = 0V,$ f = 1MHz	
Output Capacitance	Coss		1648	-	pF		
Reverse Transfer Capacitance	Crss		104	-			
Gate resistance	Rg		0.7	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	-	34.6	—	nC		
Total Gate Charge (V _{GS} = 10V)	Qg	_	82.2	—		$V_{DD} = 20V, I_D = 30A$	
Gate-Source Charge	Q _{gs}	_	9.9	—	nC		
Gate-Drain Charge	Q _{qd}	_	11.2	—			
Turn-On Delay Time	t _{D(ON)}	_	5.9	_		V _{DD} = 20V, V _{GS} = 10V,	
Turn-On Rise Time	t _R	_	13.3	_			
Turn-Off Delay Time	t _{D(OFF)}		25.9	—	ns	$I_D = 30A, R_G = 1.6\Omega$	
Turn-Off Fall Time	t _F	_	7.9	—			
Body Diode Reverse Recovery Time	t _{RR}	_	48.4	—	ns	L 500 di/dt 1000/0-	
Body Diode Reverse Recovery Charge	Q _{RR}	_	72.4	—	nC	— I _F = 50A, di/dt = 100A/μs	

5. Device mounted with exposed drain pad on 25mm by 25mm 2oz copper on a single- sided 1.6mm FR-4 PCB; device is measured under still air conditions Notes: b) Device mounted with exposed train pad on 25mm by 25mm 202 copper on a sill whilst operating in a steady state.
c) Thermal resistance from junction to soldering point (on the exposed drain pad).
7) Short duration pulse test used to minimize self-heating effect.
8) Guaranteed by design. Not subject to production testing.



DMT4004LPS

1,25°C

85°C

25°,C

3

4

-55°C

2

8

25

50

75

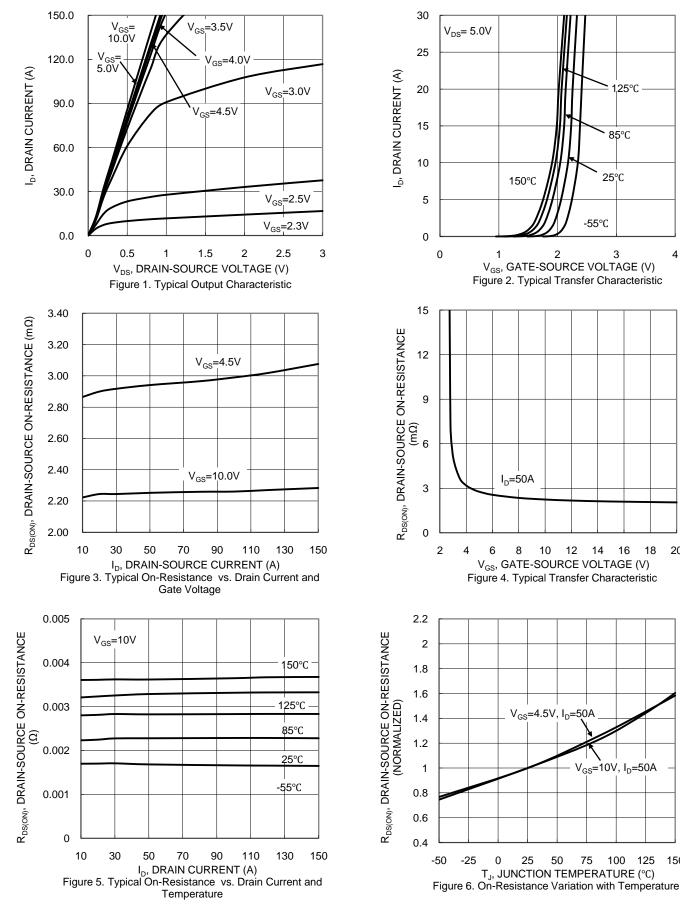
10 12 14

16 18

 V_{GS} =10V, I_{D} =50A

100 125

20

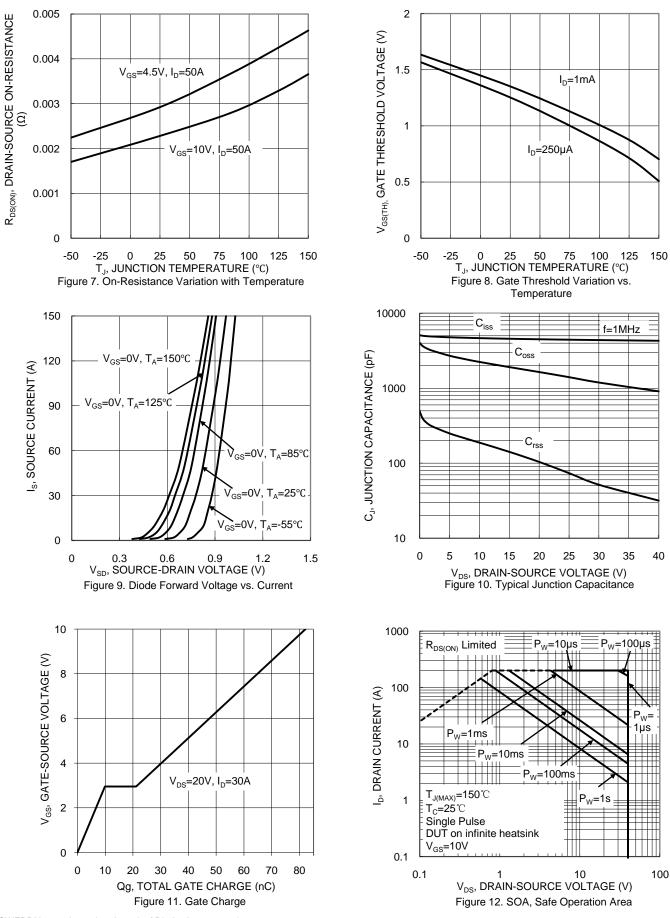


POWERDI is a registered trademark of Diodes Incorporated. DMT4004LPS Document number: DS37587 Rev.3 - 2

3 of 6 Downloaded From Oneyac.com 150



DMT4004LPS



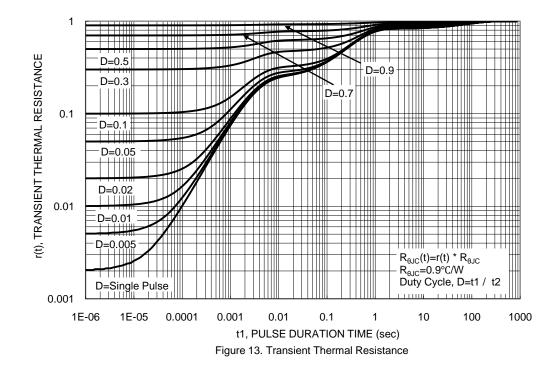
POWERDI is a registered trademark of Diodes Incorporated. DMT4004LPS Document number: DS37587 Rev.3 - 2

Downloaded From Oneyac.com

4 of 6

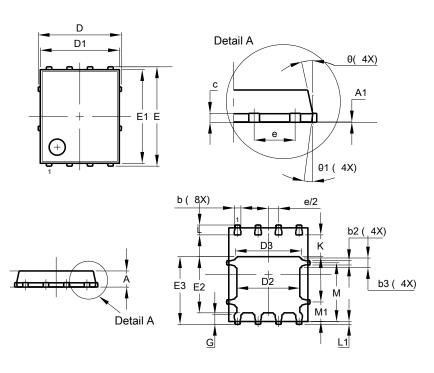
May 2016 © Diodes Incorporated





Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

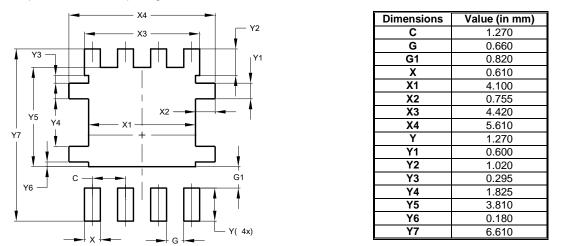


PowerDI5060-8					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0.00	0.05	-		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
c	0.230	0.330	0.277		
D	Į	5.15 BSC			
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
E	6.15 BSC				
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
e	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51	-	-		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
Μ	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
θ	10º	12º	11º		
θ1	6°	8º	7°		
All	All Dimensions in mm				



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

- 1. are intended to implant into the body, or
- 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2016, Diodes Incorporated

www.diodes.com

POWERDI is a registered trademark of Diodes Incorporated. DMT4004LPS Document number: DS37587 Rev.3 - 2 单击下面可查看定价,库存,交付和生命周期等信息

>>Diodes Incorporated(达迩科技(美台))