

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25 ℃
	$25m\Omega$ @ $V_{GS} = 4.5V$	9A
20V	$29m\Omega @ V_{GS} = 2.5V$	5.5A
	$37m\Omega @ V_{GS} = 1.8V$	4.8A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Power Management Functions
- DC-DC Converters

Features

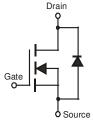
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

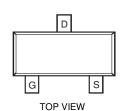
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)







Internal Schematic



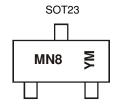
Ordering Information (Note 5)

Part Number	Case	Packaging
DMG3414UQ-7	SOT23	3,000/Tape & Reel
DMG3414UQ-13	SOT23	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



MN8 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2013)M = Month (ex: 9 = September)

Date Code Key

	Year	2009	2010	2011	2012	2013	2014	2015	2016	201	7 201	8 2019	2020	2021
	Code	W	Χ	Υ	Z	Α	В	С	D	E	F	G	Н	ı
	Month	Jan	Feb	Mar	Apr	May	Ju	n J	ul	Aug	Sep	Oct	Nov	Dec
Г	Code	1	2	2	1	5	6		7	0	٥	0	N	ם

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Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characte	ristic		Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current (Note 6)	T _A = +25 °C T _A = +70 °C	I _D	4.2 3.2	А	
Pulsed Drain Current (Note 7)			I _{DM}	30	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P_{D}	0.78	W
Thermal Resistance, Junction to Ambient @T _A = +25℃	$R_{\theta JA}$	162	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	℃

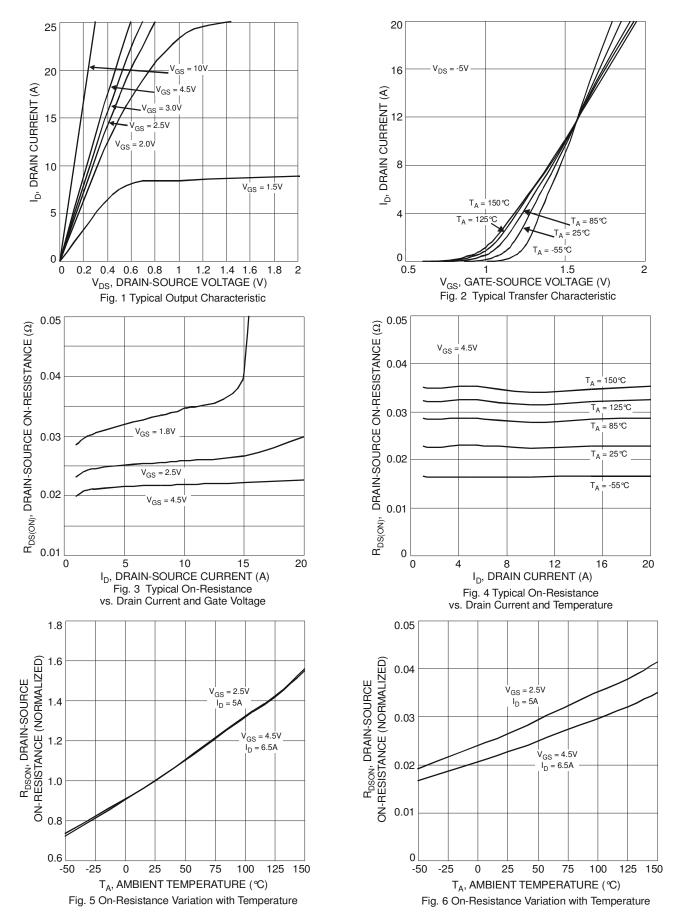
Electrical Characteristics (@T_A = +25 °C, unless otherwise specified.)

a.			_			
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	I _{DSS}	_	_	1.0	μΑ	$V_{DS} = 20V$, $V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(th)}$	0.5	_	0.9	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
			19	25		$V_{GS} = 4.5V, I_D = 8.2A$
Static Drain-Source On-Resistance	R _{DS (ON)}		22	29	mΩ	$V_{GS} = 2.5V, I_D = 3.3A$
			28	37		$V_{GS} = 1.8V, I_D = 2.0A$
Forward Transfer Admittance		_	7	_	S	$V_{DS} = 10V, I_{D} = 4A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance		_	829.9	_	pF	
Output Capacitance		_	85.3	_	pF	$V_{DS} = 10V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	81.2	_	pF	1 - 1.00012
Total Gate Charge	Qg	_	9.6	_	nC	
Gate-Source Charge	Qgs	_	1.5	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V, I_D = 8.2A$
Gate-Drain Charge		_	3.5	_	nC	
Turn-On Delay Time		_	8.1	_	ns	
Turn-On Rise Time		_	8.3		ns	$V_{DD} = 10V, V_{GS} = 4.5V,$
Turn-Off Delay Time	t _{D(off)}	_	40.1	_	ns	$R_L=10\Omega,R_G=6\Omega,I_D=1A$
Turn-Off Fall Time	t _f		9.6		ns	

- 6. Device mounted on FR-4 PCB with 2oz. Copper and test pulse width t ≤ 10s.
- Beyte mointed of 1-4 role with 202. Copper and less purchases.
 Repetitive rating, pulse width limited by junction temperature.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.

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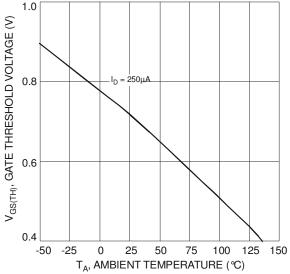
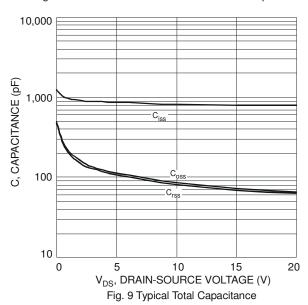
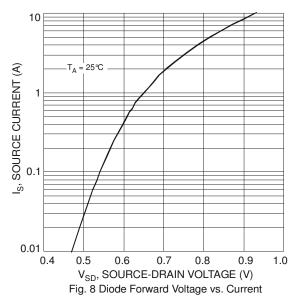


Fig. 7 Gate Threshold Variation vs. Ambient Temperature





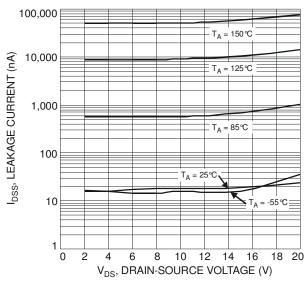


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

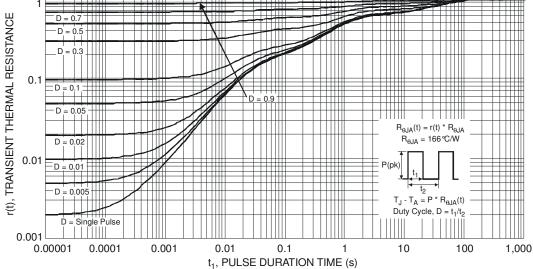
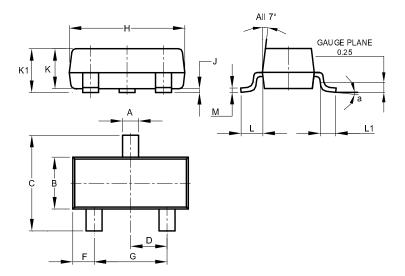


Fig. 11 Transient Thermal Response



Package Outline Dimensions

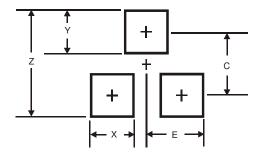
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085 0.150 0.110						
а	a 8°						
All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Υ	0.9
С	2.0
Е	1.35



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