



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVDSS	Rds(on)	I _D T _A = +25°C
20V	175mΩ @ $V_{GS} = 4.5V$	2.1A
	240mΩ @ V _{GS} = 2.5V	1.7A
	360mΩ @ V _{GS} = 1.8V	1.5A
	500mΩ @ V _{GS} = 1.5V	1.2A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Load Switch





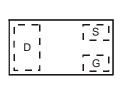
Bottom View

Features and Benefits

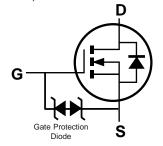
- Footprint of Just 0.6mm² Thirteen Times Smaller than SOT23
- 0.4mm Profile Ideal for Low Profile Applications
- Low Gate Threshold Voltage
- · Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: X2-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.001 grams (Approximate)



Top View Internal Schematic



Equivalent Circuit

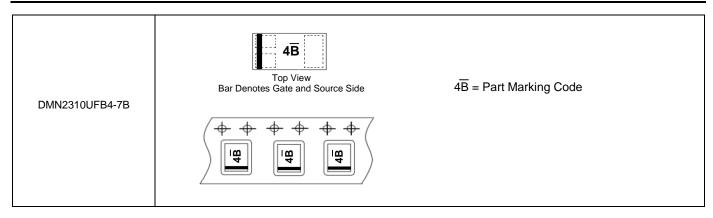
Ordering Information (Note 4)

Part Number	Marking	Reel Size (inches)	Tape Width (mm)	Tape Pitch (mm)	Quantity per Reel
DMN2310UFB4-7B	4B	7	8	2	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



DMN2310UFB4
Document number: DS42733 Rev. 2 - 2

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Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			VDSS	20	V
Gate-Source Voltage			V_{GSS}	±8	V
Continuous Prain Current (Note C) Vos. 4 EV	Steady	T _A = +25°C	l-	2.1	А
Continuous Drain Current (Note 6) V _{GS} = 4.5V	State	$T_A = +70$ °C	ID	1.6	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	4.7	Α		

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	0.71	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{0JA}	177	°C/W
Total Power Dissipation (Note 6)	PD	1.14	W
Thermal Resistance, Junction to Ambient (Note 6)	Reja	110	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

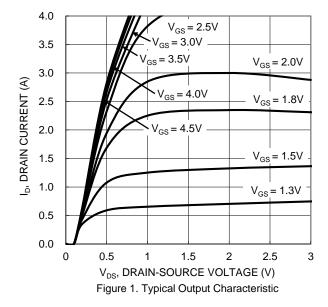
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

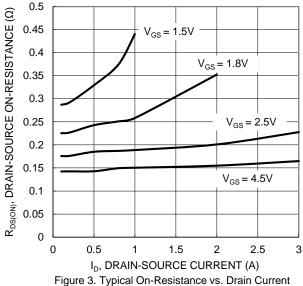
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	Oymboi	141111	iyp	Wax	Onic	rest containen	
Drain-Source Breakdown Voltage	BVDSS	20	_	_	V	$V_{GS} = 0V, I_{D} = 250 \mu A$	
Zero Gate Voltage Drain Current TJ = +25°C	IDSS	_	_	1	μA	V _{DS} = 20V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	_	10	μA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(TH)	0.45	_	0.95	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
		_	150	175	mΩ	V _G S = 4.5V, I _D = 1A	
Static Drain-Source On-Resistance	D	_	190	240		$V_{GS} = 2.5V, I_D = 750mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	-	250	360		$V_{GS} = 1.8V, I_{D} = 500mA$	
		_	295	500		V _G S = 1.5V, I _D = 200mA	
Diode Forward Voltage	V _{SD}	_	0.8	1.2	V	$V_{GS} = 0V, I_{S} = 300mA$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	38	_	pF	101/1/	
Output Capacitance	Coss	_	10	_	pF	$V_{DS} = 10V$, $V_{GS} = 0V$,	
Reverse Transfer Capacitance	C _{rss}	_	6	_	pF	f = 1.0MHz	
Total Gate Charge (V _{GS} = 4.5V)	Qg	-	0.7	_	nC	\\ 45\\\\ 40\\\	
Gate-Source Charge	Qgs	_	0.1	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$	
Gate-Drain Charge	Q _{gd}	_	0.1	_	nC	I _D = 1A	
Turn-On Delay Time	t _D (ON)	_	8	_	ns		
Turn-On Rise Time	t _R	_	138	_	ns	V _{DD} = 10V, V _{GS} = 5V,	
Turn-Off Delay Time	tD(OFF)	_	154	_	ns	$R_L = 1.7\Omega$, $R_G = 6\Omega$	
Turn-Off Fall Time	tF	_	180	_	ns	1	

Notes:

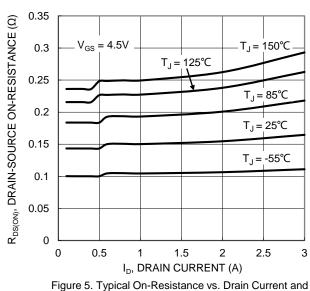
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 25mm X 25mm square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.







and Gate Voltage



Temperature

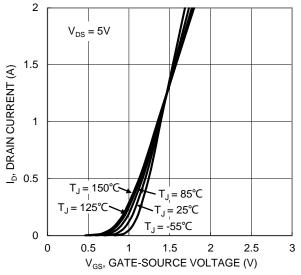


Figure 2. Typical Transfer Characteristic

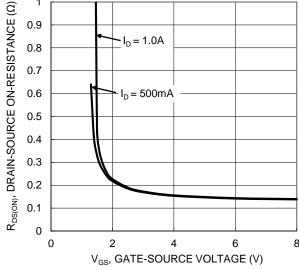


Figure 4. Typical Transfer Characteristic

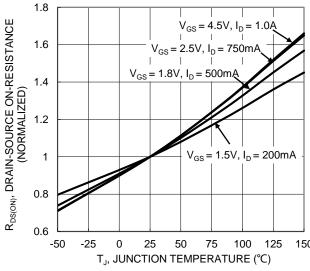


Figure 6. On-Resistance Variation with Temperature



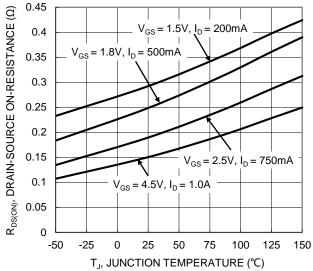


Figure 7. On-Resistance Variation with Temperature

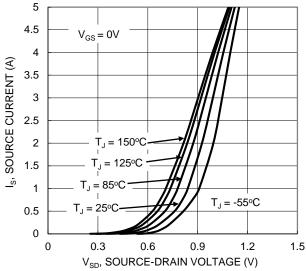
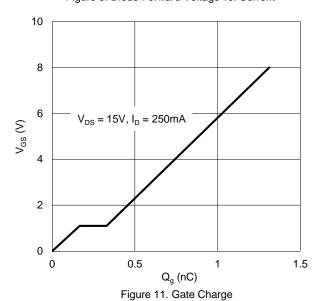
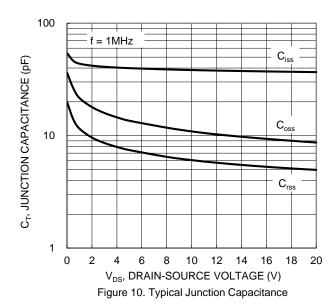


Figure 9. Diode Forward Voltage vs. Current



0.9 $V_{\text{GS(TH)}}, \text{ GATE THRESHOLD VOLTAGE (V)}$ 0.8 0.7 $I_D = 1mA$ 0.6 0.5 $I_{D} = 250 \mu A$ 0.4 0.3 0.2 0.1 0 -50 -25 0 25 50 75 100 125 150 T_.I, JUNCTION TEMPERATURE (°C)

Figure 8. Gate Threshold Variation vs. Junction Temperature



100 R_{DS(ON)} 10 DRAIN CURRENT (A) T_{J(Max)} = 150°C T_C = 25 °C 0.1 Single Pulse DUT on 1*MRP board $V_{GS} = 4.5V$ 0.01 0.1 10 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



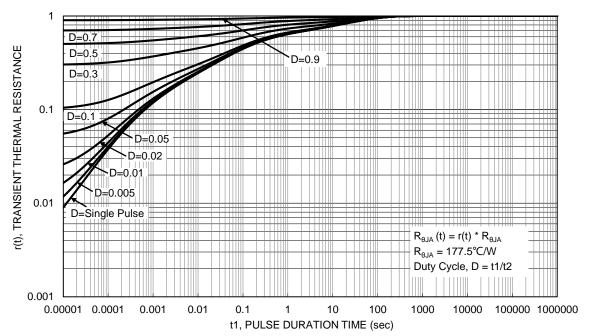


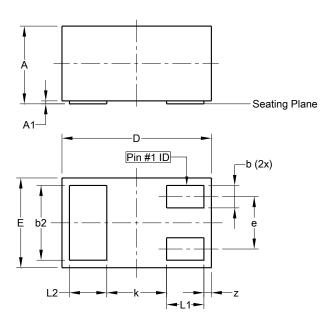
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN1006-3

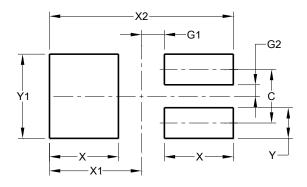


X2-DFN1006-3					
Dim	Min	Max	Тур		
Α	_	0.40	_		
A1	0.00	0.05	0.03		
b	0.10	0.20	0.15		
b2	0.45	0.55	0.50		
D	0.95	1.05	1.00		
Е	0.55	0.65	0.60		
е	_		0.35		
L1	0.20	0.30	0.25		
L2	0.20	0.30	0.25		
k	_	_	0.40		
Z	0.02	0.08	0.05		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN1006-3



Dimensions	Value (in mm)		
С	0.350		
G1	0.150		
G2	0.075		
Х	0.450		
X1	0.600		
X2	1.200		
Y	0.200		
Y1	0.550		

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