



DMN1150UFB

#### N-CHANNEL ENHANCEMENT MODE MOSFET

# **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on) max</sub>	<b>Ι</b> <sub>D</sub> T <sub>A</sub> = +25°C
	0.15Ω @ V <sub>GS</sub> = 4.5V	1.41A
12V	0.185Ω @ V <sub>GS</sub> = 2.5V	1.25A
	0.21Ω @ V <sub>GS</sub> = 1.8V	1.16A

## Description

This new generation MOSFET has been designed to minimize the onstate resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

- DC-DC Converters
- Power management functions

### Features

- Low On-Resistance
- Very Low Gate Threshold Voltage V<sub>GS(TH)</sub>, 1.0V max
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### Mechanical Data

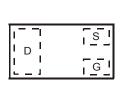
- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 <sup>64</sup>
- Weight: 0.001 grams (approximate)

X1-DFN1006-3

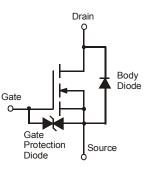




Bottom View



Top View Internal Schematic



Equivalent Circuit

### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN1150UFB-7B	X1-DFN1006-3	10,000/Tape & Reel

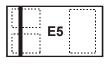
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and

<1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com.

# **Marking Information**



E5 = Product Type Marking Code Bar Denotes Gate and Source Side

Top View



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			VDSS	12	V
Gate-Source Voltage			V <sub>GSS</sub>	±6	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 4.5V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	1.41 1.15	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I <sub>DM</sub>	7	A
Maximum Body Diode continuous Current			ls	1	А

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Total Dower Dissinction (Note 5)	T <sub>A</sub> = +25°C	D	0.5	14/	
Total Power Dissipation (Note 5)	T <sub>A</sub> = +70°C	PD	0.3	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	$R_{\theta JA}$	251	°C/W	
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

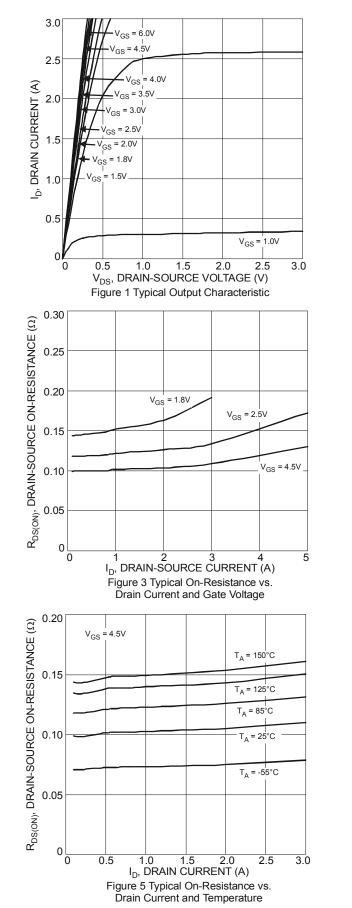
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

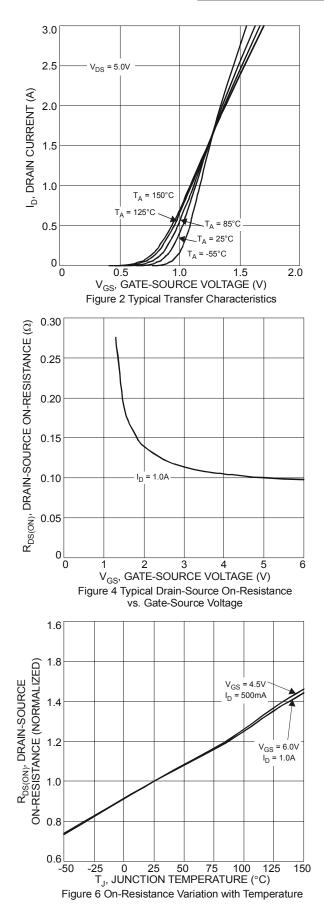
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	12	_		V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA	
Zero Gate Voltage Drain Current TJ = +25°C	I <sub>DSS</sub>	_	—	100	nA	$V_{DS} = 12V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS		_	±1	μA	$V_{GS} = \pm 6V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.35	—	1.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
			—	150		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 1A	
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>	—	_	185	mΩ	V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 1A	
			_	210		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 1A	
Diode Forward Voltage	V <sub>SD</sub>		0.7	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 150mA	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss		106		pF		
Output Capacitance	C <sub>oss</sub>		23		pF	V <sub>DS</sub> =10V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	Crss	-	21		pF		
Gate resistance	R <sub>g</sub>		92.4		Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	-	1.5		nC		
Gate-Source Charge	Q <sub>gs</sub>	-	0.2		nC	$V_{DS} = 4V, I_D = 1A$	
Gate-Drain Charge	Q <sub>gd</sub>		0.2		nC		
Turn-On Delay Time	t <sub>D(on)</sub>		4.1		ns		
Turn-On Rise Time	tr		34.5		ns	$V_{DD} = 4V, V_{GS} = 6V, I_D = 1A$	
Turn-Off Delay Time	t <sub>D(off)</sub>	_	57		ns	R <sub>G</sub> = 1Ω	
Turn-Off Fall Time	t <sub>f</sub>		30		ns		

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing. Notes:



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NEW PRODUCT



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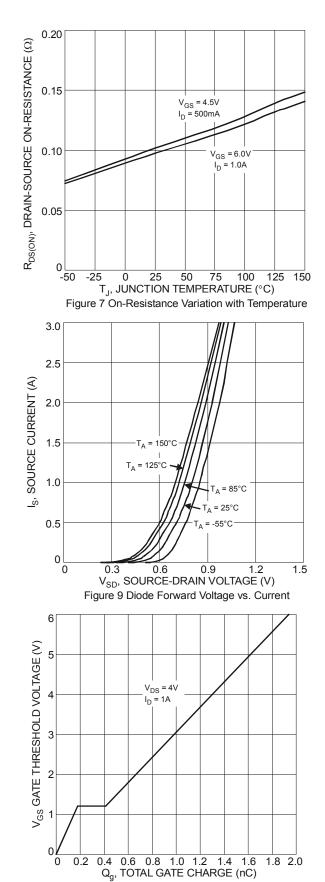
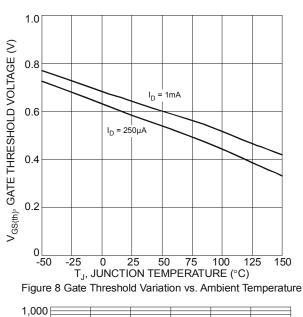
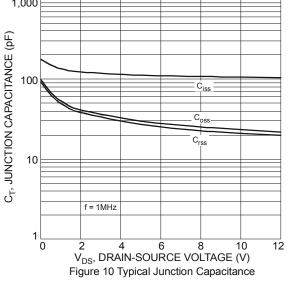
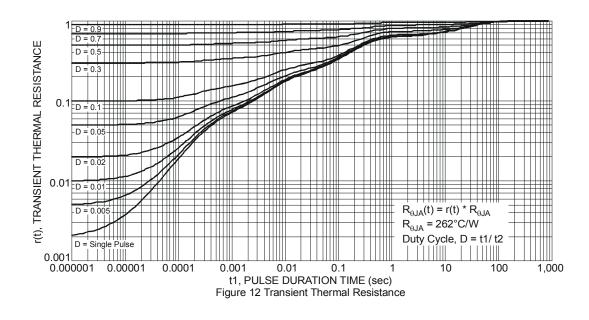


Figure 11 Gate Charge



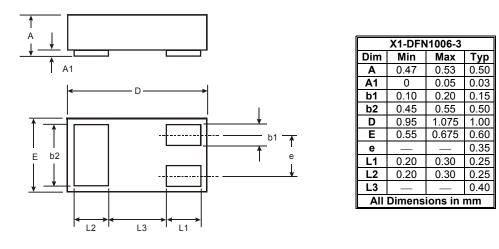






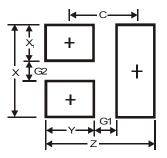
# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



# Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
Х	0.7
X1	0.25
Y	0.4
С	0.7



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