

#### 74HC86

#### **QUADRUPLE 2-INPUT EXCULSIVE OR GATES**

#### Description

The 74HC86 provides provides four independent 2-input Exclusive OR gates with standard push-pull outputs. The device is designed for operation with a power supply range of 2.0V to 6.0V.

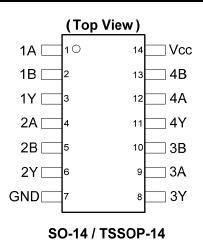
The gates perform the Boolean function:

$$Y = A \oplus B$$
 or  $Y = \overline{A}B + A\overline{B}$ 

#### **Features**

- Wide Supply Voltage Range from 2.0V to 6.0V
- Sinks or sources 4mA at V<sub>CC</sub> = 4.5V
- CMOS low power consumption
- Schmitt Trigger Action at All Inputs
- ESD Protection Exceeds JESD 22
  - 200-V Machine Model (A115-A)
    - 2000-V Human Body Model (A114-A)
  - Exceeds 1000-V Charged Device Model (C101C)
- Range of Package Options SO-14 and TSSOP-14
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### Pin Assignments



# Applications

- General Purpose Logic
- Wide array of products such as:
  - PCs, Networking, Notebooks, Netbooks
  - Computer Peripherals, Hard Drives, CD/DVD ROM
  - TV, DVD, DVR, Set Top Box

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

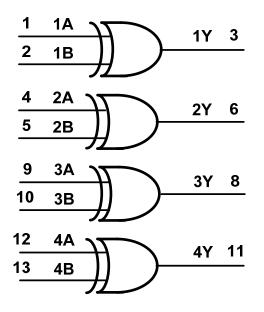
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



# **Pin Descriptions**

Pin Number	Pin Name	Function
1	1A	Data Input
2	1B	Data Input
3	1Y	Data Output
4	2A	Data Input
5	2B	Data Input
6	2Y	Data Output
7	GND	Ground
8	3Y	Data Output
9	3A	Data Input
10	3B	Data Input
11	4Y	Data Output
12	4A	Data Input
13	4B	Data Input
14	V <sub>CC</sub>	Supply Voltage

# Logic Diagram



## **Function Table**

Inp	Output	
Α	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L



## **Absolute Maximum Ratings** (Note 4) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit	
ESD HBM	Human Body Model ESD Protection	2	KV	
ESD CDM	Charged Device Model ESD Protection	1	KV	
ESD MM	Machine Model ESD Protection	200	V	
V <sub>CC</sub>	Supply Voltage Range	-0.5 to +7.0	V	
VI	Input Voltage Range (Note 5)	-0.5 to +7.0	V	
Input Clamp Current VI < -0.5V or Vi > V <sub>CC</sub> +0.5V		±20	mA	
$I_{OK}$ Output Clamp Current $V_O < -0.5V$ or $V_O > V_{CC} + 0.5V$		±20	mA	
$I_0$ Continuous output current -0.5V < V <sub>0</sub> V <sub>CC</sub> +0.5V		+/- 25	mA	
Icc	Continuous Current Through Vcc	50	mA	
I <sub>GND</sub> Continuous Current Through GND		-50	mA	
T <sub>J</sub> Operating Junction Temperature		-40 to +150	°C	
T <sub>STG</sub> Storage Temperature		-65 to +150	°C	
PTOT Total Power Dissipation		500	mW	

Notes: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

5. Input Voltage cannot exceed  $V_{\text{CC}}$  to the extent the Maximum clamp current is exceeded.

## Recommended Operating Conditions (Note 6) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
Vcc	Supply Voltage		2.0	6.0	V
VI	Input Voltage		0	V <sub>CC</sub>	V
Vo	Output Voltage		0	V <sub>CC</sub>	V
	V <sub>CC</sub> = 2.0V		625		
Δt/ΔV	Δt/ΔV Input Transition Rise or Fall Rate	V <sub>CC</sub> = 4.5V		140	ns/V
		V <sub>CC</sub> = 6.0V		85	
T <sub>A</sub>	Operating Free-Air Temperature		-40	+125	°C

Note: 6. Unused inputs should be held at  $V_{CC}$  or Ground.



## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Sumbal	Devementer	Test Conditions	N	T <sub>A</sub> = -40°0	C to +85°C	T <sub>A</sub> = -40°C	to +125°C	L Init
Symbol	Parameter	Test Conditions	Vcc	Min	Max	Min	Мах	Unit
			2.0V	1.5		1.5		
VIH	High-level Input Voltage		4.5V	3.15		3.15		V
			6.0V	4.2		4.2		
			2.0V		0.5		0.5	
VIL	Low-level input voltage		4.5V		1.35		1.35	V
			6.0V		1.8		1.8	
		I <sub>OH</sub> = -20μA	2.0V	1.9		1.9		
		I <sub>OH</sub> = -20μA	4.5V	4.4		4.4		v
Voh	High-level Output Voltage	I <sub>OH</sub> = -20μA	6.0V	5.9		5.9		
	Vollage	I <sub>OH</sub> = -4.0mA	4.5V	3.84		3.7		
		I <sub>OH</sub> = -5.2mA	6.0V	5.34		5.2		
		I <sub>OL</sub> = 20μA	2.0V		0.1		0.1	
		I <sub>OL</sub> = 20μA	4.5V		0.1		0.1	
V <sub>OL</sub>	Low-level Output Voltage	I <sub>OL</sub> = 20μA	6.0V		0.1		0.1	V
	Vollage	I <sub>OL</sub> = 4mA	4.5V		0.33		0.44	1
		I <sub>OL</sub> = 5.2mA	6.0V		0.33		0.44	
h	Input Current	V <sub>I</sub> =GND to 5.5V	6.0V		± 1		± 1	μA
Icc	Supply Current	$V_{I} = GND \text{ or } V_{CC}, I_{O} = 0$	6.0V		20		40	μA

# **Switching Characteristics**

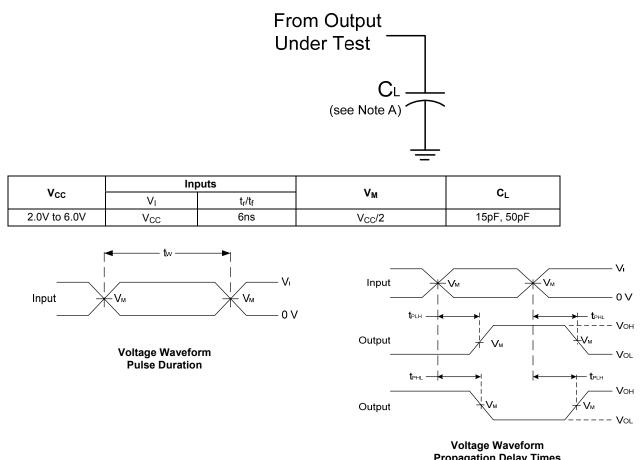
Symbol	Parameter	Test Conditions	N	-	T <sub>A</sub> = +25°C		-40°C to +85°C	-40°C to +125°C	Unit
			Conditions V <sub>CC</sub>	Min	Тур.	Max	Max	Max	Unit
t <sub>PD</sub> Propagation Delay A <sub>N</sub> to Y <sub>N</sub>	Figure 1 C <sub>L</sub> = 50pF	2.0V	_	25	90	115	135		
		4.5V	_	9	18	23	27	ns	
		6.0V	_	7	15	20	23		
		Eisense 4	2.0V	_	19	75	95	110	
t <sub>t</sub> Transition time	Transition time	time Figure 1 C <sub>L</sub> = 50pF	4.5V	_	7	15	19	22	ns
	CL = SOPE	6.0V	_	6	13	16	19		

## Operating Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Parameter		Test Conditions	V <sub>CC</sub> = 6V Typ	Unit
C <sub>pd</sub>	Power Dissipation Capacitance per Gate	f = 1MHz	25	pF
CI	Input Capacitance	$V_1 = V_{CC} - or GND$	4	pF



### **Parameter Measurement Information**



Propagation Delay Times Inverting and Non Inverting Outputs

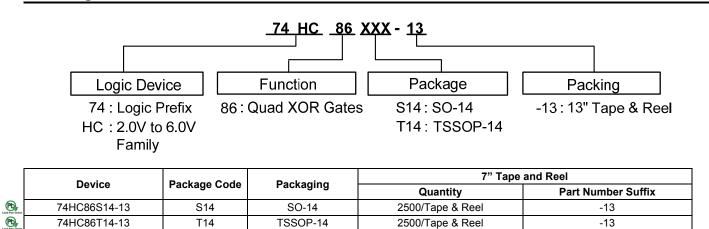
Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.C. Inputs are measured separately one transition per measurement.
- D.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{PD}$ .

Figure 1 Load Circuit and Voltage Waveforms

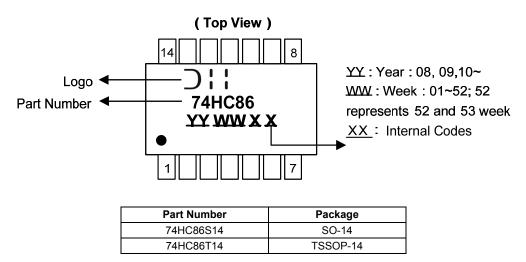


## **Ordering Information**



### **Marking Information**

(1) SO-14, TSSOP-14

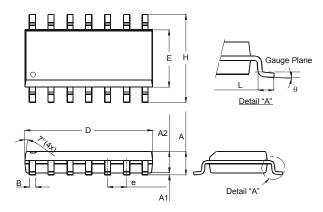




# Package Outline Dimensions (All dimensions in mm.)

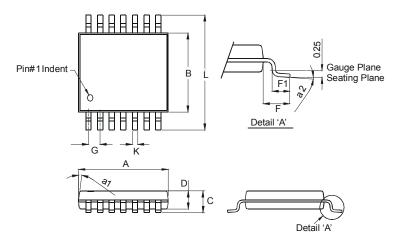
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

#### Package Type: SO-14



	SO-14					
Dim	Min	Max				
Α	1.47	1.73				
A1	0.10	0.25				
A2	1.45	Тур				
В	0.33	0.51				
D	8.53	8.74				
ш	3.80	3.99				
e	1.27	Тур				
Н	5.80	6.20				
Ц	0.38	1.27				
θ	0°	8°				
All Di	mension	s in mm				

Package Type: TSSOP-14



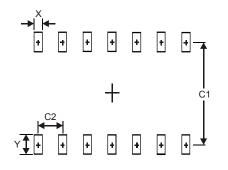
	TSSOP-14				
Dim	Min Max				
a1	7° (	4X)			
a2	0°	8°			
Α	4.9	5.10			
в	4.30 4.50				
С	_	1.2			
D	0.8	1.05			
F	1.00	Тур			
F1	0.45	0.75			
G	0.65	Тур			
ĸ	0.19 0.30				
L	L 6.40 Typ				
All Dir	nensions	s in mm			



# Suggested Pad Layout

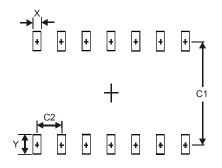
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.

#### Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Y	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Y	1.45
C1	5.9
C2	0.65



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