



A Product Line of Diodes Incorporated



30V N-CHANNEL ENHANCEMENT MODE MOSFET IN SOT89 PACKAGE

Product Summary

V _(BR) dss	R _{DS(on)} Max	I _D max T _A = 25°C (Note 5)
2014	$120m\Omega @ V_{GS} = 10V$	3.3A
30V	180mΩ @ V_{GS} = 4.5V	2.7A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(on)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

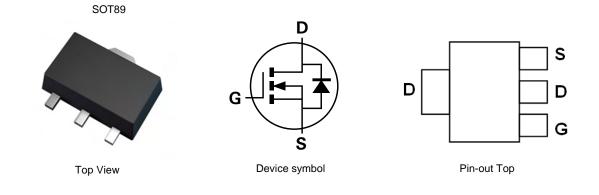
- DC-DC Converters
- Power Management functions
- Motor control

Features and Benefits

- Low On-Resistance
- Low Threshold
- Fast Switching Speed
- Low Gate Drive
- Lead Free/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT89
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish
- Weight: 0.052 grams (approximate)



Ordering Information (Note 3)

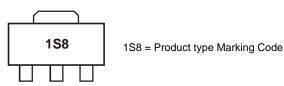
Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMN3A01ZTA	1S8	7	12	1,000

Notes: 1. No purposefully added lead.

2. Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com

3. For packaging details, go to our website at http://www.diodes.com

Marking Information







ZXMN3A01Z

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current	Steady State	@ $V_{GS} = 10V$; $T_A = 25^{\circ}C$ (Note 5) @ $V_{GS} = 10V$; $T_A = 75^{\circ}C$ (Note 5) @ $V_{GS} = 10V$; $T_A = 75^{\circ}C$ (Note 4)	Ι _D	3.3 2.7 2.2	А
Pulsed Drain Current (Note 6)			I _{DM}	20	A
Continuous Source Current (Body Diode) (Note 5)			Is	3.3	A
Pulsed Source Current (Body Diode) (Note 6)		I _{SM}	20	A	

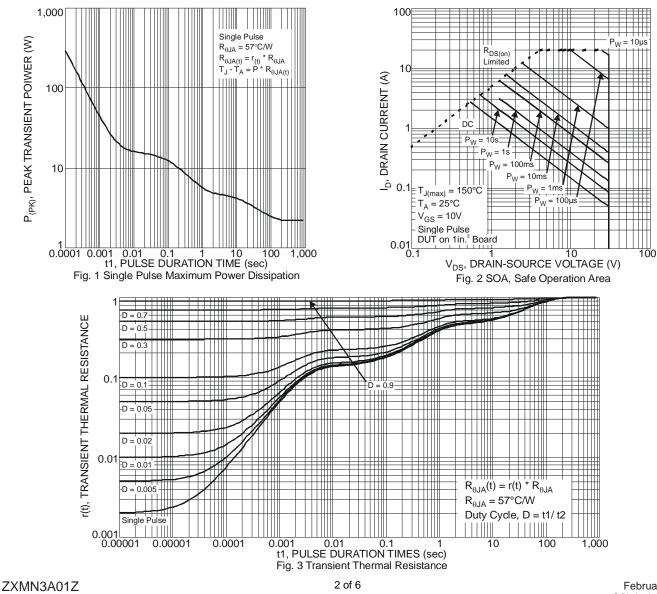
Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 4)	P	0.97	W
Power Dissipation	(Note 5)	PD	2.12	W
Thermal Resistance, Junction to Ambient	(Note 4)		129	°C/W
	(Note 5)	R _{θJA}	59	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Notes:

4. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout 5. Device mounted on 25mm X 25mm FR-4 substrate PC board with 2oz copper

6. Single pulse rating - 25mm x 25mm FR4 PCB, D=0.02, pulse width 300us - pulse width limited by maximum junction temperature.







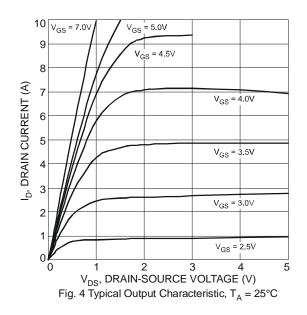
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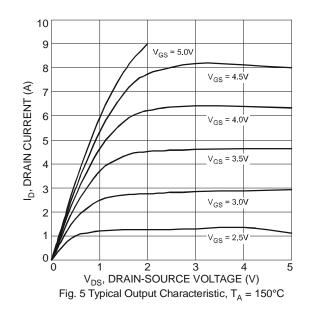
Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Cumpleal	Min	T	Max	11	Toot Condition
Characteristic OFF CHARACTERISTICS	Symbol	Min	Тур	Max	Unit	Test Condition
	51	00			14	
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current $T_J = 25^{\circ}C$	I _{DSS}	-	-	0.5	μΑ	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	-	-	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS					-	
Gate Threshold Voltage	V _{GS(th)}	1	-	-	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Quality During Que Daviate and (Nucles 7)			0.106	120		$V_{GS} = 10V, I_{D} = 2.5A$
Static Drain-Source On-Resistance (Note 7)	R _{DS} (ON)	-	-	180	mΩ	$V_{GS} = 4.5V, I_D = 2A$
Forward Transconductance (Note 7 & 9)	g fs	-	3.5	-	S	$V_{DS} = 4.5V, I_D = 2.5A$
Diodes Forward Voltage (Note 7)	V _{SD}	-	0.85	0.95	V	$T_J = 25^{\circ}C, I_S = 1.7A, V_{GS} = 0V$
DYNAMIC CHARACTERISTICS						
Input Capacitance (Note 8 & 9)	Ciss	-	186	-	pF	
Output Capacitance (Note 8 & 9)	Coss	-	48	-	pF	− V _{DS} = 25V, V _{GS} = 0V, − f = 1.0MHz
Reverse Transfer Capacitance (Note 8 & 9)	C _{rss}	-	29	-	pF	1 = 1.00012
Gate Charge (Note 8 & 9)	Qg	-	2.6	-	nC	$V_{GS} = 4.5V, V_{DS} = 15V, I_D = 2.5A$
Total Gate Charge (Note 8 & 9)	Qg	-	5.0	-	nC	
Gate-Source Charge (Note 8 & 9)	Q _{gs}	-	0.8	-	nC	$V_{GS} = 10V, V_{DS} = 15V,$
Gate-Drain Charge (Note 8 & 9)	Q _{gd}	-	1.2	-	nC	$-I_{\rm D} = 2.5 {\rm A}$
Reverse Recovery Time (Note 9)	t _{rr}		17.7		ns	T _J = 25°C, I _S = 2.5A,
Reverse Recovery Charge (Note 9)	Q _{rr}		13.0		nC	di/dt = 100A/µs
Turn-On Delay Time (Note 8 & 9)	t _{D(on)}	-	2.6	-	ns	
Turn-On Rise Time (Note 8 & 9)	tr	-	4.1	-	ns	$V_{GS} = 10V, V_{DD} = 15V,$
Turn-Off Delay Time (Note 8 & 9)	t _{D(off)}	-	13.5	-	ns	$R_G = 6\Omega$, $I_D = 2.5A$
Turn-Off Fall Time (Note 8 & 9)	t _f	-	3.6	-	ns	

Notes:

Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤2%.
Switching characteristics are independent of operating junction temperature.
For design aid only, not subject to production testing.

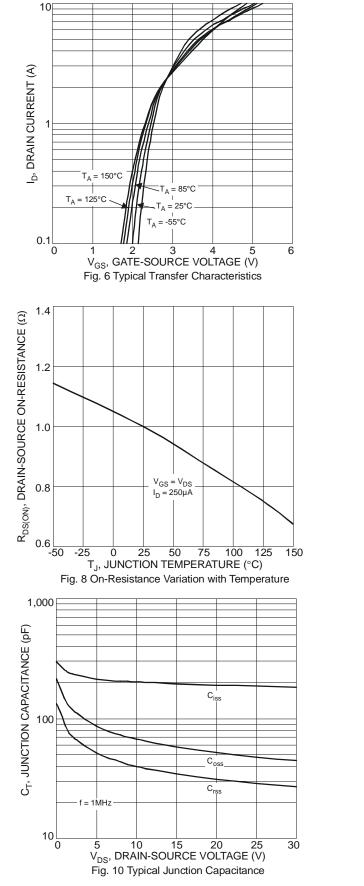


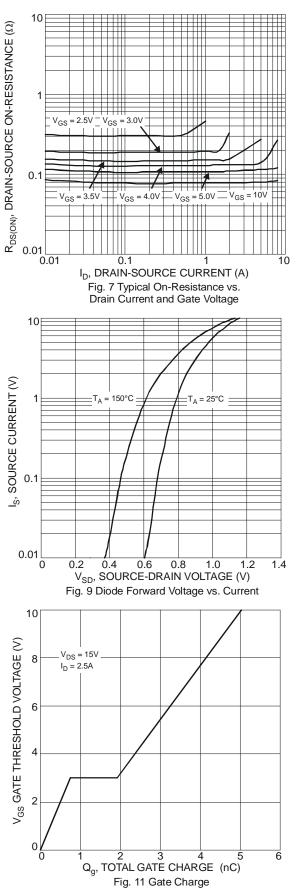




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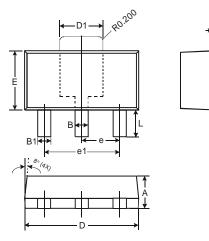
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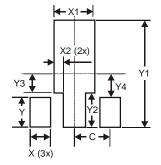
Package Outline Dimensions



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SOT89				
Dim	Min	Max		
Α	1.40	1.60		
В	0.44	0.62		
B1	0.35	0.54		
С	0.35	0.43		
D	4.40	4.60		
D1	1.52	1.83		
ш	2.29	2.60		
e	1.50 Typ			
e1	3.00 Typ			
Н	3.94 4.25			
L	0.89	1.20		
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)			
Х	0.900			
X1	1.733			
X2	0.416			
Y	1.300			
Y1	4.600			
Y2	1.475			
Y3	0.950			
Y4	1.125			
С	1.500			



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