

Ultra Low On-Resistance Dual, SPDT Analog Switch

Features

Switch Type: SPDT(2X)

Voltage Operation: 1.8V to 4.3V

Ultra-Low On Resistance: 0.75Ω @ +4.3V

-3dB Bandwidth: 75MHz

High Off-isolation: -78dB@100kHz

Low Crosstalk: -100dB@100kHz

Excellent On Resistance Matching: 0.03Ω

Low Total Harmonic Distortion (THD)

Rail-to-Rail Input and Output Operation

Break-Before-Make Switching

Green Packaged:DFN3*3-10 and MSOP-10

8kV HBM ESD

Applications

- Cell-Phone/PDA
- MP3/MP4/PMP
- Portable Instrumentation
- Battery Powered Communications
- Computer Peripherals

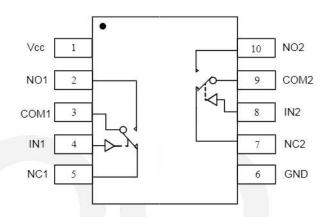
Descriptions

The DIO1005 is a dual Single-Pole, Double-Throw (SPDT) analog switch. DIO1005 operates from a single 1.8V to 4.3V supply and features an ultra-low on resistance of 0.75Ω at a +4.3V supply and $T_A = 25^{\circ}$ C. This device is fabricated with sub-micron CMOS technology to achieve fast switching speeds and is designed for break-before-make operation.

DIO1005 guarantees 0.03Ω on-resistance matching between switches, on-resistance flatness over the signal range, high off-isolation and low crosstalk, which ensures excellent linearity and low distortion when switching audio signals. DIO1005 consists of two normally open and two normally close switches.

DIO1005 provides packages with Green DFN3*3-10 and MSOP-10.

Block Diagram



Ordering Information

Order Part Number	Top Marking		T _A	Package			
DIO1005DN10	D1005	Green	-40 to +85°C	DFN-10	Tape & Reel, 5000		
DIO1005MP10	DIO1005	Green	-40 to +85°C	MSOP-10	Tape & Reel, 3000		



Pin Assignment

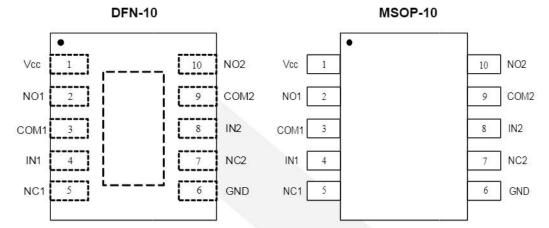


Figure 1 Top View

Pin Descriptions

Pin Name	Description
V _{co} /GND	Power Supply
IN1, IN2	Digital control pin to connect the COM terminal to the NO or NC terminals
COM1, COM2	Common terminal
NO1, NO2	Normally-open terminal
NC1, NC2	Normally-closed terminal

Truth Table

IN1, IN2	NO	NC
L	OFF	ON
Н	ON	OFF



Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Rating" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maxim rating conditions for extended periods may affect device reliability.

Symbol	Parameter	Min.	Max.	Unit	
Vcc	Supply Voltage	-0.3	+4.6	V	
Vcntrl	DC input Voltage	-0.3	(V _{CC}) + 0.3	٧	
V_{SW}	DC input I/O Voltage	-0.3	(V _{CC}) + 0.3	V	
I _{IK}	DC input Diode current		-50	mA	
T _{STG}	Storage Temperature	-65	+150	°C	
ESD	HBM, JEDEC: JESD22-A114	V	8000	٧	

Recommend Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended Operating conditions are specified to ensure optimal performance to the datasheet specifications. DIOO does not Recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit	
Vcc	Supply voltage	1.8	4.3	V	
V _{CNTRL}	Control input voltage (IN1/IN2)	0	Vcc	V	
Vsw	Switch I/O voltage	0	Vcc	V	
TA	Operating Temperature	-40	85	°C	



Electrical Characteristics

All typical value are at V_{CC} =4.3V, GND=0V, V_{IH} =1.6V, V_{IL} =0.5V, T_A =25 $^{\circ}$ C unless otherwise specified.

Symbol	Parameter	Condit	tions	V _{cc} /V	Temp	Min	Тур	Max	Unit
Analog Sv	vitch Characteristics								
ь	On-Resistance	V _{NO} , V _{NC} , or V _{COM} =1V, I _{COM} = -100mA		4.3	+25°C		0.75	0.85	Ω
R _{ON}					-40 to 85°C			0.95	Ω
45	On-ResistanceMatch	V _{NO} , V _{NC} or V _{COM} =1V, I _{COM} = -100mA		4.3	+25°C		0.03	0.15	Ω
ΔR_{ON}	Between Channels				-40 to 85°C			0.20	Ω
		V _{NO} , V _{NC} or V _{COM} =1V, 2.5V		4.2	+25°C		0.15	0.23	Ω
R _{FLAT(ON)}	On-Resistance Flatness	I _{COM} = -100mA		4.3	-40 to 85°C			0.30	Ω
I _{NC(OFF),}	Source OFF Leakage	V _{NO} , V _{NC} =3.3V,	0.3V	4.3	-40 to 85°C			50	- ^
I _{NO(OFF)}	Current	V _{COM} = 0.3V/3.3	BV	4.3	-40 to 65 C			50	nA
I _{NO(ON)} , I _{NO(ON)} I _{COM(ON)}	Channel ON Leakage Current	V_{NO} , V_{NC} =3.3V, 0.3V or floating V_{COM} = 0.3V/3.3V		4.3	-40 to 85°C			50	nA
Digital Inp	uts			Li.			1		
V _{INH}	Input High Voltage				-40 to 85°C	1.6			V
V _{INL}	Input Low Voltage				-40 to 85°C			0.5	٧
I _{IN}	Input Leakage Current	V _{CC} =4.3V, V _{IN} =0V, or 4.2V			-40 to 85°C			1	μΑ
Dynamic 0	Characteristics		//						
t _{ON}	Turn-On Time	V_{IN} =2.1V to 0V, R_L =50 Ω , C_L =35pF, V_{NO1} or V_{NO2} or V_{NC2} =2.1V,			+25°C		25		ns
t _{OFF}	Turn-Off Time	V_{IN} =2.1V to 0V, R_L =50 Ω , C_L =35pF, V_{NO1} or V_{NO2} or V_{NC2} =2.1V.			+25°C		35		ns
to	Break-Before-Make Time Delay	$V_{IN=2.1V}$ to 0V, R_L =50 Ω , C_L =35pF, V_{NO1} or V_{NO2} or V_{NO2} =2.1V,			+25°C		45		ns
	Off Isolation	V _{BIAS} =2.1V, 100kHz Signal=0dBm 1MHz			+25°C	- //	-78	1	dB
O _{ISO}							-58		
v	Channel-to-Channel Crosstalk	V _{BIAS} =2.1V,	100kHz		+25°C -	l,	-100		- n=
X _{TALK}		Signal=0dBm	1MHz				-75	1	dB
BW	-3dB Bandwidth	V _{BIAS} =2.1V, Signal=0dBm			+25°C		75		MHz
THD	Total Harmonic Distortion	f=20Hz to 20kHz, RL=32Ω, V _{SW} =1V _{PP}			+25°C		0.02		%
Q	Charge Injection Select Input to Common I/O	V _G =0V, R _S =0 Ω, C _L =1.0nF			+25°C		4.0		рС
C _{ON}	Channel on Capacitance				+25°C		106		pF



Electrical Characteristics

All typical value are at V_{CC} =4.3V, GND=0V, V_{IH} =1.6V, V_{IL} =0.5V, T_A =25 $^{\circ}$ C unless otherwise specified.

Symbol	Parameter	Conditions	V _{cc} /V	Temp	Min	Тур	Max	Unit
Power R	equirements			I.				
Vcc	Power Supply Range			-40 to 85°C	1.8		4.3	٧
Icc	Quiescent Supply Current	V _{IN} =0V or V _{CC}	4.3	-40 to 85°C			500	nA
CCT	Increase in I _{CC} per	Input at 2.6∨		-40 to 85°C			5	Haller & C
	Input	Input at 1.8V	4.3				10	μA



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Test Diagrams

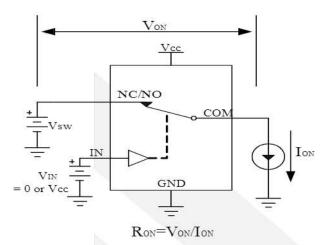


Figure 2 Switch on resistor

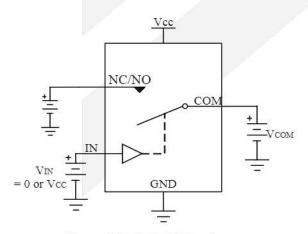


Figure 3 Switch Off Leakage

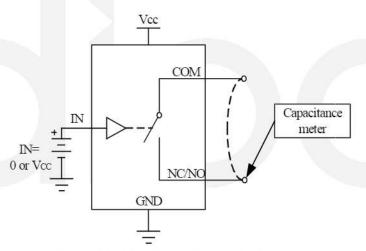


Figure 4 On/off Capacitance test



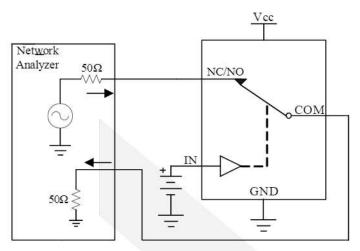


Figure 5 Bandwidth

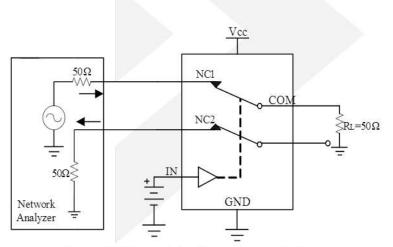


Figure 6 Channel-to-channel crosstalk

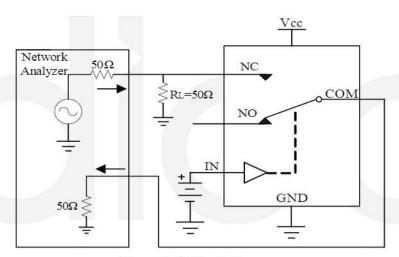


Figure 7 Off-isolation



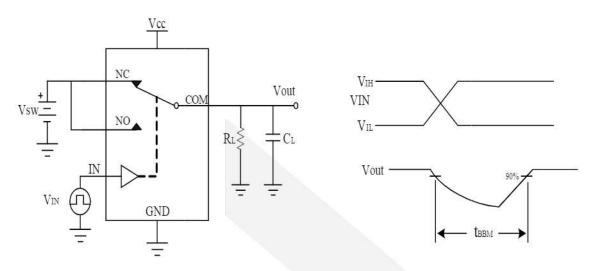


Figure 8 Break-Before-Make

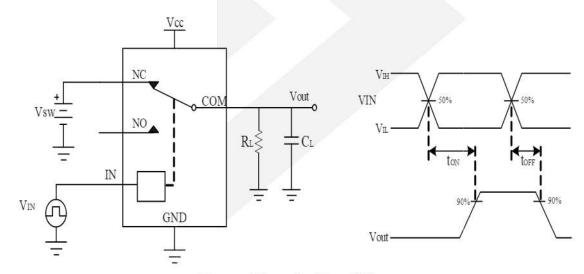


Figure 9 Turn-On/Turn-Off



CONTACT US

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