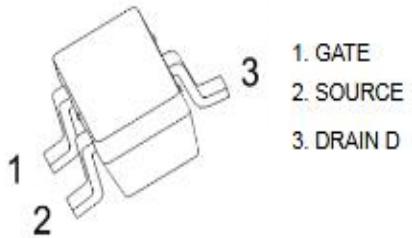
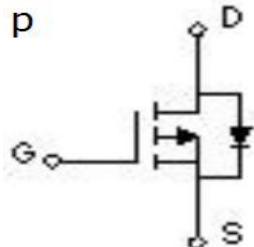


P-Channel 30-V(D-S) MOSFET	SOT-23-3L Plastic-Encapsulate MOSFETS
<p><u>SOT-23-3L</u></p>  <p>Equivalent Circuit</p> 	<p>Features</p> <ul style="list-style-type: none"> ※ TrenchFET Power MOSFET <p>Application</p> <ul style="list-style-type: none"> ※ Load Switch for Portable Devices ※ DC/DC Converter <p>MARKING</p> 

V(BR)DSS	RDS(on)MAX	ID
-30 V	60m Ω @ -10V 68m Ω @ -4.5V 90m Ω @ -2.5V	-4.1A

Maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	VDS	-30	V
Gate-Source Voltage	VGS	±12	
Continuous Drain Current	ID	-4.1	A
Pulsed Diode Current	IDM	-15	
Continuous Source-Drain Current(Diode Conduction)	IS	-0.8	
Power Dissipation	PD	0.35	W
Thermal Resistance from Junction to Ambient (t≤5s)	R θ JA	150	°C/W
Operating Junction	TJ	150	°C
Storage Temperature	TSTG	-55~+150	°C

MOSFET ELECTRICAL CHARACTERISTICS

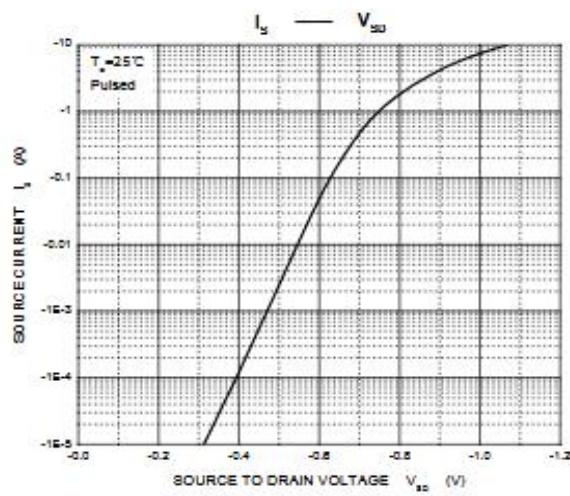
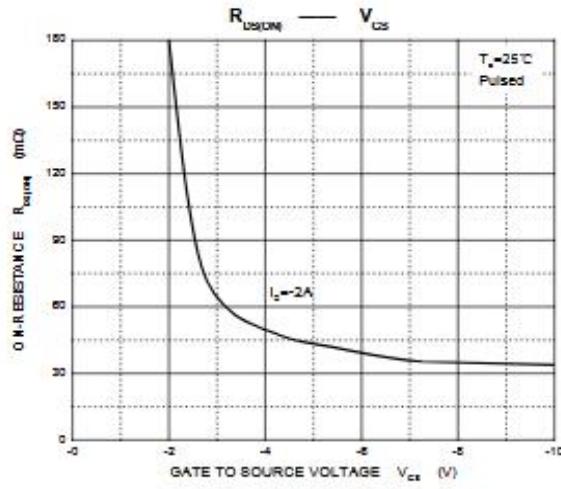
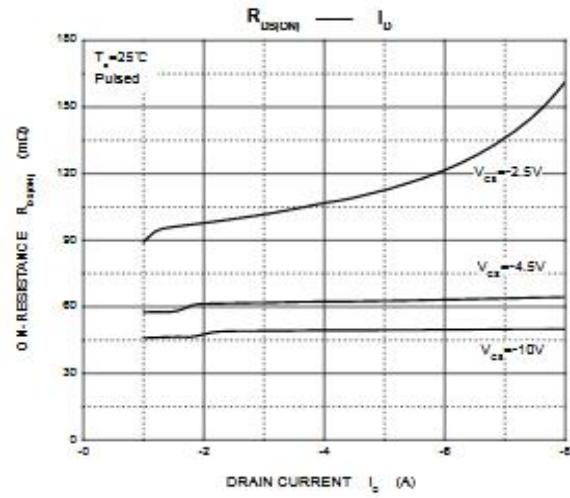
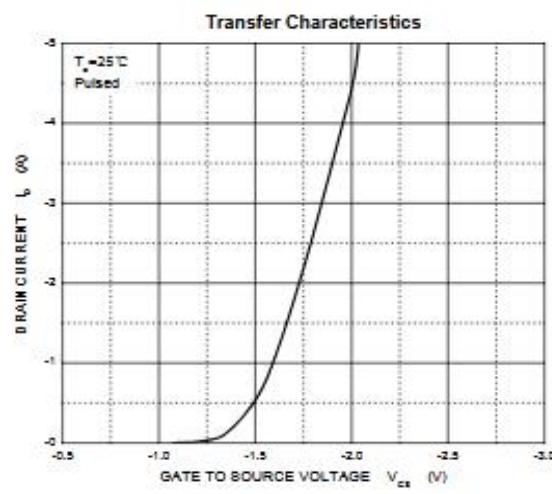
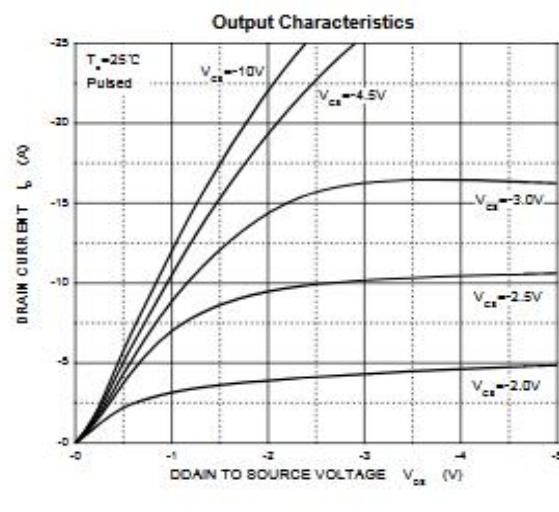
Static Electrical Characteristics ($T_a = 25^\circ C$ Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Drain-source breakdown voltage	V(BRD)SS	$V_{GS} = 0V, ID = -250\mu A$	-30			V
Gate-source threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, ID = -250\mu A$	-0.6	-0.87	-1.2	V
Gate-source leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 12V$		2.1/-0.76	± 100	nA
Zero gate voltage drain current	I_{DSS}	$V_{DS} = -30V, V_{GS} = 0V$			-1	μA
Drain-source on-state resistancea	RDS(on)	$V_{GS} = -10V, ID = -0.5A$		47.35	60	$m\Omega$
		$V_{GS} = -4.5V, ID = -0.5A$		60.06	68	$m\Omega$
		$V_{GS} = -2.5V, ID = -0.5A$		82.17	90	$m\Omega$
Forward transconductancea	g_{fs}	$V_{DS} = -4.5V, ID = -4A$	7			S
Diode forward voltage	V_{SD}	$IS=-1A, V_{GS}=0V$	-0.5	-0.8	-1	V
Dynamic						
Input capacitance	C_{iss}	$V_{DS} = 15V, V_{GS} = 0V, f = 1MHz$			1050	pF
Output capacitance	C_{oss}			99		pF
Reverse transfer capacitanceb	C_{rss}			77		pF
Total gate charge	Q_g	$V_{DS} = -10V, V_{GS} = -4.5V, ID = -4.5A$		11	14	nC
Gate-source charge	Q_{gs}			1.3		nC
Gate-drain charge	Q_{gd}			2.8		nC
Gate resistance	R_g	$f = 1MHz$			3.6	Ω
Switchingb						
Turn-on delay time	$t_{d(on)}$	$V_{DD} = -10V, RL = 10\Omega, ID \approx 1A, V_{GEN} = -4.5V, R_g = 6\Omega$		7	15	ns
Rise time	t_r			15	20	ns
Turn-off delay time	$t_{d(off)}$			38	50	ns
Fall time	t_f			3	10	ns
Drain-source body diode characteristics						
Continuous Source-Drain Diode Current	I_S	$T_c = 25^\circ C$			-1.3	A
Pulsed Diode forward Current	I_{SM}				-20	A

Note :

1. Repetitive Rating : Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t < 5$ sec.
3. Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production testing.

Typical Characteristics :



单击下面可查看定价，库存，交付和生命周期等信息

[>>DIOS\(迪恩思\)](#)