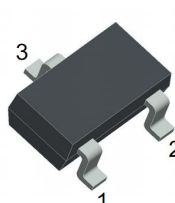
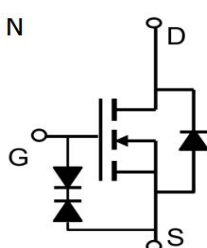
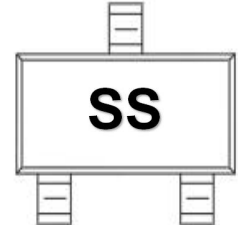


N-Channel 50-V(D-S) MOSFET		SOT-23 Plastic-Encapsulate MOSFETS	
<p style="text-align: center;"><u>SOT-23</u></p>  <p>1.GATE 2.SOURCE 3.DRAIN</p> <p>Equivalent Circuit:</p> 		<p>Features</p> <ul style="list-style-type: none"> • Rugged and Reliaible • High density cell design for extremely low RDS(on) • ESD Protected <p>Application</p> <ul style="list-style-type: none"> ※ Direct Logic-Level Interface: TTL/CMOS ※ Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc. ※ Battery Operated Systems ※ Solid-State Relays <p>MARKING:</p> 	
V(BR)DSS	RDS(on)MAX		ID
50 V	2Ω@10V 3Ω@4.5V		300mA
Mosfet Maximum ratings (Ta=25°C unless otherwise noted)			
Parameter	Symbol	Value	Unit
Drain-Source Voltage	VDS	50	V
Gate-Source Voltage	VGS	±20	
Continuous Drain Current	ID	0.3	A
Continuous Source-Drain Current(Diode Conduction)	IS	1.3	
Power Dissipation	PD	0.35	W
Thermal Resistance from Junction to Ambient	RθJA	357	°C/W
Junction Temperature	TJ	150	°C
Storage Temperature	TSTG	-55~+150	°C

MOSFET ELECTRICAL CHARACTERISTICS						
unless otherwise specified Ta = 25 °C						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Characteristics						
Drain-source breakdown voltage	V(BR)DSS	VGS = 0V , ID =250µA	50			V
Gate-threshold voltage	VGS(th)	VDS =VGS , ID =250µA	0.8		1.6	V
Gate-body leakage	IGSS	VDS =0V , VGS =±20V			±1	µA
Zero gate voltage drain current	IDSS	VDS =30V , VGS =0V			1	µA
Drain-source on-resistancea	RDS(on)	VGS =10V , ID = 300mA		1.1	2	Ω
		VGS =4.5V , ID =300mA		1.2	3	Ω
Forward transconductancea	gfs	VDS =10V , ID =300mA	0.12			S
Diode forward voltage	VSD	IS=100mA , VGS=0V		0.8	1.28	V
Dynamic Characteristics						
Input capacitance	Ciss	VDS =25V ,VGS =0V, f=1MHz		27		pF
Output capacitance	Coss			13		pF
Reverse transfer capacitanceb	Crss			6		pF
Switchingb Characteristics						
Turn-on delay time	td(on)	VDD=30V Rg=2Ω, ID =220mA, VGEN=4.5V,RL=3Ω			5	ns
Rise time	tr				18	ns
Turn-off delay time	td(off)				36	ns
Fall time	tf				14	ns
Note :						
1. These parameters have no way to verify.						
2. Pulse Test ; Pulse Width ≤300µs, Duty Cycle ≤2%.						

单击下面可查看定价，库存，交付和生命周期等信息

[>>DIOS\(迪恩思\)](#)