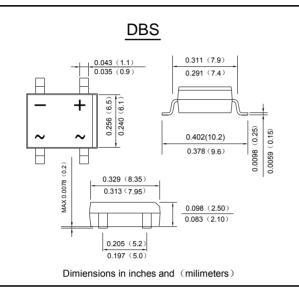


SINGLE PHASE 3.0AMP GLASS PASSIVATED BRIDGE RECTIFIER



Features

- · Glass passivated die construction
- · Low forward voltage drop
- · High current capability
- · High surge current capability
- Plastic material-UL flammability 94V-0

Mechanical Data

- · Case: DB-S, molded plastic
- Terminals: plated leads solderable per MIL-STD-202. Method 208
- · Polarity: as marked on case
- Mounting position: Any
- · Marking: type number
- · Lead Free: For RoHS / Lead Free Version,

Maximum Ratings and Electrical Characteristics

Rating at 25℃ ambient temperature unless otherwise specified.

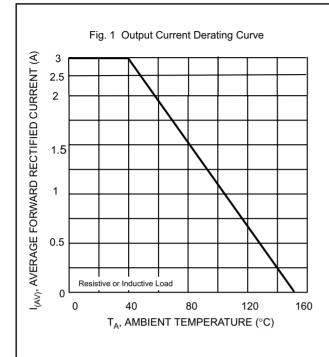
Single Phase, half wave, 60Hz, resistive or inductive load.

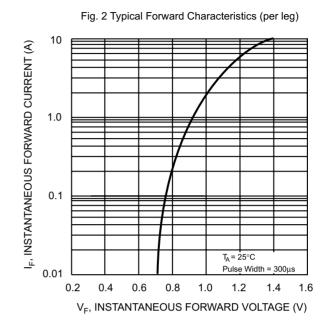
For capacitive load, derate current by 20%.

TYPE NUMBER	SYMBOL	DB301S	DB302S	DB303S	DB304S	DB305S	DB306S	DB307S	UNITS
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM	50	100	200	400	600	800	1000	V
	VRWM								
	VDC								
RMS Reverse Voltage	VRMS	35	70	140	280	420	560	700	V
Average Rectified Output Current (Note 1)@Ta=40℃	lo	3.0						Α	
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	İFSM	80						А	
Forward Voltage per element @IF=3.0A	VFM	1.1						V	
Peak Reverse Current @Ta=25℃ At Rated DC Blocking Voltage @Ta=125℃	lR	5.0 500							uA
Typical Junction Capacitance per leg (Note 2)	СJ	25						pF	
Typical Thermal Resistance per leg	Rеја	40							°C/W
	Røjl	15							
Operating and Storage Temperature Range	Т _J ,Тsтg	-55to+150							$^{\circ}$

Note:1. Mounted on glass epoxy PC board with 1.3mm² solder pad. 2.Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.





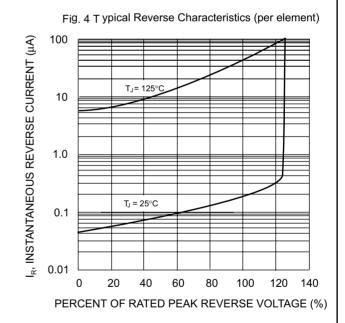


AV LAND BORN BO TA = 25°C Single Half Sine-Wave Pulse Width =8.3ms (JEDEC Method)

1.0 10

NUMBER OF CYCLES AT 60 Hz

Fig. 3 Maximum Peak Forward Surge Current (per leg)



单击下面可查看定价,库存,交付和生命周期等信息

>>DIOS(迪恩思)