Features

- Two independent interfaces complying to DSI3 Bus Standard, revision 1.00, Feb. 16, 2011
- Suitable for fast data acquisition of sensors in signal function class
- Supports:
  - Point-to-point
  - Daisy chain bus
  - Parallel bus
- Versatile in-circuit configuration:
  - up to 15 DSI3-slaves
  - up to 16 packets per frame
  - 2 .. 63 bytes of average payload data per packet
  - supports EMC design by adjustable signal shapes
- Operating modes:
  - CRM - Command Response Mode
  - PDCM - Periodic Data Collection Mode
  - BDM - Background Diagnostic Mode
  - DM - Discovery Mode for address assignment in resistor connected daisy chain
- SPI interface

Applications

- Ultra sonic parking assistant system
- DSI3 bus networks

General Description

The E521.42 DSI3 master supports two independent DSI3-networks connecting sensor devices.

It provides physical and digital protocol handling according to the DSI3-bus standard. Besides established DSI3 applications it enables ultrasonic park assist sensor systems taking advantage of significantly increased data payloads. The E521.42 serves different numbers of slaves with different amount of acquired data. It allows network configuration in operation (TDMA scheme upload).

The IC includes a 140 words buffer for seamless acquisition of sensor data and exchange of control and configuration information.

The IC was developed according to ISO26262 as a safety element out of context with safety requirements rated ASIL B.
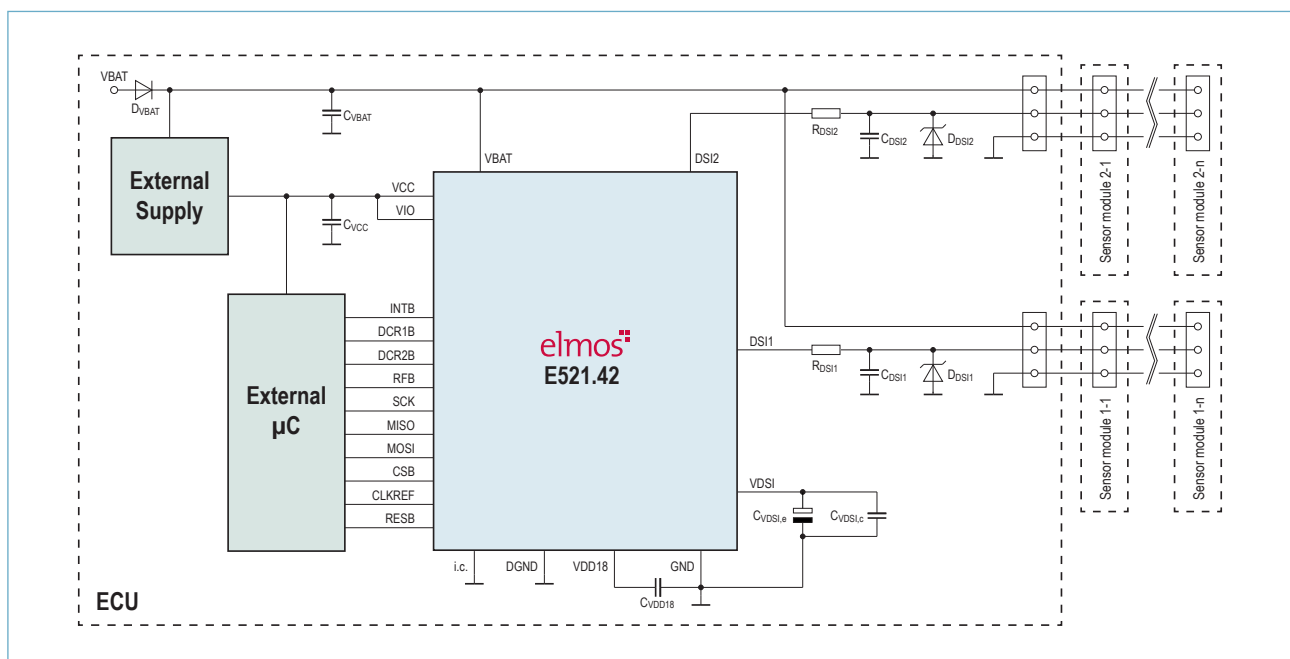
Ordering Information

Product ID	Temp Range	Package
E52142A52C	-40°C to +150°C	QFN20L4
E52142A52CXX2	-40°C to +150°C	QFN20L4 with SLP

Typical Application Circuit

Elmos Semiconductor SE reserves the right to change the detail specifications as may be required to permit improvements in the design of its products.

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