

2MHz, 3A, COT Synchronous BUCK converter in SOT23-5

DESCRIPTION

The ETA3448 is a high-efficiency, DC-to-DC step-down switching regulator, capable of delivering up to 3A of output current. The devices operate from an input voltage range of 2.5V to 5.5V and provide output voltages from 0.6V to VIN, making the ETA3448 ideal for low voltage power conversions. ETA3448 adopts an Adaptive COT control scheme that enables very fast transient response and provides a very smooth transition when the output varies from light load to heavy load.

The adaptive COT control also maintains a constant switching frequency across line and load. Running at a fixed frequency of 2MHz allows the use of small inductance value and low DCR inductors, thereby achieving higher efficiencies. Other external components, such as ceramic input and output caps, can also be small due to higher switching frequency, while maintaining exceptional low noise output voltages. Internal soft-start control circuitry reduces inrush current. Short-circuit and thermal-overload protection improves design reliability.

ETA3448 is available in a tiny SOT23-5L package.

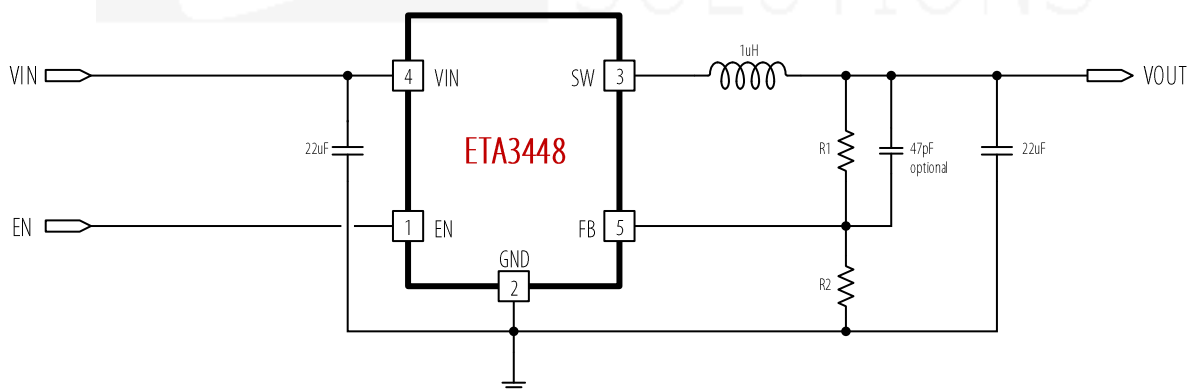
FEATURES

- ◆ Up to 93% Efficiency
- ◆ Up to 3A Max Output Current
- ◆ Adaptive COT Control
- ◆ Ultra-fast load transient response
- ◆ Forced PWM Mode
- ◆ 100% Duty Cycle Operation
- ◆ 1% Feedback Accuracy
- ◆ Adjustable Output from 0.6V
- ◆ Cycle-by-cycle Over Current Protection
- ◆ Short Circuit Protection with Hiccup Mode
- ◆ Stable with Low-ESR Output Ceramic Capacitors
- ◆ Available in SOT23-5L Package

APPLICATIONS

- ◆ LCD TV
- ◆ Set Top Box
- ◆ IP CAM

TYPICAL APPLICATION

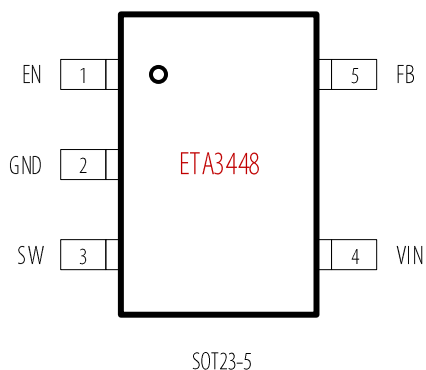


$$V_{out} = \frac{R1+R2}{R2} \times 0.6(V)$$

ORDERING INFORMATION

PART No.	PACKAGE	TOP MARK	Pcs/Reel
ETA3448S2F	SOT23-5	EB YW	3000

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATINGS

(Note: Exceeding these limits may damage the device. Exposure to absolute maximum rating conditions for long periods may affect device reliability.)

VIN, FB, EN, SW Voltage	−0.3V to 9V
Operating Temperature Range	−40°C to 85°C
Storage Temperature Range	−55°C to 150°C
Thermal Resistance	θ_{JA} θ_{JC}
SOT23-5.....	180.....90..... °C/W
Lead Temperature (Soldering 10sec)	260°C
ESD HBM (Human Body Mode)	2KV
ESD MM (Machine Mode)	200V

ELECTRICAL CHARACTERISTICS

(VIN = 5.0V, unless otherwise specified. Typical values are at TA = 25°C.)

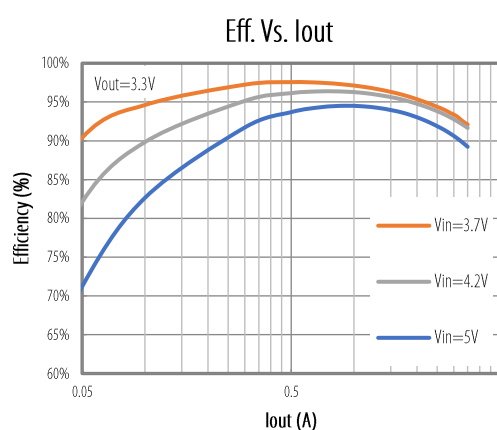
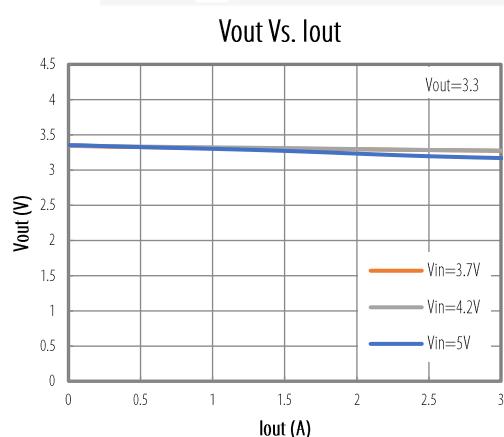
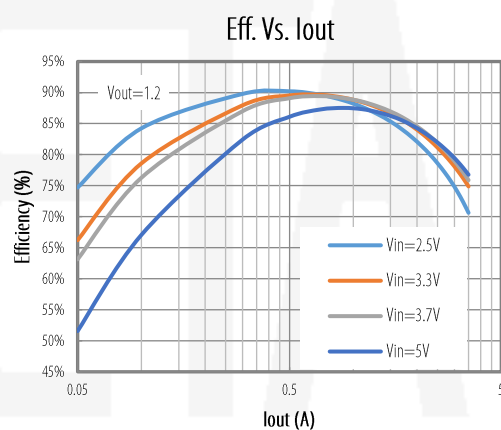
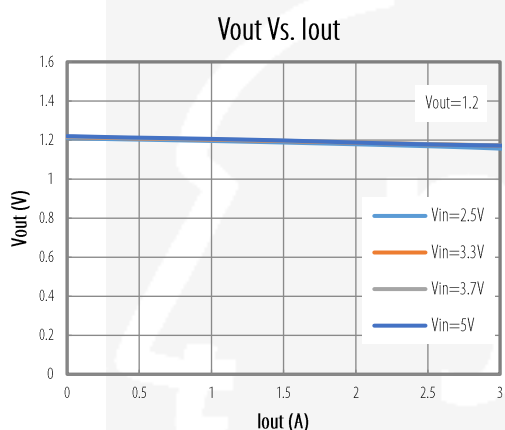
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage Range		2.5		5.5	V
Input UVLO	Rising, Hysteresis=500mV	2.3	2.4	2.5	V
Input OVP	Rising, Hysteresis=0.3V	5.55	5.85	6.15	V
Input Supply Current	VFB=0.65V, no switching		50	100	μA
Input Shutdown Current			0	1	μA
FB Voltage		0.594	0.6	0.606	V
FB Input Current			0	1	μA
Output Voltage Range		0.6		VIN	V
Load Regulation			1		%/A
Line Regulation	Vin=2.7V to 5.5V		0.2		%/V
Switching Frequency		1.5	2	2.5	MHz
Maximum Duty Cycle			100		%
Short Circuit Hiccup Time	On Time		2		mS
	Off Time		18		mS
FB Hiccup Threshold			0.2		V
High Side Switch On Resistance			50	75	mΩ
Low Side Switch On Resistance			40	60	mΩ
High Side Current Limit		4.5	6		A
Low Side Current Limit		3	4		A
SW Leakage Current	VOUT=5.5V, VSW=0 or 5.5V, EN= GND			10	μA
EN Rising Threshold	Rising,	1.2			V
EN Falling Threshold	Falling			0.4	V
EN Input Current	VEN=2V			1	uA
Thermal Shutdown	Rising, Hysteresis =34°C		150		°C

PIN DESCRIPTION

PIN #	NAME	DESCRIPTION
1	EN	Enable. EN is high voltage level to enable. For automatic start-up, connect EN pin to VIN pin with a pull-up resistor.
2	GND	Ground
3	SW	Inductor Connection. Connect an inductor Between SW and the regulator output.
4	VIN	Supply Voltage. Bypass with a 22 μ F ceramic capacitor to GND
5	FB	Feedback Input. Connect an external resistor divider from the output to FB and GND to set V_{OUT}

TYPICAL CHARACTERISTICS

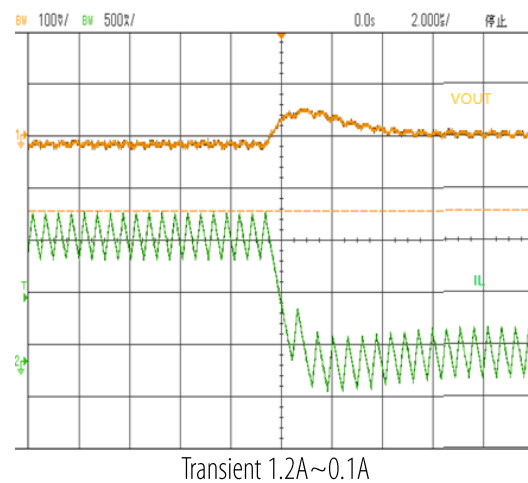
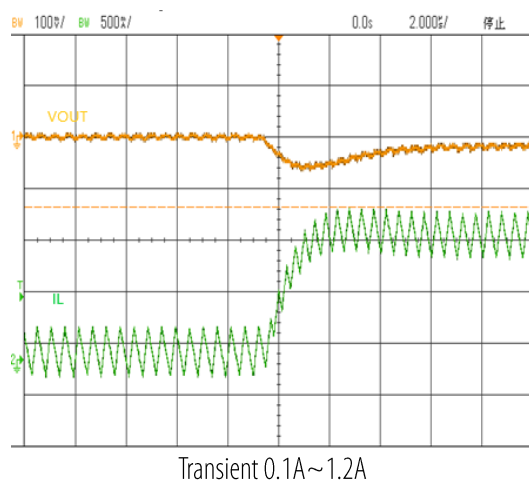
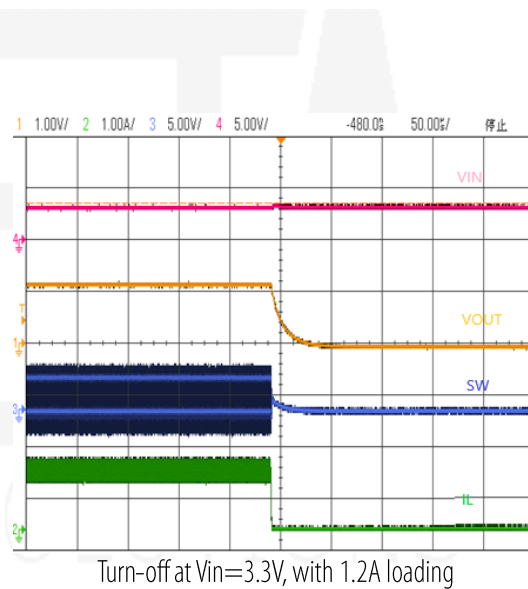
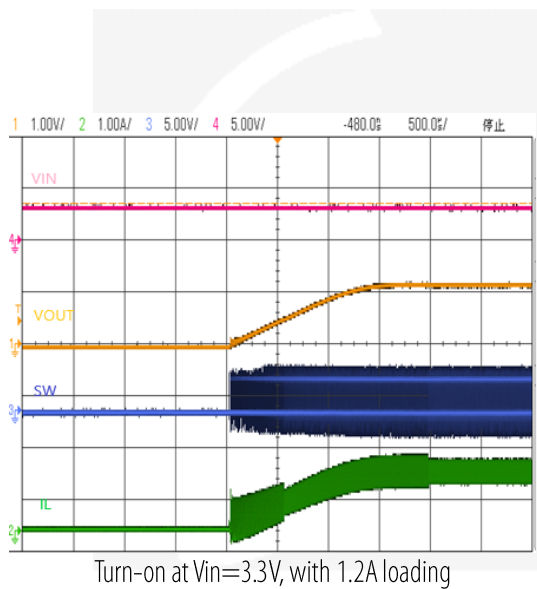
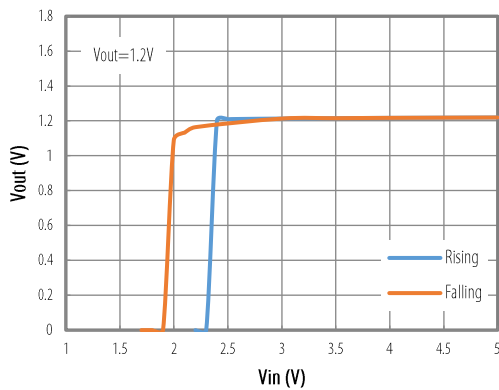
(Typical values are without Cff, at $T_A = 25^\circ\text{C}$ unless otherwise specified.)



TYPICAL CHARACTERISTICS

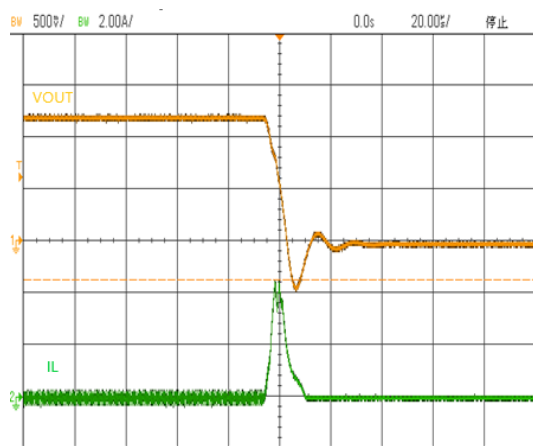
(Typical values are without C_{ff}, at T_A = 25°C unless otherwise specified.)

UVLO

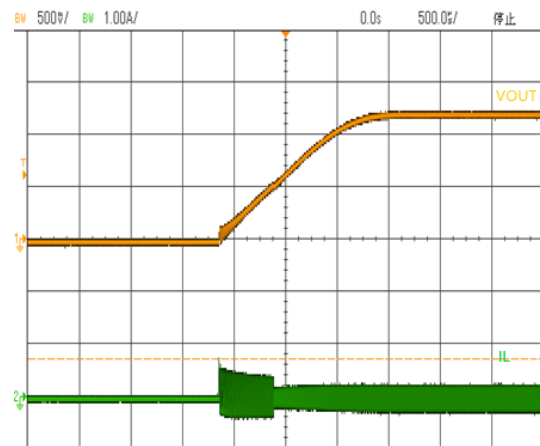


TYPICAL CHARACTERISTICS

(Typical values are without C_{ff}, at T_A = 25°C unless otherwise specified.)

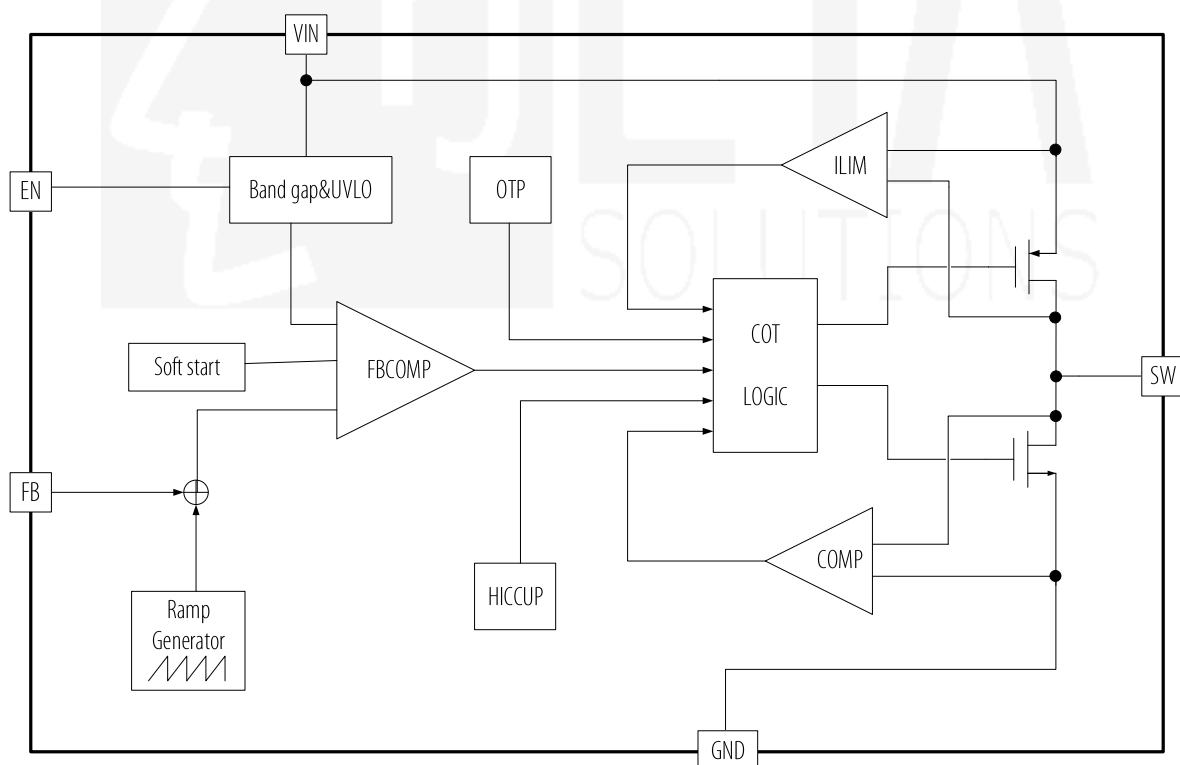


Short Circuit Protection



Recovery

FUNCTIONAL BLOCK DIAGRAM



FUNCTIONAL DESCRIPTION

The ETA3448 is a synchronous buck regulator ICs that integrates the adaptive COT control, top and bottom switches on the same die to minimize the switching transition loss and conduction loss.

ETA3448 is a high-efficiency and high frequency DC-to-DC step-down switching regulator, capable of delivering up to 3A of output current. It adopts an Adaptive COT control scheme that enables very fast transient response and provides a very smooth transition when the output varies from light load to heavy load. It compares the sum of the FB voltage and a ripple voltage that mimics the voltage due to the output ESR and capacitance. The constant-on-time timer varies with line to achieve relative constant switching frequency across line.

Forced PWM Operation

A forced PWM DC-DC regulator always switches at a fixed frequency when the output heavy load or light load. This is to ensure a minimum output voltage ripple over the full load range.

Over Current Protection and Hiccup

ETA3448 has a cycle-by-cycle over current limit for when the inductor current peak value is over the set current limit threshold. When the output voltage drop until FB falls below UV threshold (0.2V), the ETA3448 will enter hiccup mode. It will turn off the chip immediately for 18mS. After that, it will try to re-starts as normal for 2mS. After 2mS, if FB is still below UV threshold, then the chip enters hiccup mode again. If FB is higher than UV threshold, it will enter the normal mode.

Over-Temperature Protection

Thermal protection disables the output when the junction temperature rises to approximately 150°C, allowing the device to cool down. When the junction temperature cools to approximately 115°C, the output circuitry is again enabled. Depending on power dissipation, thermal resistance, and ambient temperature, the thermal protection circuit may cycle on and off. This cycling limits regulator dissipation, protecting the device from damage as a result of overheating.

APPLICATION INFORMATION

External Output Voltage Setting

In external Output Voltage Setting Version selected, the ETA3448 regulator is programmed using an external resistor divider. The output voltage is calculated using below equation.

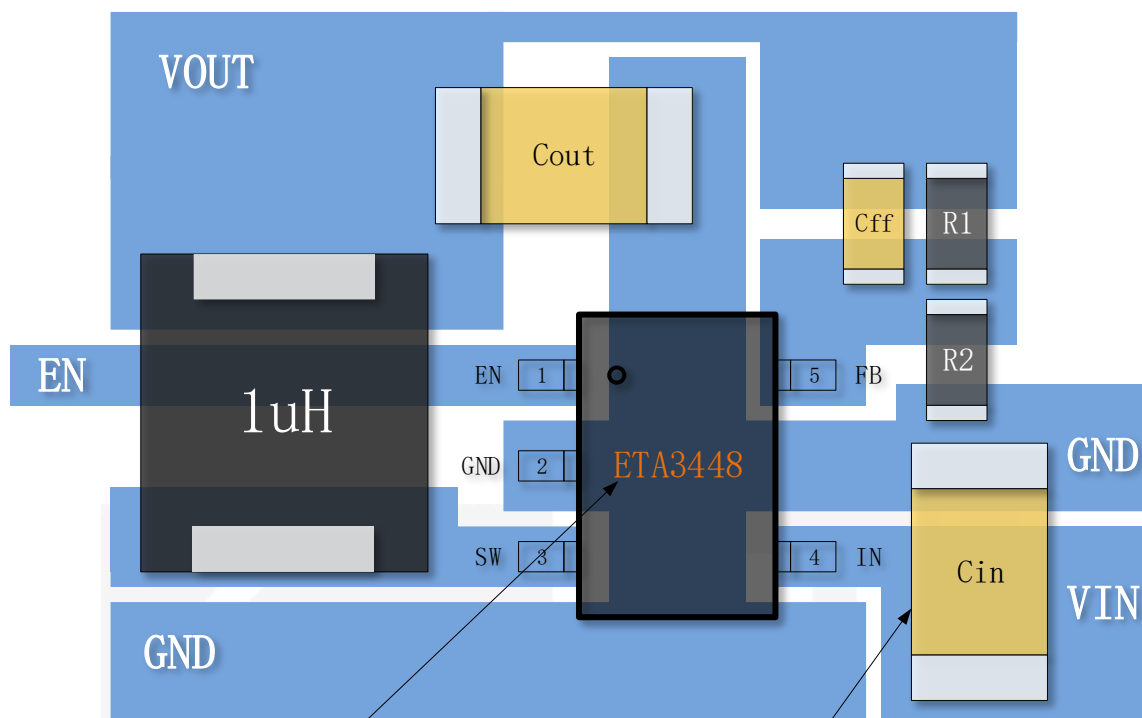
$$V_{OUT} = V_{REF} \times \left(1 + \frac{R_1}{R_2}\right)$$

Where: $V_{REF} = 0.6V$ typically (the internal reference voltage)

Resistors R2 has to be between 1kOhm to 70KOhm and thus R1 is calculated by following equation.

$$R_1 = \left(\frac{V_{OUT}}{V_{REF}} - 1\right) \times R_2$$

PCB LAYOUT GUIDE

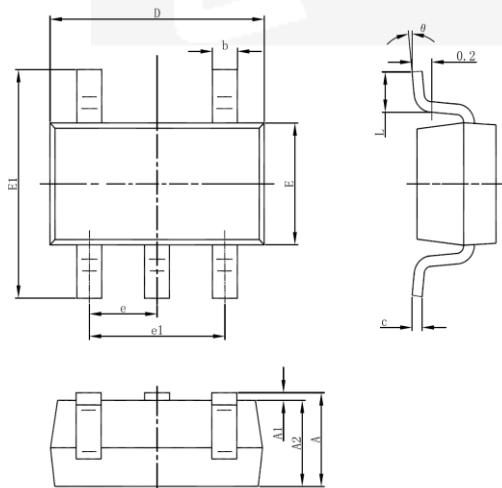


GND trace goes underneath the IC body to have shortest connection to Cin.

Cin has to be placed very close to VIN and GND pins.

PACKAGE OUTLINE

Package: SOT23-5



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

单击下面可查看定价，库存，交付和生命周期等信息

[>>ETA](#)