

DATASHEET

8 PIN DIP DUAL CHANNEL HIGH SPEED 1Mbit/s TRANSISTOR PHOTOCOUPLER EL253X series



Features

- High speed 1Mbit/s
- 10kV/µs min. common mode transient immunity (EL2611)
- Guaranteed performance from -40 to 85°C
- · Logic gate output
- High isolation voltage between input and output (Viso=5000 V rms)
- Pb free and RoHS compliant.
- UL and cUL approved(No. E214129)
- VDE approved (No. 132249)
- SEMKO approved
- NEMKO approved
- DEMKO approved
- FIMKO approved

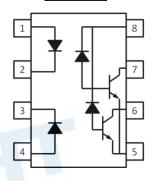
Description

The EL2530 and EL2531 dual channel devices each consist of an infrared emitting diode, optically coupled to a high speed photo detector transistor. A separate connection for the photodiode bias and output-transistor collector increase the speed by several orders of magnitude over conventional phototransistor couplers by reducing the base-collector capacitance of the input transistor. The devices are packaged in an 8-pin DIP package and available in wide-lead spacing and SMD option.

Applications

- Line receivers
- Telecommunication equipments
- Power transistor isolation in motor drives
- Replacement for low speed phototransistor photo couplers
- Feedback loop in switch-mode power supplies
- Home appliances
- High speed logic ground isolation

Schematic



Pin Configuration

- 1. Anode
- 2. Cathode
- 3. Cathode
- 4. Anode
- 5. Gnd
- 6. V_{out 2}
- 7. V_{out 1} 8. V_{CC}

Truth Table (Positive Logic)

	•	0 /
Input	Enable	Output
Н	Н	L
L	Н	Н
Н	L	Н
L	L	Н
Н	NC	L
L	NC	Н



Absolute Maximum Ratings (T_A=25°C)

	Parameter	Symbol	Rating	Unit
	Forward current	lf	25	mA
	Peak forward current (50% duty, 1ms P.W)	l _{FP}	50	mA
Input	Peak transient current (≤1µs P.W,300pps)	lFtrans	1	А
	Reverse voltage	V_R	5	V
	Power dissipation	P _{IN}	45	mW
	Power dissipation	Po	35	mW
	Average Output current	$I_{O(AVG)}$	8	mA
Output	Peak Output current	I _{O(PK)}	16	mA
	Output voltage	Vo	-0.5 to 20	V
	Supply voltage	V _{CC}	-0.5 to 30	V
Isolation voltage *1		V _{ISO}	5000	Vrms
Operating temperature		T _{OPR}	-40~+100	°C
Storage te	emperature	T _{STG}	-40~+125	°C
Soldering	temperature *2	T _{SOL}	260	°C

Notes:

^{*1} AC for 1 minute, R.H.= 40 ~ 60% R.H. In this test, pins 1, 2, 3 & 4 are shorted together, and pins 5, 6, 7 & 8 are shorted together.

^{*2} For 10 seconds.



Electrical Characteristics (T_A=0 to 70°C unless specified otherwise)

Input

Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Forward voltage	V_{F}	-	1.45	1.8	V	$I_F = 16$ mA, $T_A = 25$ °C
Reverse voltage	V_R	5.0	-	-	V	$I_R = 10\mu A$
Temperature coefficient of forward voltage	$\Delta V_F/\Delta T_A$	-	-1.9	-	mV/°C	I _F = 16mA
Input capacitance	C_IN	-	60	-	pF	$V_F=0V$, $f=1MHz$

Output

Parameter	Symbol	Min.	Тур.*	Max.	Unit	Conditions
Logic High Output Current	Іон -	-	0.001	0.5	μA	I_F =0mA, V_O = V_{CC} =5.5 V , T_A =25 $^{\circ}$ C
	IOH	-	-	μ <i>λ</i>		I _F =0mA, V _O =V _{CC} =15V T _A =25°C
Logic Low Supply Current	I _{CCL}	-	140	400	μΑ	I_{F1} = I_{F2} 16mA, V_O =Open, V_{CC} =15 V
Logic High Supply Current		5	0.01	1	μA	I _F =0mA, V _O =Open, V _{CC} =15V, T _A =25°C
	Іссн		-	4		I _F =0mA, V _O =Open, V _{CC} =15V

^{*} Typical values at T_A = 25°C

Transfer Characteristics (T_A=0 to 70°C unless specified otherwise)

Parameter		Symbol	Min.	Тур.*	Max.	Unit	Conditions	
	EL2530		7	-	50	%	$I_F = 16mA, V_O = 0.4V,$	
Current Transfer	EL2531		19	-	50		V _{CC} =4.5V, T _A =25°C	
Ratio	EL2530		5	-	-	, ,	$I_F = 16 \text{mA}, V_O = 0.5 \text{V},$	
	EL2531		15	-	-		V _{CC} =4.5V	
	EL2530		-	0.18	0.5		$I_F = 16mA$, $I_O = 1.1mA$, $V_{CC}=4.5V$, $T_A=25$ °C	
Logic Low	EL2531	V	-	0.25	0.5	V	$I_F = 16mA$, $I_O = 3mA$, $V_{CC} = 4.5V$, $T_A = 25$ °C	
Output Voltage	EL2530	· V _{OL} -	-	-	0.5	V	$I_F = 16mA$, $I_O = 0.8mA$, $V_{CC} = 4.5V$	
	EL2531		-	-	0.5		I _F =16mA ,I _O =2.4mA, V _{CC} =4.5V	



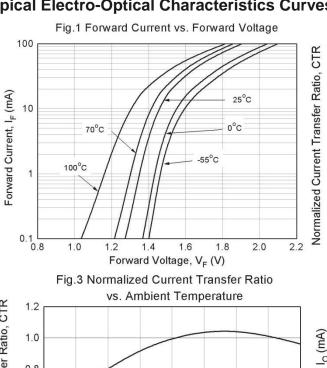
Switching Characteristics (T_A=0 to 70°C unless specified otherwise, I_F=16mA, V_{CC}=5V)

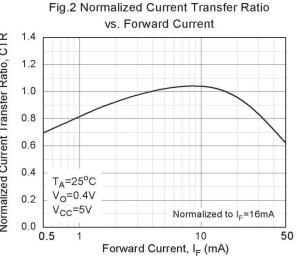
Parameter		Symbol	Min.	Тур.*	Max.	Unit	Conditions	
Propagation Delay Time to	EL2530		-	0.35	1.5		R _L =4.1KΩ, T _A =25°C	
	EL2550	t _{PHL}	-	-	2.0	- µs	$R_L=4.1K\Omega$	
Logic Low (Fig.8)	EL2531	PHL	-	0.35	0.8	- μ3 -	$R_L=1.9K\Omega$, $T_A=25$ °C	
(1 19.0)	ELZOST		-	-	1.0		R _L =1.9KΩ	
Danasation	EL2530		-	0.5	1.5	_	R _L =4.1KΩ, T _A =25°C	
Propagation Delay Time to		. , .	-	-	2.0	_	R _L =4.1KΩ	
Logic High	EL2531	t _{PLH}	-	0.3	0.8	μs	R _L =1.9KΩ, T _A =25°C	
(Fig.8)	EL2551		-	-	1.0		$R_L=1.9K\Omega$	
Common Mode Transient Immunity at Logic High (Fig.9)*3	EL2530	СМн	1,000	10,000	-	\//uo	$I_F = 0$ mA , $V_{CM} = 10V_{p-p}$, $R_L = 4.1$ K Ω , $T_A = 25$ °C	
	EL2531		1,000	10,000	-	V/µs	$I_F = 0$ mA , $V_{CM} = 1000 V_{p-p}$, $R_L = 1.9$ K Ω , $T_A = 25$ °C	
Common Mode Transient	EL2530		1,000	10,000	-	V/µs	I_F = 16mA , V_{CM} =10 V_{p-p} , R_L =4.1 $K\Omega$, T_A =25° C	
Immunity at Logic Low (Fig.9) _{*3}	EL2531	CM _L	1,000	10,000	-		$\begin{split} I_F &= 16\text{mA} \text{ , } V_{CM} \!\!=\! 1000 V_{\text{p-p}}, \\ R_L \!\!=\! 1.9 \text{K}\Omega, T_A \!\!=\! 25^{\circ}\text{C} \end{split}$	
* Typical values a	$T_A = 25^{\circ}C$							
EVERLIGHT								

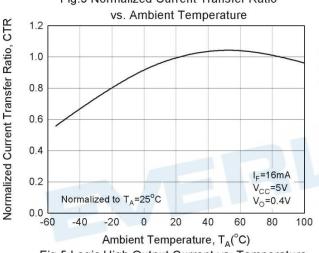
^{*} Typical values at T_A = 25°C

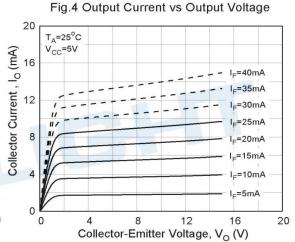


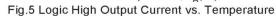
Typical Electro-Optical Characteristics Curves

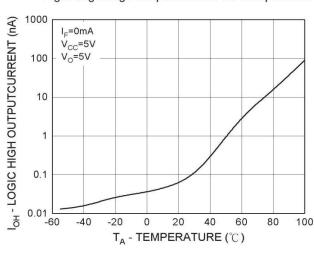


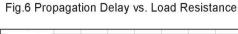












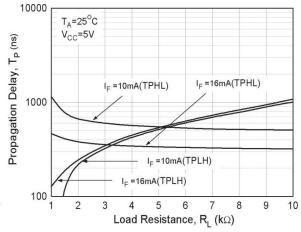
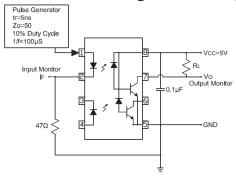




Fig. 8 Switching Time Test Circuit & Waveform



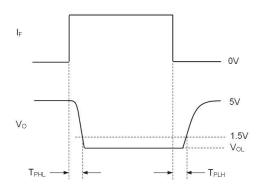
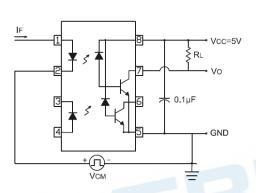
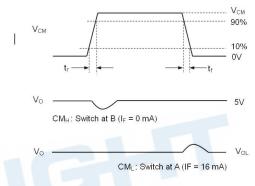


Fig. 9 Transient Immunity Test Circuit & Waveform





Note:

*3 Common mode transient immunity in logic high level is the maximum tolerable (positive) dVcm/dt on the leading edge of the common mode pulse signal VCM, to assure that the output will remain in a logic high state (i.e., V₀ > 2.0V).

Common mode transient immunity in logic low level is the maximum tolerable (negative) dVcm/dt on the trailing edge of the common mode pulse signal, VCM, to assure that the output will remain in a logic low state (i.e., $V_0 < 0.8V$).



Order Information

Part Number

EL253XY(Z)-V

Note

X = Part no. (0 or 1)

Y = Lead form option (S, S1, M or none) Z = Tape and reel option (TA, TB or none)

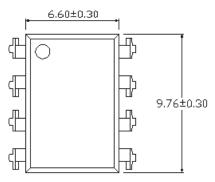
V = VDE (optional)

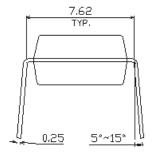
Option	Description	Packing quantity
None	Standard DIP-8	45 units per tube
M	Wide lead bend (0.4 inch spacing)	45 units per tube
S (TA)	Surface mount lead form + TA tape & reel option	1000 units per reel
S (TB)	Surface mount lead form + TB tape & reel option	1000 units per reel
S1 (TA)	Surface mount lead form (low profile) + TA tape & reel option	1000 units per reel
S1 (TB)	Surface mount lead form (low profile) + TB tape & reel option	1000 units per reel

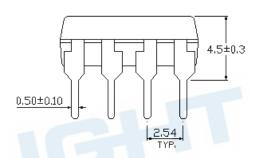


Package Dimension (Dimensions in mm)

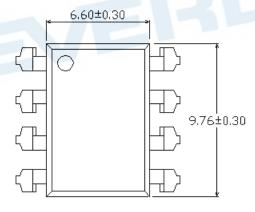
Standard DIP Type

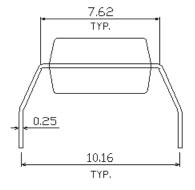


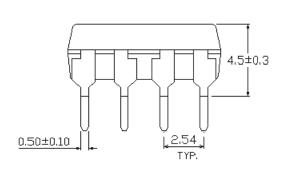




Option M Type

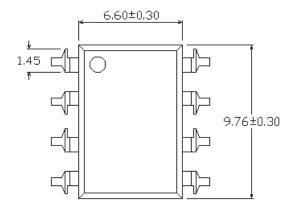


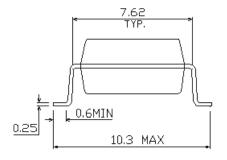


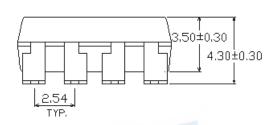




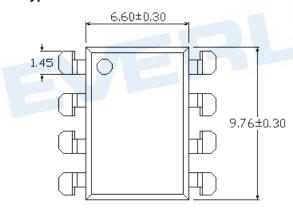
Option S Type

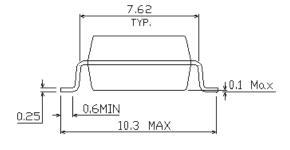


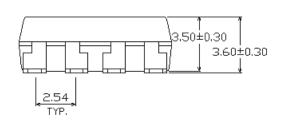




Option S1 Type

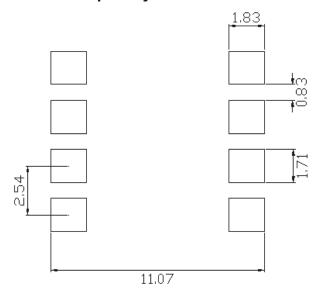








Recommended pad layout for surface mount leadform



Notes.

Suggested pad dimension is just for reference only.

Please modify the pad dimension based on individual need.

Device Marking



Notes

T denotes Factory

No code : made in China

T : made in Taiwan

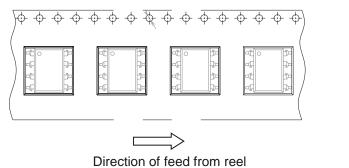
EL denotes EVERLIGHT
2530 denotes Device Number
Y denotes 1 digit Year code
WW denotes 2 digit Week code
V denotes VDE (optional)

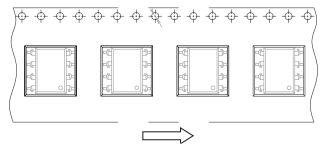


Tape & Reel Packing Specifications

Option TA

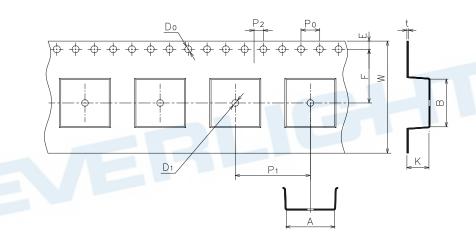






Direction of feed from reel

Tape dimension



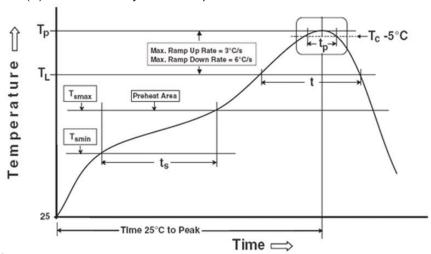
Dimension No.	Α	В	Do	D1	E	F
Dimension(mm)	10.4±0.1	10.0±0.1	1.5+0.1/-0	1.5±0.25	1.75±0.1	7.5±0.1
Dimension No.	Ро	P1	P2	t	W	К
Dimension(mm)	4.0±0.1	12.0±0.1	2.0±0.05	0.4±0.05	16.0±0.3	4.5±0.1



Precautions for Use

1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note: Reference: IPC/JEDEC J-STD-020D

Preheat

Temperature min (T_{smin}) 150 °C

Temperature max (T_{smax}) 200°C

Time $(T_{smin} \text{ to } T_{smax})$ (t_s) 60-120 seconds

Average ramp-up rate $(T_{smax} \text{ to } T_p)$ 3 °C/second max

Other

Liquidus Temperature (T_L)

217 °C

Time above Liquidus Temperature (t_L)

60-100 sec

Peak Temperature (T_P)

260°C

Time within 5 °C of Actual Peak Temperature: T_P - 5°C

Ramp- Down Rate from Peak Temperature

6°C /second max.

Time 25°C to peak temperature

8 minutes max.

Reflow times

3 times



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