

DATASHEET

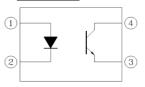
4 PIN DIP VERY HIGH ISOLATION VOLTAGE PHOTOCOUPLER CNY64S Series

Features:

- Compliance Halogens Free (Br < 900 ppm, Cl < 900 ppm, Br+Cl < 1500 ppm)
- High Voltage, BV_{CEO}=80V (min.)
- Operating temperature up to +85°C
- High isolation voltage between input and output V_{IOTM} = 8200 V perk for CNY64 V_{IOTM} = 10000 V perk for CNY64-V
- Rated recurring peak voltage (repetitive), Viorim = 2200 V
- Creepage current resistance according to VDE 0303/IEC 60112 comparative tracking index: CTI ≥ 200
- Thickness through insulation ≥3mm
- Pb free and RoHS compliant.
- CUL approved (No. E214129)
- VDE approved (No. 40027351)
- FIMKO approved (No. 25464)



Schematic



Pin Configuration

- 1. Anode
- 2. Cathode
- 3. Emitter
- 4. Collector

Description

The CNY64S series contains an infrared emitting diode optically coupled to a phototransistor.

These devices are packaged in an 4-pin SMD package and providing a distance between input and output for highest safety requirement of >3mm.

Applications

- · Switch mode power supply
- · Line receiver
- · Computer peripheral interface
- · Microprocessor system interface
- Circuits for safe protective separation against electrical shock according to safety class II (reinforced isolation):
 - for appl. class I IV at mains voltage ≤ 300 V
 - for appl. class I IV at mains voltage ≤ 600 V
 - for appl. class I III at mains voltage ≤ 1000 V according to DIN EN 60747-5-5.



Absolute Maximum Ratings (Ta=25℃)

	Parameter	Symbol	Rating	Unit
	Forward current	l _F	75	mA
Input	Peak forward current (<10µs)	I _{FM}	1.5	А
Input	Reverse voltage	V_R	/ _R 5	
	Power dissipation	P_D	120	mW
Output	Collector power dissipation	Pc	150	mW
	Collector current	Ic	50	mA
	Collector-Emitter voltage	V _{CEO}	80	V
	Emitter-Collector voltage	V_{ECO}	7	V
Total Power Dissipation		Ртот	250	mW
Isolation Voltage*1		V_{ISO}	8200	V rms
Operating Temperature		T _{OPR}	-55~+85	°C
Storage Temperature		T _{STG}	-55~+100	°C
Soldering Temperature*2		T _{SOL}	260	°C

Notes:

^{*1} AC for 1 minute, R.H.= 40 ~ 60% R.H. In this test, pins 1 & 2 are shorted together, and pins 3 & 4 are shorted together.

^{*2 2}mm from case, <10 seconds.



Electro-Optical Characteristics (Ta=25°C unless specified otherwise)

Input

Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Forward voltage	V_{F}	-	1.6	2.0	V	I _F = 50mA
Reverse current	I _R	-	-	10	μA	V _R = 5V
Input capacitance	C_in	-	-	100	pF	V = 0, f = 1MHz

Output

Parameter	Symbol	Min	Тур.	Max.	Unit	Condition
Collector-Emitter dark current	I _{CEO}	-	-	200	nA	$V_{CE} = 20V$, $I_F=0mA$
Collector-Emitter breakdown voltage	BV_CEO	80	-	-	V	I _C = 1mA
Emitter-Collector breakdown voltage	BV _{ECO}	7	-	-	V	$I_E = 0.1 \text{mA}$
Collector-Emitter capacitance	C _{CE}	-	-	50	pF	$V_{CE} = 0V$, $f = 1MHz$

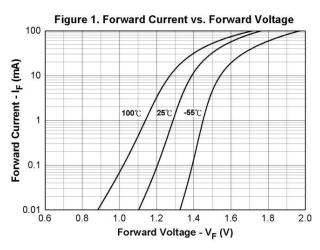
Transfer Characteristics

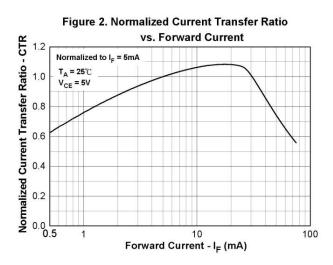
Parameter		Symbol	Min	Тур.	Max.	Unit	Condition	
Current Transfer ratio	CNY64	CTR	50	-	300	- % -		
	CNY64A		63	-	125		$I_F = 5mA$, $V_{CE} = 5V$	
	CNY64B		100	-	200			
Collector-Emitter saturation voltage		V _{CE(sat)}	-	-	0.3	V	I _F = 10mA , I _C = 1mA	
Isolation resistance		R _{IO}	10 ¹¹	-	-	Ω	V _{IO} = 500Vdc,	
Coupling capacitance		C _{IO}	-	0.3	-	pF	f = 1MHz	
Turn-on time		Ton	-	6	18		$V_{CC} = 5V$,	
Turn-off time		T _{off}	-	7	18		$I_C = 5mA$, $R_L = 100\Omega$	
Rise time		t _r	-	3	18	μs	V _{CC} = 5V,	
Fall time		t _f	-	5	18		$I_C = 5\text{mA}, R_L = 100\Omega$	

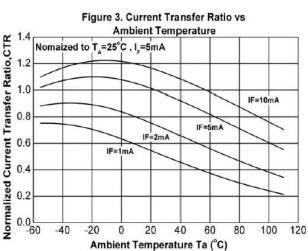
^{*} Typical values at $T_a = 25$ °C

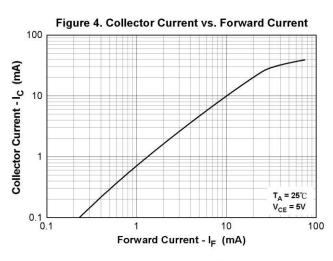


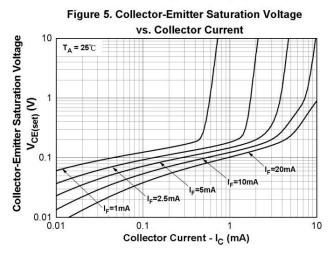
Typical Electro-Optical Characteristics Curves

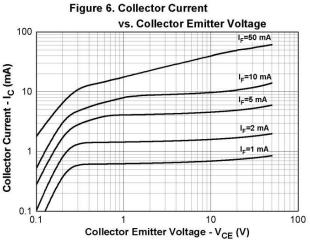


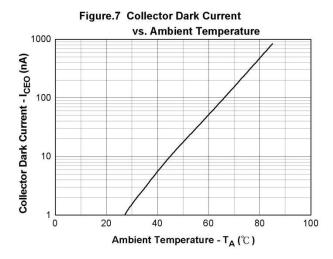


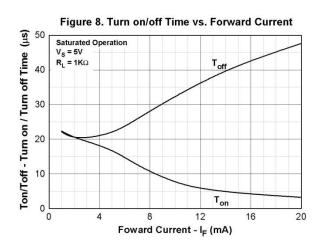


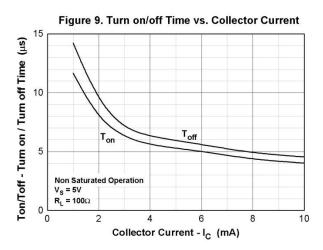












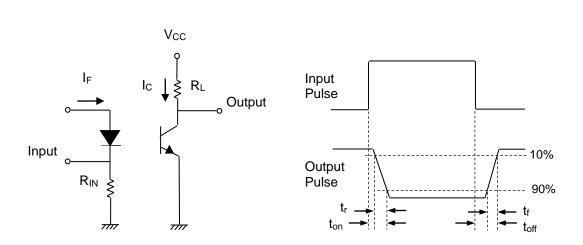


Figure 10. Switching Time Test Circuit & Waveforms



Order Information

Part Number

CNY64SX-V

Note

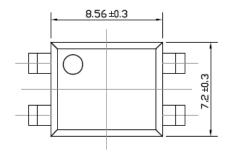
= CTR rank option (A, B or none)

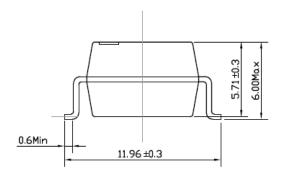
X V = VDE safety (optional)

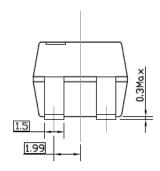
Option	Description	Packing quantity
CNY64S	Standard	60 units per tube
CNY64S-V	Standard + VDE	60 units per tube
CNY64S(TA)	Standard	500 units per tube



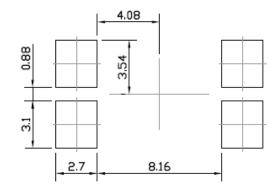
Package Drawings (Dimensions in mm)







Recommended pad layout for surface mount leadform





Device Marking



Notes

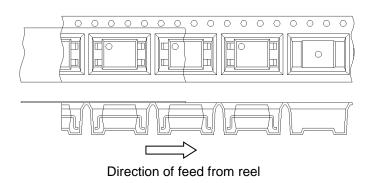
EL denotes Everlight CNY64 denotes Part no.

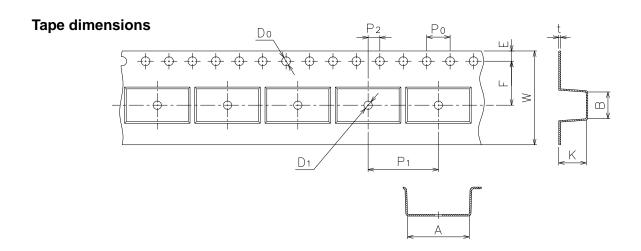
R denotes CTR rank (A or B)
Y denotes 1 digit Year code
WW denotes 2 digit Week code
V denotes VDE safety (optional)



Tape & Reel Packing Specifications

Option TA

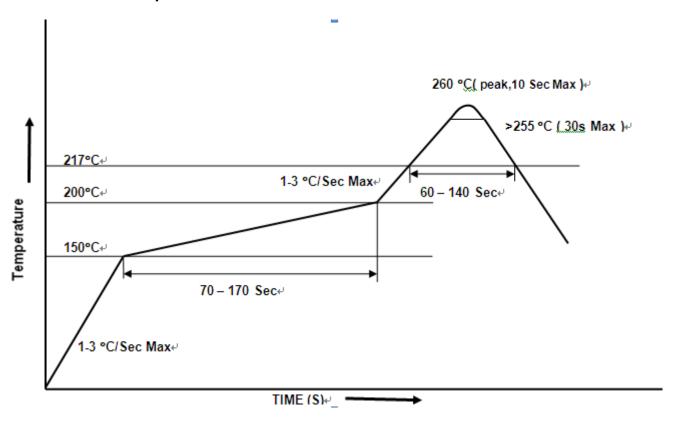




Dimension No.	Α	В	Do	D1	E	F
Dimension(mm)	12.6±0.1	6.6±0.1	1.5+0.1/-0	1.5±0.1	1.75±0.1	7.5±0.1
Dimension No.	Ро	P1	P2	t	W	К
Dimension(mm)	4.0±0.1	16.0±0.1	2.0±0.1	0.5±0.05	16.0±0.3	7.31±0.1



Solder Reflow Temperature Profile





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