

# Arrays MLCC

# ♦ Feature

- \* There is high reliability on monolithic structure of laminated layers.
- \* And its character of excellent soldering ability and soldering resistance ability is suitable for reflow soldering and peak soldering.
- \* It includes high and stable capacitance.

\* Space saving: CA can save 50% space of the PC board and improve the assembling density.

\* Provide more capacitance per volumetric area: Efficiently use the side margins and thickness. Promoting mounting efficiency. One chip of CA equals to four chips of 0603 type capacitor. So it can reduce times of picking and placing.

\* Cost saving: Reduce times for picking and placing, reduce manufacturing time, reduce the cost for manage the equipments and reduce the cost of PCB.

\* Easy to picking and placing: SMT package, easy to mounting.

\* Improve the working efficiency of the printed board: Reduce the amount of printed circuits and promote the working speed of the printed circuit.

\*Executive Standard: GB/T 21041-2007 GB/T 21042-2007

# Application

\* Applied in PCB which require strictly about space speed, such as notebook computer, PDA and portable telephone, etc.

\* CA is best suitable to use in I/O interface circuit.





# ♦ How To Order

	6124		В		1	03		к		5	500		N		-	r
		Diel	ectric Code		No Capa	ominl acitanc	ce			Ratec	I Voltage iit: V			1	Package	Styles
		Dielecti Code	<sup>ic</sup> Dielec	tric	Express Method	A	Actual Value		E	Express Method	Actual	Value		Exp Me	oress thod	Package Styles
		CG	C00		0R5		0.5			6R3	6.3	3			В	Bulk Bag
		В	X7F	2	1R0		1.0			500	50×1	10 <sup>0</sup>			т	Taping
					102	10	0×10 <sup>2</sup>			201	20×1	10 <sup>1</sup>				Package
				Note: the first two digits are significant; third digit denotes number of zeros; R=decimal point.			di d z	Note: tł igitsare si ligit denot zeros; R=c	ne first tw gnificant es numb decimal p	/0 ; third per of point.						
I	Product	Size And	Гуре									Termin	I al Matei	ial St	yles	
Size Code	L(inch	) W(inch)	Elements Inside								Te	erminat Styles	ion	Ex Me	press ethod	
6124	0.06	0.12	4									Coppe	r Ne		C	
5084	0.05	0.08	4								Te	erminat	ion		0	
5082	0.05	0.08	2								Nic Te	ckel Ba erminat	rrier ion		N	
							Capac	itano	e To	lerance						]
		Code	A	В	С		D	F	=	G	J	K	N	1	S	Z
		Toler ance	±0.05pF	±0.10	0pF ±0.25	öpF	±0.50pF	±1	%	±2%	±5%	±10%	±20	)%	-20% +50%	-20% +80%
		Note:	Note: These Capacitance tolerance A, B, C, D are just applicable the capacitance that equals to or less the						less that	an 10pF。						

# Product shape and size





## ◆ Temperature Coefficient /Characteristics

Dielectric	Reference Temperature Point	Temperature Coefficient	Operation Temperature Range
COG	20°C	<b>0</b> ±30 ppm/℃	-55℃~125℃
X7R	20°C	±15%	-55℃~125℃

Note: Nominal temperature coefficient and allowed tolerance of class I are decided by the changing of the capacitance between 20°C and 85°C. Nominal temperature coefficient of class II are decided by the temperature of 20°C.

# ◆ Capacitance Range and Operating Voltage

Dimension				6	124			
Dielectric		0	OG			Х	7R	
Rated Voltage	16V	25V	50V	100/	16V	25V	50V	100V
Capacitance								
0.5PF								
5 PF								
10 PF								
15 PF								
20 PF								
22 PF								
33 PF								
47 PF								
100 PF								
150 PF								
220 PF								
330 PF								
470 PF								
1000 PF								
2.2 nF								
3.3nF								
4.7nF								
6.8nF								
10 nF								
22 nF								
33 nF								
47 nF								
68 nF								
100 nF								
220 nF								

Note: 1, Normal production 2, We can design according to the customer requirements.



Dimension			50	34				
Dielectric		COG			X7R			
Rated Voltage	16V	25V	50V	16V	25/	50V		
Capacitance					·			
0.5PF								
5 PF								
10 PF								
15 PF								
20 PF								
22 PF								
33 PF								
47 PF								
100 PF								
150 PF								
220 PF								
330 PF								
470 PF								
1000 PF								
2.2nF								
3.3nF								
4.7nF								
6.8nF								
10 nF								
22 nF								
33 nF								
47 nF								
68 nF								
100 nF								
220nF								

Note: 1, Normal production 2, We can design according to the customer requirements.



Dimension			50	82		
Dielectric		COG			X7R	
Rated Voltage	16V	25V	50V	16V	25V	50V
Capacitance						
0.5PF						
5 PF						
10 PF						
15 PF						
20 PF						
22 PF						
33 PF						
47 PF						
100 PF						
150 PF						
220 PF						
330 PF						
470 PF						
1000 PF						
2.2nF						
3.3nF						
4.7nF						
6.8nF						
10 nF						
22 nF						
33 nF						
47 nF						
68 nF						
100 nF						
220nF						

Note: 1, I Normal production 2, We can design according to the customer requirements.



# • Reliability Test

Item		Techr	ical Spec	ification		Test	Method a	nd Rema	arks
	Class I	Shoul	d be withi	n the speci	fied	Capacitance	Meas Frequ	uring iency	Measuring Voltage
Item Capacitance Insulation Resistance (DF, tanō) Dissipation Factor Dielectric Withstanding Voltage						≤1000pF	1MHz	±10%	1.0±0.2Vrms
Capacitance	Class II	Shoul tolera	d be withi nce.	n the speci	fied	Test Temperature: 2 Test Frequency: 1K Test Voltage: 1.0±0	25℃±3℃ Hz±10% .2Vrms		
	Class I	C≤10 C>10	nF, Ri≥50 ) nF, Ri•C	000MΩ <sub>R</sub> ≥500S		Measuring Voltage:	Rated Vol	tage	
Insulation Resistance	Class II	C≤25 C>25	nF, Ri≥10 5 nF, Ri•C	0000MΩ <sub>R</sub> ≥100S		Test Humidity: ≤75% Test Temperature: 25℃±3℃ Test Current: ≤50mA			
				DF		Capacitance	Measuring Frequency		Measuring Voltage
	Class I	≤1/ (4	400+20C)	)		C<30 pF 1MHz+10% 1.0+0.2			4.0.0.00/////
(DF, tanδ)		≤0.1%				C≥30pF		±10%	1.0±0.2Vrms
Dissipation Factor	Class II	X7R	≥50V ≤2.5%	25∨ ≤3.5%	16V ≤5.0%	Test Temperature: 2 Test Frequency: 1K Test Voltage: 1.0±0			
Dielectric Withstanding Voltage	No breakdo	own or d	amage.		Measuring Voltage: Class I :300% Rated voltage Class II :250% Rated voltage Duration: 1~5s Charge/ Discharge Current: 50mA max.				
Termination Adhesion	rmination dhesion No visible damage.				As shown T force side of th seconds.	As shown in the picture , Slowly apply a T force to the porcelain body on the side of the capacitor and hold for 60+1 seconds.			T 第力工長 ▶ PCB 板 ● 面定夫
						Preheating conditio	ns:80 to 12	20℃; 10 <sup>,</sup>	~30s.
Solderability	At least 95 by new sole Visual Appe	% of the der. earance	e terminal : No visibl	electrode i	is covered	Pb-Sn soldering Solder Temperature 235±5℃ Duration: 2±0.5s	r.	Lead-fre Solder 245±5℃ Duration	ee soldering Temperature: C n: 2±0.5s



Item		Tech	nical Specification	on		Test Method and Remarks		
Resistance to Soldering Heat	Item ΔC/C DF IR Appearar terminal	Cl <pre></pre>	ass I or ±0.25PF, s larger tial value. tial value. sible damage.At overed by new s	Class II ±15%	Preheat Solder T Duratior Clean th 10X(min Recover Recover	ing conditions: 100 to 200°C; 60– Temperature: 265±5°C n: 10±1s ne capacitor with solvent and ex n.) microscope. ry Time: 24±2h. ry condition: Room temperature	120s. amine it with a	
Resistance to Flexure of Substrate (Bending Strength)	Appearar ΔC/C: Class I : Class II :	nce: No visibl ≤±5% or ±0.∜ ≪±10%	e damage. 5pF,whichever is	larger.	Test Board:PCB Warp: 1mm Speed: 1mm/sec. Unit: mm The measurement should be made with the board in the bending position. 20 + f + 105 100 + 100 100 + 10			
Temperature Cycle	Item ΔC/C	Class ≤±1% or ± whichever is	I Class 1pF, -15% arger ~+15%	<u>5 II</u> 6	Preheat Recover Initial N Cycling Step 1 2 3 4 Recover	ing conditions: up-category tempory ry time: 24±1h Aeasurement Times: 5 times, 1 cycle, 4 steps: Temperature (°C) Low- category temp: COG/X7R:-55 Normal temp : +20 Up- category temp: COG/X7R: +125 Normal temp. : +20 ry time after test:24±2h	erature, 1h (Time) 30min 2~3min 30min 2~3min	
Humidity load	ΔC/C DF IR Appea	Class I : ± Class II : ≤ Not more t Class I Class II rance: No vis	<ul> <li>7.5% or ±0.75p larger. ±12.5%</li> <li>han twice of initia Ri≥5000MΩ或 whichever is sn</li> <li>Ri≥1000MΩ或 whichever is sn</li> <li>ible damage.</li> </ul>	DF, whichever is al value. Ri•C <sub>R</sub> ≥50S naller. Ri•C <sub>R</sub> ≥10S naller.	※ Pre 140℃~1 for 24±2 Tempera Voltage: Recover Recover	treatment (Class II) : After 150℃ for 1h±10min, place at roo 2h. ature: 40±2℃ Humidity: 90~95 : Rated Voltage Duration: 500h ry conditions: Room temperature ry Time::24h±2h	preheating at om temperature %RH	



Item			Technical Specification	Test Method and Remarks
Item Life Test	Δ C/ C DF IR	Class I Class II Class I Class I Class II earance: No	Technical Specification $\leq \pm 3\%$ 或 $\pm 0.3$ pF, whichever is larger. -20% ~ +20% Not more than twice of initial value. Ri≥4000MΩ or Ri•C <sub>R</sub> ≥40S whichever is smaller. Ri≥2000MΩ or Ri•C <sub>R</sub> ≥50S whichever is smaller. visible damage.	Test Method and Remarks         ※ Pretreatment (Class II) : After preheating at 140°C~150°C for 1h±10min, place at room temperature for 24±2h.         Applied Voltage: 2*Ur, except the table 1         Duration: 1000h.         Temperature: 125°C(COG、X7R)         Charge/ Discharge Current: 50mA max.         Recovery Conditions: Room Temperature         Recovery Time: :24h±2h

Note: Pretreatment (only for class2 capacitor)

Pretreatment (only for class2 capacitor) is a method to treat the capacitor before measurement. First, place the capacitor in the up-category temperature or other specified higher temperature environment for 1hour. Then recovery the capacitor at standard pressure conditions for 24±1hours.



\* Dimensions of paper taping for 6124, 5084, 5082 types





									Unit:	mm
Code paper size	A	В	С	D*	E	F	G*	н	J	т
5082	1.45	2.30	8.0	3.50	1.75	4.00	2.00	4.00	1.50	1.10
	±0.15	± 0.15	± 0.15	± 0.05	± 0.10	±0.10	± 0.10	± 0.10	-0/+0.10	Max
5084	1.45	2.30	8.0	3.50	1.75	4.00	2.00	4.00	1.50	1.10
	±0.15	± 0.15	± 0.15	± 0.05	± 0.10	±0.10	± 0.10	± 0.10	-0/+0.10	Max
6124	1.80	3.40	8.00	3.50	1.75	4.00	2.00	4.00	1.50	1.10
	±0.20	± 0.20	± 0.20	± 0.05	± 0.10	± 0.10	± 0.10	± 0.10	-0/+0.10	Max

Note: The place with "\*" means where needs exactly dimensions.

\* Embossed taping



\* Dimensions of embossed taping for 6124, 5084, 5082 type



Code Tape size	A	В	С	D*	E	F	G*	Н	J	т
5082	1.55	2.35	8.00	3.50	1.75	4.00	2.00	4.00	1.50	1.50
	± 0.20	± 0.20	±0.20	± 0.05	± 0.10	± 0.10	± 0.10	± 0.10	-0/+0.10	Max
5084	1.55	2.35	8.00	3.50	1.75	4.00	2.00	4.00	1.50	1.50
	± 0.20	± 0.20	±0.20	± 0.05	± 0.10	± 0.10	± 0.10	± 0.10	-0/+0.10	Max
6124	1.95	3.60	8.00	3.50	1.75	4.00	2.00	4.00	1.50	1.85
	± 0.20	± 0.20	± 0.20	± 0.05	± 0.10	± 0.10	± 0.10	± 0.1	-0/+0.10	Max

Note: The place with "\*" means where needs exactly dimensions.



\* Structure of leader part and end part of the carrier paper



\* Reel dimensions (unit: mm)



- \* Taping specification: top tape peeling strength
  - \* Paper Tapin





\* Embossed Taping

Cover tape peeling direction



Standard: 0.1N < peeling strength < 0.7NNo paper dirty remains on the scotch when peeling, and sticks to top and bottom tape.

## \* Bulk Case Package

					unit:	mm
Symbol	A	В	Т	С	D	E
Dimension	6.80±0.10	8.80±1.00	12.00±0.10	15.00+0.10/-0	2.00+0/-0.10	4.70±0.10
Symbol	F	W	G	Н	L	I
Dimension	31.50+0.20/-0	36.00+0/-0.20	19.00±0.35	7.00±0.35	110.00±0.70	5.00±0.35

## \* Packing Quantity

917E	Package Style & Quantity) unit: pcs									
SIZE	(EPT)	(PT)	(ET)	(BC)	(BP)					
5082 5084		4000	3000	10000	5000					
6124		4000	T≤1.35mm 3000 T>1.35mm 2000	5000	5000					

Note: We can choose packing style and quantity can be according to the customer's requirement.

## \* Outer packing







## Storage Methods

- \* The guaranteed period for solderability is 12 months (Under deliver package condition).
- \* Storage conditions: Temperature 5~40°C Relative Humidity 20~70%

## Precautions For Use

The Multi-layer Ceramic Capacitors (MLCC) may fail in a short circuit modern in an open circuit mode when subjected to severe conditions of electrical environment and / or mechanical stress beyond the specified "rating" and specified "conditions" in the specification, which will result in burn out, flaming or glowing in the worst case. Following "precautions for "safety" and Application Notes shall be taken in your major consideration. If you have a question about the precautions for handling, please contact our engineering section or factory.

### \* Soldering Profile

To avoid the crack problem by sudden temperature change, follow the temperature profile in the adjacent graph (refer to the graph in the enclosure page).

### \* Manual Soldering

Manual soldering can pose a great risk of creating thermal cracks in capacitors. The hot soldering iron tip comes into direct contact with the end terminations, and operator's careless may cause the tip of the soldering iron to come into direct contact with the ceramic body of the capacitor. Therefore the soldering iron must be handled carefully, and pay much attention to the selection of the soldering iron tip and temperature contact of the tip.

### \*Optimum Solder Amount for Reflow Soldering



## \* Recommended Soldering amounts

The optimal solder fillet amounts for re-flow soldering



The optimal solder fillet amounts for reworking by using soldering iron









## \* Recommended Soldering Method

Size	Temperature Characteristics	Rated Voltage	Capacitance	Soldering Method
5082	C0G、X7R	/	1	R
5084	C0G、X7R	1	1	R
6124	C0G、X7R	/	1	R

Soldering method: Reflow Solering Wave Soldering

## The temperature profile for soldering

\* Re-flow soldering)



	Pb-Sn soldering	Lead-free soldering	
Peak temperature	<b>230</b> ℃~250℃	<b>240℃~260℃</b>	

While in preheating, please keep the temperature difference between soldering temperature and surface temperature of chips as:  $T \le 150$  °C.

\* Wave soldering





	Pb-Sn soldering	Lead-free soldering
Peak temperature	<b>230℃~260℃</b>	240℃~270℃

While in preheating, please keep the temperature difference between soldering temperature and surface temperature of chips as:  $T \le 150^{\circ}C$ .

\* Hand soldering

Temperature



Conditions:

Preheating	Temperature of soldering iron head	Power of soldering iron	Diameter of soldering iron head	Soldering time	Solder paste amount	Restricted conditions
∆≤130℃	Highest temperature:35 0℃	20W at the highest	1mm recommended	3s at the longest	≤1/2 chip thickness	Please avoid the derect contact between soldering iron head and ceramic components

\*The latest version prevails

单击下面可查看定价,库存,交付和生命周期等信息

>>FH(风华高科)