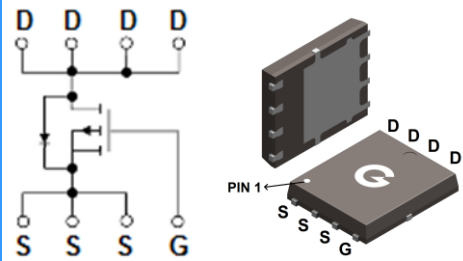


Features

- Super low gate charge
- Excellent C_{av}/d_t effect decline
- Advanced high cell density Trench technology
- JESD22-A114-B ESD rating of class 3A per human body model
- Halogen free
- Qualified to AEC-Q101 standards for high reliability

HF



Mechanical Data

- Case: PDFN5x6-8L
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

PDFN5x6-8L

Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
TBL250P06-5DL8	PDFN5x6-8L	5000 pcs / Tape & Reel	250P06

Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	-60	V
Gate-to-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current ($T_C = 25^\circ\text{C}$)	I_D	-29	A
Continuous Drain Current ($T_C = 100^\circ\text{C}$)		-18	A
Pulsed Drain Current ($t_p = 10\mu\text{s}$, $T_C = 25^\circ\text{C}$)	I_{DM}	-175	A
Single Pulse Avalanche Energy ^{*3}	E_{AS}	39	mJ
Power Dissipation ($T_C = 25^\circ\text{C}$)	P_D	40	W
Operating Junction Temperature Range	T_J	-55 ~ +150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	-	-	3.1	$^\circ\text{C}/\text{W}$
Thermal Resistance Junction-to-Air ^{*1}	$R_{\theta JA}$	-	-	25	$^\circ\text{C}/\text{W}$

Electrical Characteristics (@ T_A = 25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = -250μA	-60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -48V, V _{GS} = 0V, T _C = 25°C	-	-	-1	μA
		V _{DS} = -48V, V _{GS} = 0V, T _C = 55°C	-	-	-5	μA
		V _{DS} = -48V, V _{GS} = 0V, T _C = 125°C	-	-	-10	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
On Characteristics						
R _{DS(ON)}	Drain-Source On-resistance *2	V _{GS} = -10V, I _D = -18A	-	18	25	mΩ
		V _{GS} = -4.5V, I _D = -12A	-	23	33	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250μA	-1	-1.9	-2.5	V
R _G	Gate Resistance	V _{GS} = 0V, f = 1MHz	-	5.6	-	Ω
Dynamic Characteristics						
C _{ISS}	Input Capacitance	V _{GS} = 0V V _{DS} = -15V f = 1.0MHz	-	5309	-	pF
C _{OSS}	Output Capacitance					
C _{RSS}	Reverse Transfer Capacitance					
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time *4	V _{GS} = -10V V _{DD} = -15V I _D = -1A R _G = 3.3Ω	-	38	-	ns
t _r	Turn-on Rise Time *4					
t _{d(OFF)}	Turn-Off Delay Time *4					
t _f	Turn-Off Fall Time *4					
Q _G	Total Gate-Charge	V _{GS} = -4.5V V _{DD} = -12V I _D = -12A	-	31.5	-	nC
Q _{GS}	Gate to Source Charge					
Q _{GD}	Gate to Drain (Miller) Charge					
Source-Drain Diode Characteristics						
V _{SD}	Diode Forward Voltage *2	I _{SD} = -1A, V _{GS} = 0V	-	-0.7	-1.2	V
t _{rr}	Reverse Recovery Time	I _F = -5A, V _{GS} = 0V diF/di = 100A/μs	-	34.5	-	ns
Q _{rr}	Reverse Recovery Charge		-	34.6	-	nC

Notes:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
2. The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%
3. The E_{AS} data shows Max. rating. The test condition is V_{DD} = -30V, V_{GS} = -10V, L = 0.1mH
4. Guaranteed by design, not subject to production testing

Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

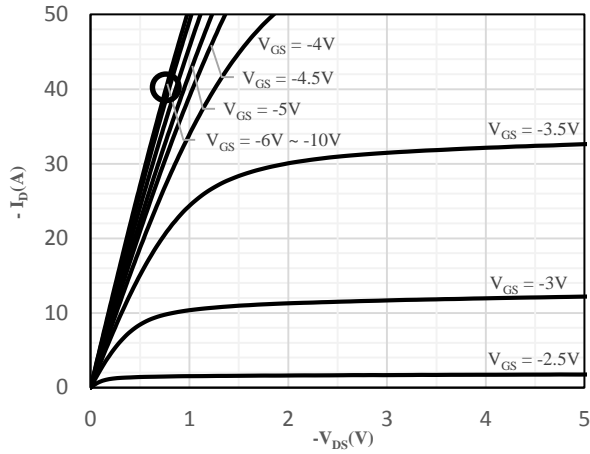


Fig 1 Typical Output Characteristics

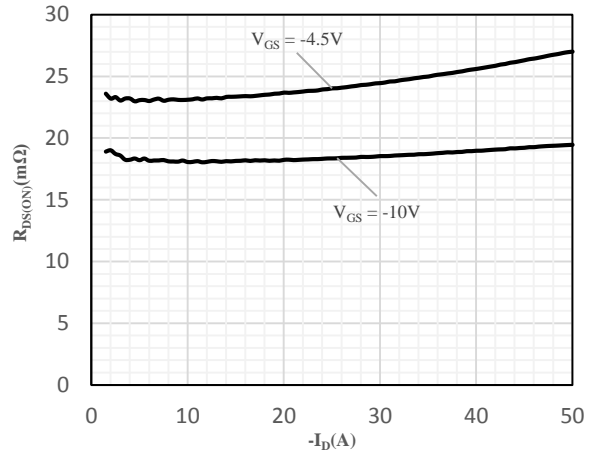


Fig 2 On-Resistance vs. Drain Current and Gate Voltage

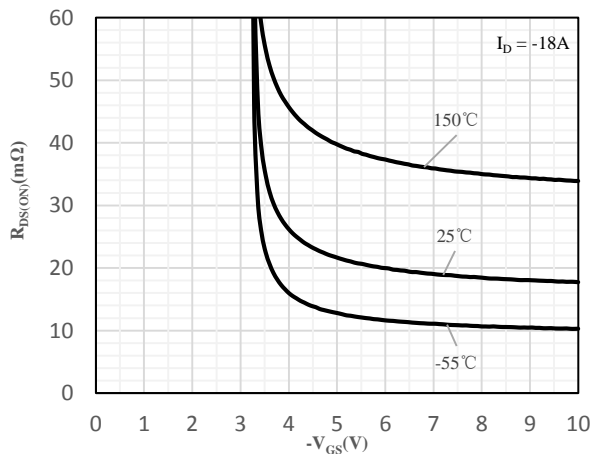


Fig 3 On-Resistance vs. Gate-Source Voltage

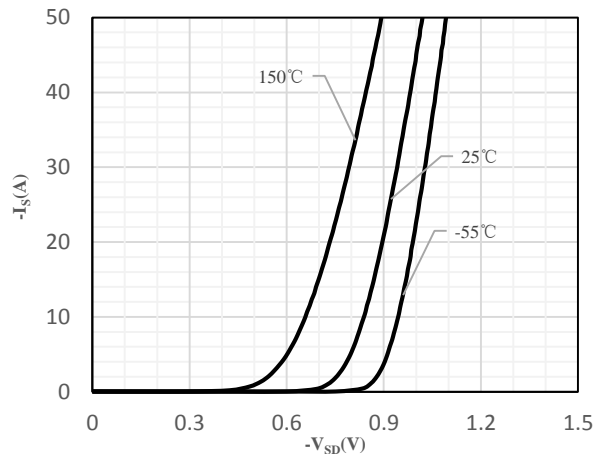


Fig 4 Body-Diode Characteristics

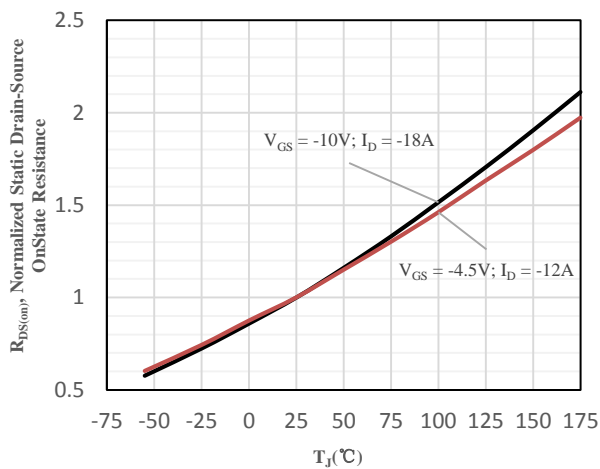


Fig 5 Normalized On-Resistance vs. Junction Temperature

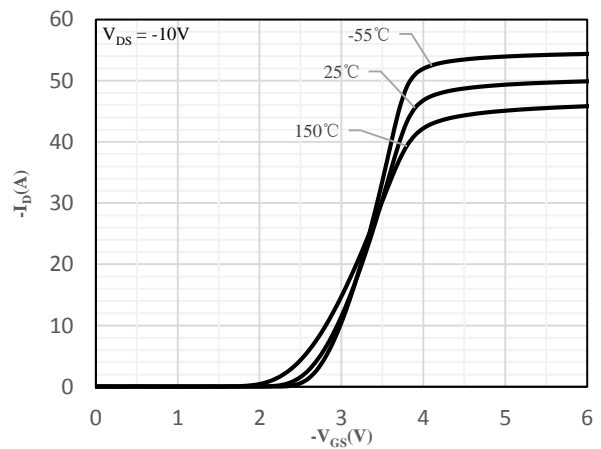


Fig 6 Transfer Characteristics

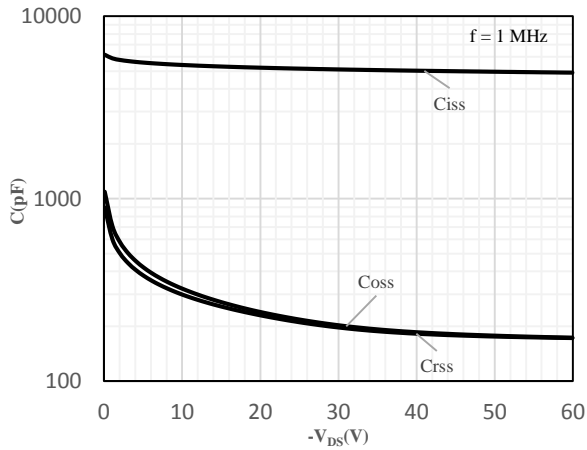


Fig 7 Capacitance Characteristics

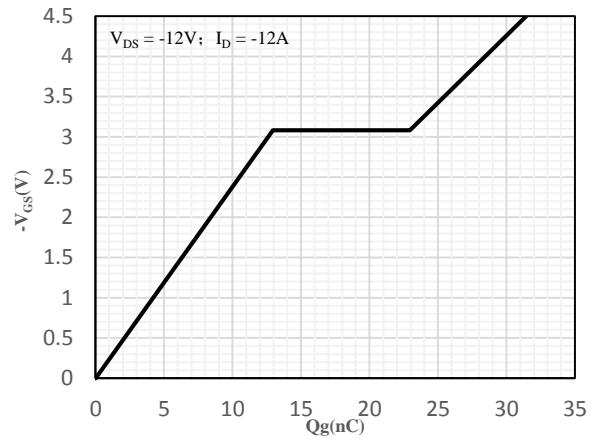


Fig 8 Gate-Charge Characteristics

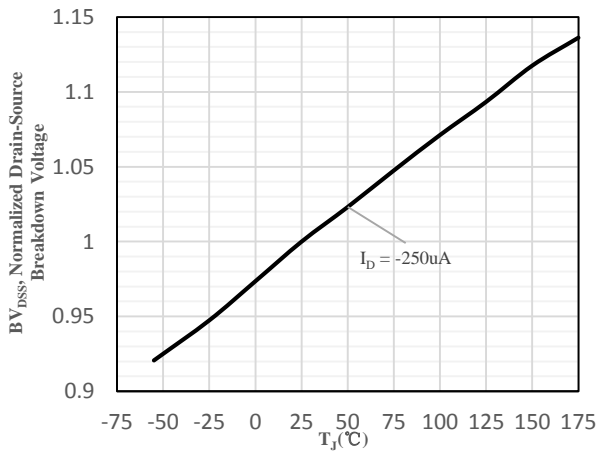


Fig 9 Normalized Breakdown Voltage vs. Junction Temperature

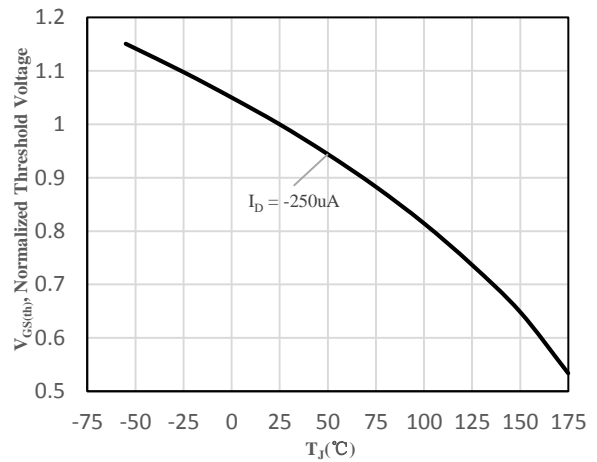


Fig 10 Normalized $V_{GS(th)}$ vs. Junction Temperature

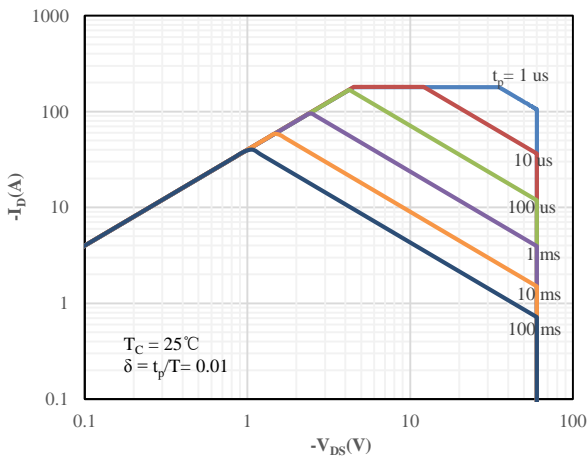


Fig 11 Safe Operation

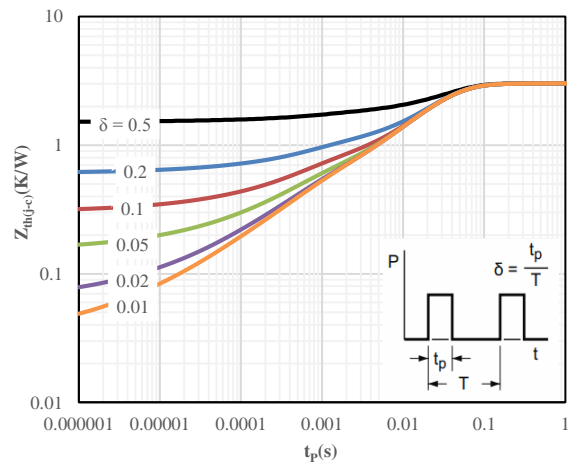
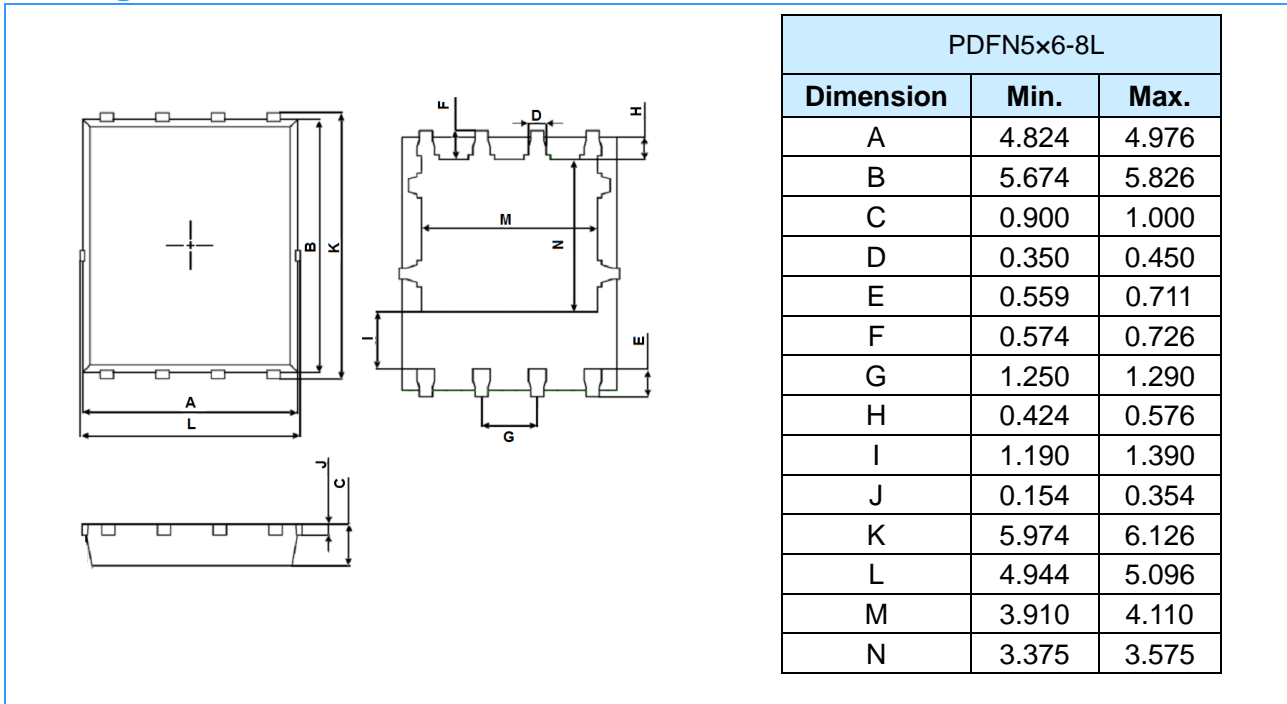
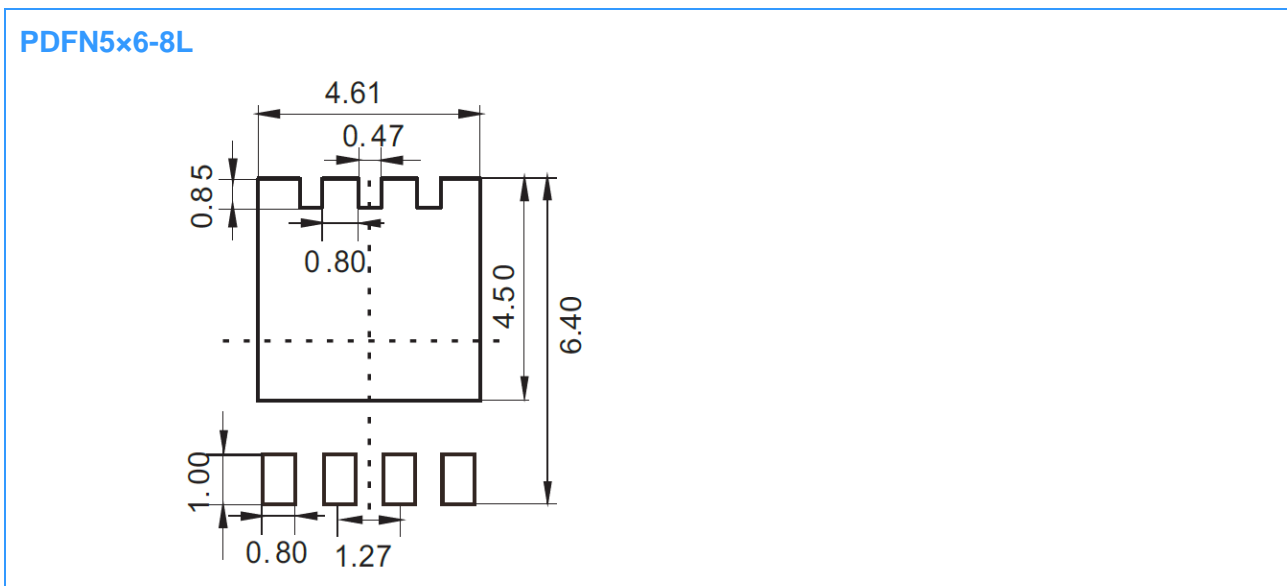


Fig 12 Maximum transient thermal impedance

Package Outline Dimensions (Unit: mm)



Mounting Pad Layout (Unit: mm)



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