# **GLF71301T**



# Nano-Current Consumed, I<sub>Q</sub>Smart<sup>™</sup> LoadSwitch with Slew Rate Control

**Product Brief** 

### **DESCRIPTION**

The GLF71301T is an ultra-efficiency, 2.0 A rated, Load Switch with integrated slew rate control. The best in class efficiency makes it an ideal choice for use in IoT, mobile, and wearable electronics.

The GLF71301T features an ultra-efficient  $I_QSmart^{TM}$  technology that supports the lowest quiescent current ( $I_Q$ ) and shutdown current ( $I_{SD}$ ) in the industry. Low  $I_Q$  and  $I_{SD}$  solutions help designers to reduce parasitic leakage current, improve system efficiency, and increase battery lifetime.

The GLF71301T integrated slew rate control can also enhance system reliability by mitigating bus voltage swings during switching events. Where uncontrolled switches can generate high inrush currents that result in voltage droop and/or bus reset events, the GLF slew rate control specifically limits inrush current during turn-on to minimize voltage droop.

GLF71301T Load Switch device supports an industry leading wide input voltage range and helps to improve operating life and system robustness. Furthermore, one device can be used in multiple voltage rail applications which helps to simplify inventory management and reduces operating cost.

GLF71301T Load Switch device is small utilizing a wafer level chip scale package with 4 bumps in a 0.77 mm x 0.77 mm x 0.35 mm die size and a 0.4 mm bump pitch.

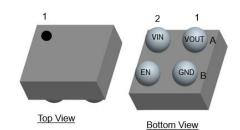
## **FEATURES**

- Ultra-Low IQ: 1 nA Typ @ 5.5 VIN
- Ultra-Low I<sub>SD</sub>: 19 nA Typ @ 5.5 V<sub>IN</sub>
- Low  $R_{ON} = 34 \text{ m}\Omega$  Typ. @ 5.5  $V_{IN}$
- I<sub>OUT</sub> Max = 2.0 A
- Supply Voltage Range: 1.1 V to 5.5 V
  6 V abs max
- Controlled Rise Time: 430 us at 3.3 V<sub>IN</sub>
- Internal EN Pull-Down Resistor
- Integrated Output Discharge Switch
- Ultra-Small: 0.77 mm x 0.77 mm

### **APPLICATIONS**

- Wearables
- Data Storage, SSD
- Mobile Devices
- Low Power Subsystems

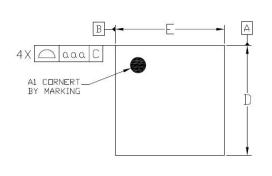
## PACKAGE

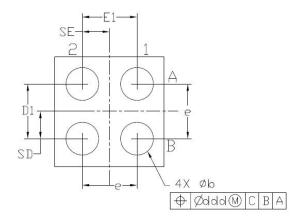


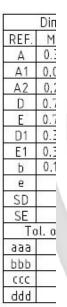
0.77 mm x 0.77 mm x 0.35 mm 0.4 mm pitch WLCSP

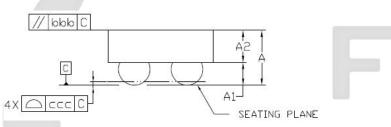


## **ULTRA-THIN PACKAGE OUTLINE**









# INTEGRATED POWER

## Notes

- 1. ALL DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
- 2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M-1994.

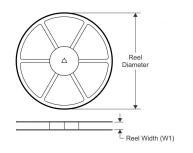


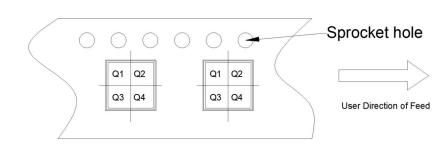
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### TAPE AND REEL INFORMATION

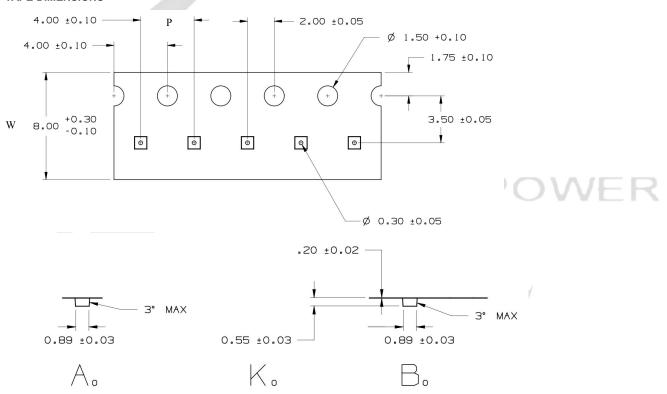
### **REEL DIMENSIONS**

### **QUADRANT ASSIGNMENTS PIN 1 ORIENTATION TAPE**





#### TAPE DIMENSIONS



Device	Package	Pins	SPQ	Reel Diameter(mm)	Reel Width W1	Α0	В0	K0	Р	w	Pin1
GLF71301T	WLCSP	4	4000	180	9	0.89	0.89	0.55	4	8	Q1

#### Remark:

- A0: Dimension designed to accommodate the component width
- B0: Dimension designed to accommodate the component length
- C0: Dimension designed to accommodate the component thickness
- W: Overall width of the carrier tape
- P: Pitch between successive cavity centers

# 单击下面可查看定价,库存,交付和生命周期等信息

# >>GLF(杰夫微)