

GLF79110 Ultra-low Quiescent Current Linear Voltage Regulator

**Product Brief** 

# DESCRIPTION

The GLF79110 are a family of ultra-efficient I<sub>Q</sub>Smart<sup>™</sup> low dropout regulators designed for wearables and IoT devices which require ultra-low quiescent current.

The GLF79110 provides up to 150 mA output current with typical quiescent current of only 0.8  $\mu$ A. In shutdown mode, the GLF79110 only draws a typical 4 nA from the supply. The typical dropout voltage is 62 mV or less in each output voltage rating.

The GLF79110 also provides short circuit current limiting of 250 mA and fast transient response from zero to maximum load. The combination of low power, protection features and small size make the GLF79110 ideal for portable and battery powered applications.

The GLF79110 is available in 0.97 mm x 0.97 mm x 0.55 mm wafer level chip scale package (WLCSP).

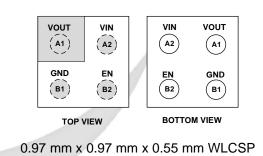
# FEATURES

- Ultra-Low Quiescent Current, Iq:
  - 0.8 µA Typ @ no load
- Ultra-Low Shutdown Current, I<sub>SD</sub>:
  - 10 nA Max @ V<sub>OUT(NOM)</sub> + 1 V
- Output Voltage Accuracy: ± 2.5 %
- Low Dropout: 62 mV Typ at 150 mA
- 150 mA Guaranteed Output Current
- Over Current Protection and Current Foldback
- Compatible with Ceramic Capacitor
- Operating Temperature Range: -40 to 85 °C
- HBM: 6 kV, CDM: 2 kV
- Ultra-Small: 0.97 mm x 0.97 mm WLCSP

# **APPLICATIONS**

- Mobiles / Wearables /IoT Devices
- Battery Powered Smart Devices
- Medical Devices

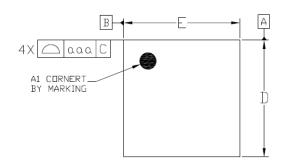
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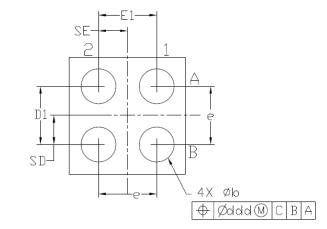


# **PACKAGE OUTLINE**

INTEGRATED POWER

GI F





Nom.

0.250

0.300

0.10

0.10

0.05

0.05

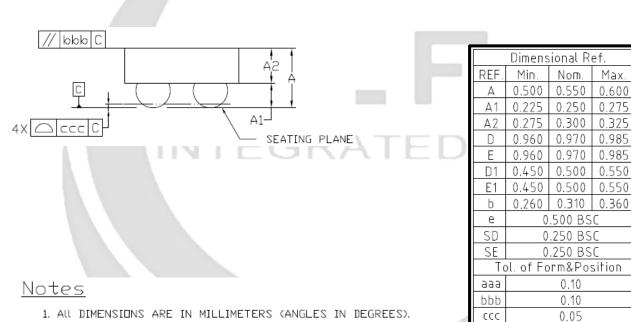
ddd

Max

0.275

0.325

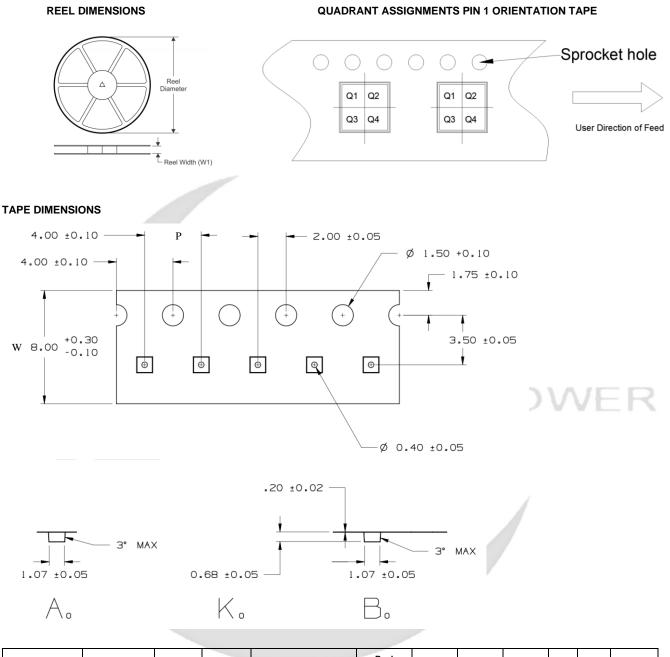
0.985



2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M-1994.

# TAPE AND REEL INFORMATION

INTEGRATED



Device	Package	Pins	SPQ	Reel Diameter(mm)	Reel Width W1	A0	В0	KO	Ρ	w	Pin1
GLF79110-18AS	WLCSP	4	3000	180	9	1.07	1.07	0.68	4	8	Q1
GLF79110-28AS	WLCSP	4	3000	180	9	1.07	1.07	0.68	4	8	Q1
GLF79110-30AS	WLCSP	4	3000	180	9	1.07	1.07	0.68	4	8	Q1

#### Remark:

A0: Dimension designed to accommodate the component width

B0: Dimension designed to accommodate the component length

C0: Dimension designed to accommodate the component thickness

W: Overall width of the carrier tape

P: Pitch between successive cavity centers

单击下面可查看定价,库存,交付和生命周期等信息

>>GLF(杰夫微)