

GLF72511

December 3 A Ultra Low Current Consumption N-channel Load Switch with Lower Input Voltage Range and Reverse Current Blocking

Product Brief

DESCRIPTION

The GLF72511 Load Switch is a fully integrated 2A NMOS load switch with I_QSmart^{TM} advanced technology. The device is targeted for the mobile computing and data storage markets as a high performance, low cost solution for load switch applications.

The GLF72511 has a constant low on-resistance of 27 m Ω at room temperature. The fixed rise time helps prevent undesirable inrush current when turned on and the internal EN pin pulldown resistor ensures the device remains in the shutdown mode when disabled. In shutdown mode the GLF72511 draws only 6 nA typical at 3.6 V input supply voltage.

The GLF72511 is available in a wafer level chip scale package (WLCSP) measuring 0.97 mm x 0.97 mm x 0.55mm with a 0.5 mm pitch. This allows the user to save board space and increase cost savings.

The GLF72511 features a reverse current blocking protection. When the GLF72511 is disabled, it prevents reverse current flowing from the output to the input source.

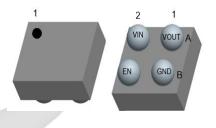
FEATURES

- Supply Voltage Range : 0.8 V to 3.6 V
- Low R_{ON} : 27 m Ω Typ at Supply Voltage Range
- I_{OUT} Max : 3 A
- Ultra-Low I_Q :
 - $\circ~~$ 50 nA Typ at 0.8 V_{IN}
 - \circ ~60 nA Typ at 1.0 V_{IN}
 - \circ 80 nA Typ at 1.2 V_{IN}
- Integrated Slew Rate Control Driver
- Reverse Current Blocking Protection When Disabled
- Internal EN Pull-Down Resistor
- Integrated Output Discharge Switch
- HBM : 6 kV, CDM : 2 kV
- Ultra-Small : 0.97mm x 0.97mm x 0.55mm Wafer Level Chip Scale Package

APPLICATIONS

- Wearables
- Data Storage, SSD
- Low Power Subsystems

PACKAGE

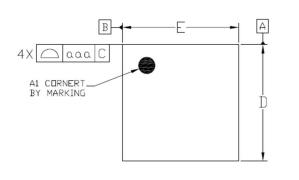


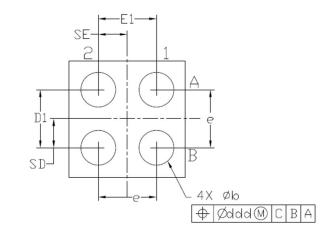
0.97 mm x 0.97 mm x 0.55 mm WLCSP

www.glfipower.com

Downloaded From Oneyac.com

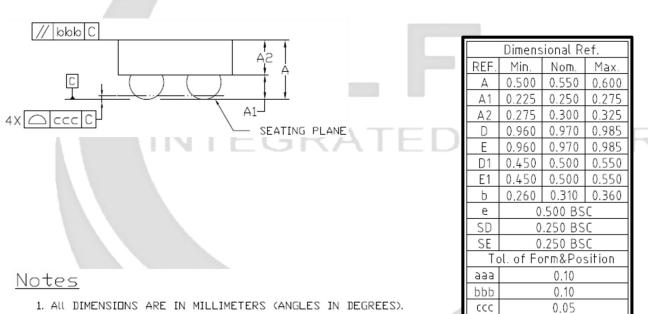
PACKAGE OUTLINE





ddd

0.05

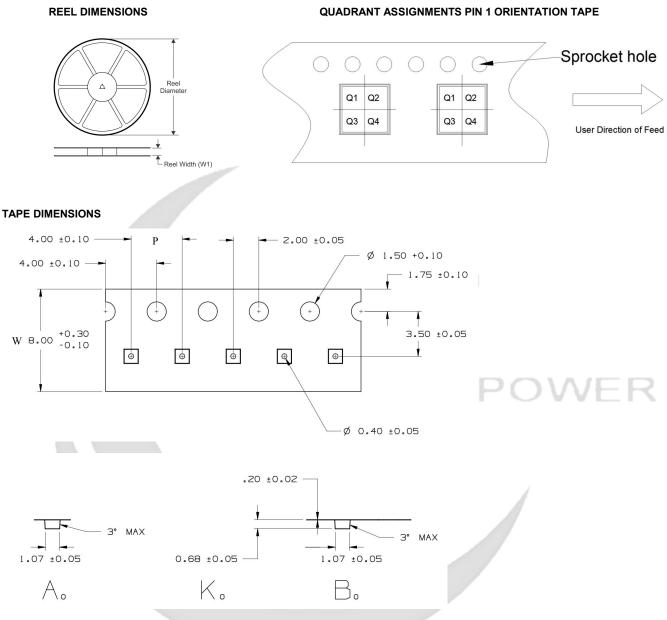


2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M-1994.

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TAPE AND REEL INFORMATION



Device	Package	Pins	SPQ	Reel Diameter(mm)	Reel Width W1	A0	B0	К0	Р	w	Pin1
GLF72511	WLCSP	4	3000	180	9	1.07	1.07	0.68	4	8	Q1

Remark:

A0: Dimension designed to accommodate the component width

- B0: Dimension designed to accommodate the component length
- C0: Dimension designed to accommodate the component thickness
- W: Overall width of the carrier tape
- P: Pitch between successive cavity centers

单击下面可查看定价,库存,交付和生命周期等信息

>>GLF(杰夫微)