



Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
30V	7.5mΩ@10V	45A
	10.5mΩ@4.5V	
-30V	13mΩ@-10V	-35A
	20mΩ@-4.5V	

Feature

- Trench Technology Power MOSFET
- Low $R_{DS(ON)}$
- Low Gate Charge
- Low Gate Resistance

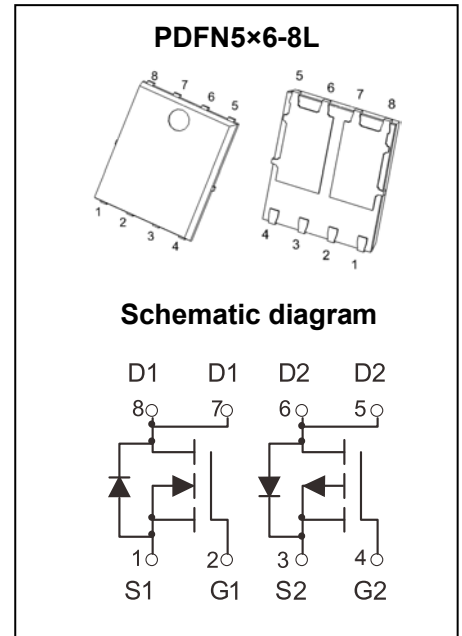
Application

- DC/DC Converter
- Motor Drive

MARKING:



30NP07 = Device Code
 XX = Data Code
 Solid Dot = Green Device Indicator



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	NMOS	PMOS	Unit
Drain - Source Voltage	V_{DS}	30	-30	V
Gate - Source Voltage	V_{GS}	±20	±20	V
Continuous Drain Current ¹	I_D	45	-35	A
$T_C = 25^\circ\text{C}$				
Pulsed Drain Current ²	I_{DM}	180	-140	A
Power Dissipation ⁴	P_D	25	28	W
Thermal Resistance from Junction to Ambient ⁵	$R_{\theta JA}$	50	50	$^\circ\text{C/W}$
Thermal Resistance from Junction to Case	$R_{\theta JC}$	5	4.5	$^\circ\text{C/W}$
Junction Temperature	T_J	150	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55~ +150	-55~ +150	$^\circ\text{C}$

MOSFET ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted)
NMOS:

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Off Characteristics						
Drain - Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24V, V_{GS} = 0V$			1	μA
Gate - Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 100	nA
On Characteristics³						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1	1.6	2.5	V
Drain-source On-resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 20A$		7.5	9	m Ω
		$V_{GS} = 4.5V, I_D = 20A$		10.5	13	
Forward transconductance	g_{FS}	$V_{DS} = 5V, I_D = 10A$		13		S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS} = 15V, V_{GS} = 0V, f = 1MHz$		1078		pF
Output Capacitance	C_{oss}			144		
Reverse Transfer Capacitance	C_{rss}			126		
Gate Resistance	R_g	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$		3		Ω
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS} = 15V, V_{GS} = 10V, I_D = 20A$		17		pC
Gate-source Charge	Q_{gs}			3.5		
Gate-drain Charge	Q_{gd}			6		
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 15V, V_{GS} = 10V, R_L = 1\Omega$ $R_G = 3\Omega$		8.5		ns
Turn-on Rise Time	t_r			7		
Turn-off Delay Time	$t_{d(off)}$			18		
Turn-off Fall Time	t_f			8		
Source - Drain Diode Characteristics						
Diode Forward Voltage ³	V_{SD}	$V_{GS} = 0V, I_S = 15A$			1.2	V

PMOS:

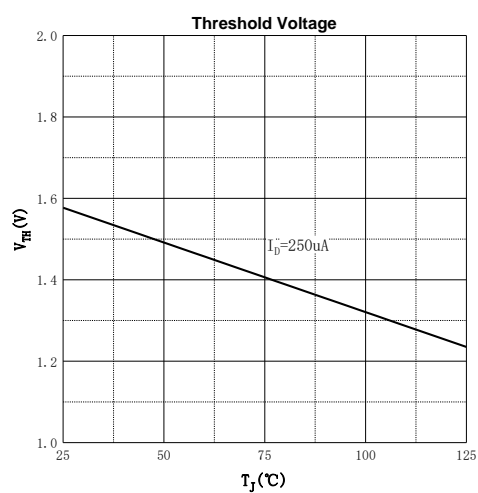
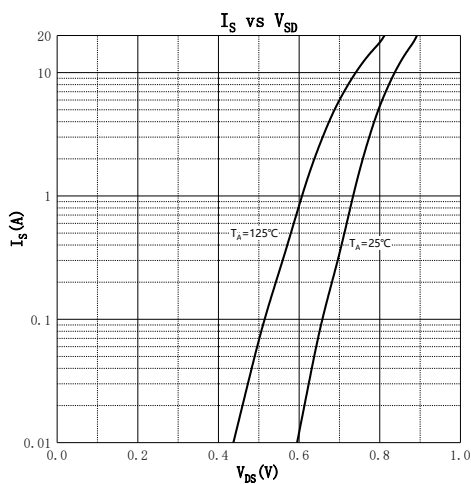
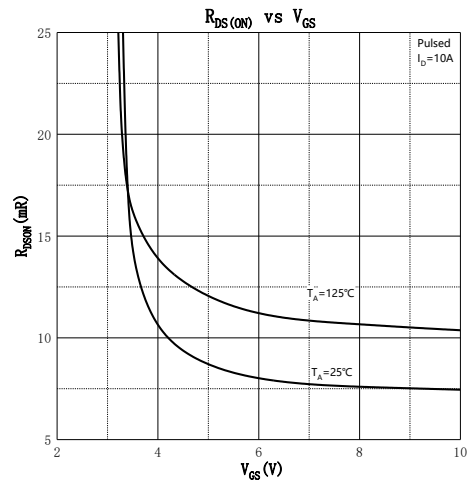
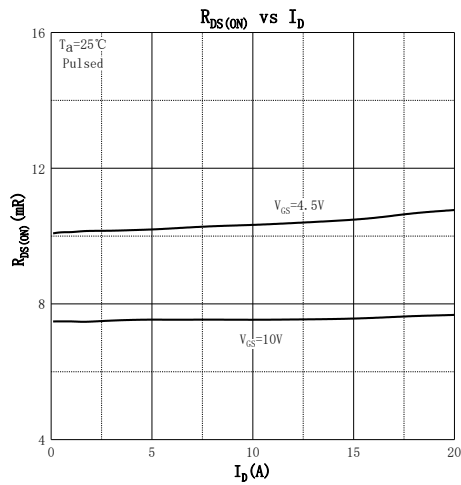
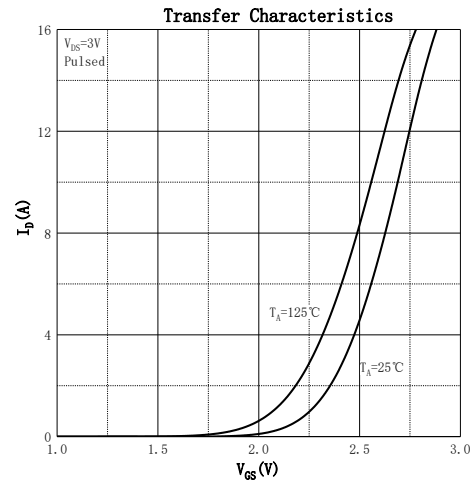
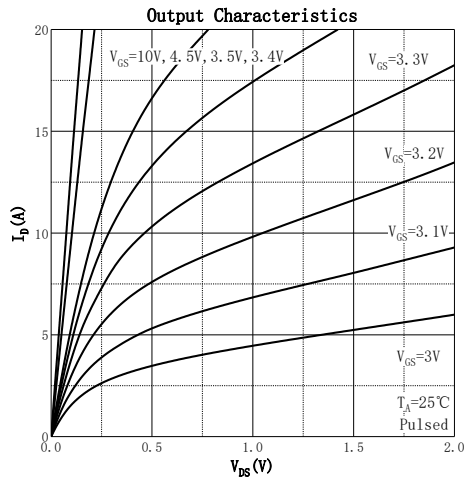
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Off Characteristics						
Drain - Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -24V, V_{GS} = 0V$			-1	μA
Gate - Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 100	nA
On Characteristics³						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1	-1.5	-3.0	V
Drain-source On-resistance	$R_{DS(on)}$	$V_{GS} = -10V, I_D = -20A$		13	16	m Ω
		$V_{GS} = -4.5V, I_D = -15A$		20	28	
Forward transconductance	g_{FS}	$V_{DS} = -10V, I_D = -9A$		16		S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS} = -15V, V_{GS} = 0V, f = 1MHz$		1381		pF
Output Capacitance	C_{oss}			245		
Reverse Transfer Capacitance	C_{rss}			20		
Gate Resistance	R_g	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$		4		Ω
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS} = -15V, V_{GS} = -10V, I_D = -20A$		27		pC
Gate-source Charge	Q_{gs}			9		
Gate-drain Charge	Q_{gd}			13		
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = -15V, V_{GS} = -10V, R_L = 1\Omega$ $R_G = 3\Omega$		8		ns
Turn-on Rise Time	t_r			20		
Turn-off Delay Time	$t_{d(off)}$			36		
Turn-off Fall Time	t_f			20		
Source - Drain Diode Characteristics						
Diode Forward Voltage ³	V_{SD}	$V_{GS} = 0V, I_S = -15A$			-1.2	V

Notes :

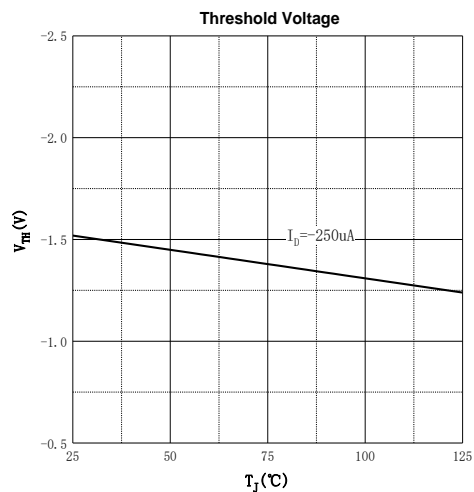
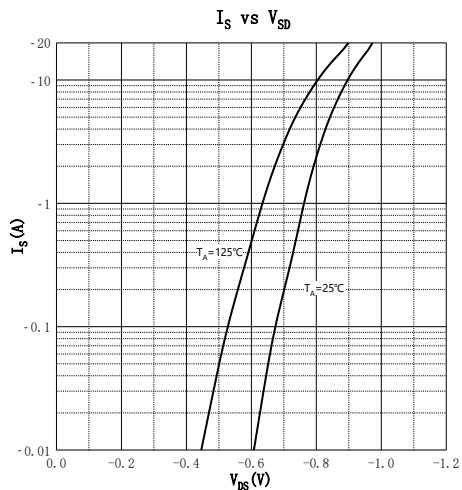
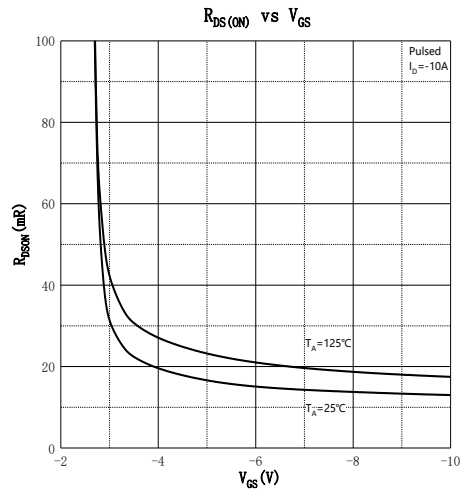
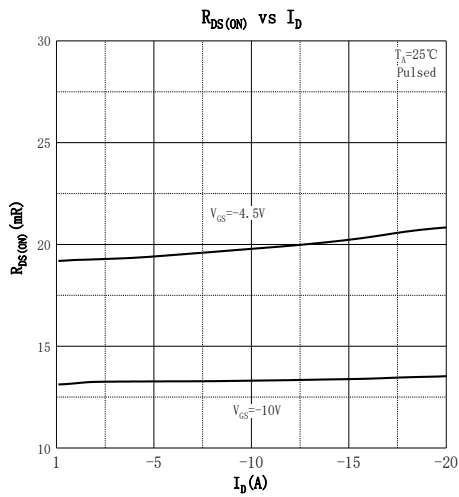
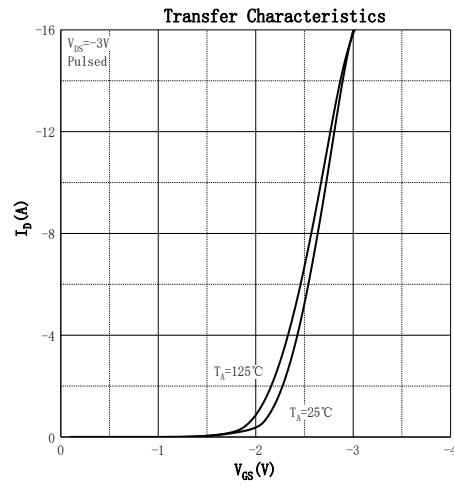
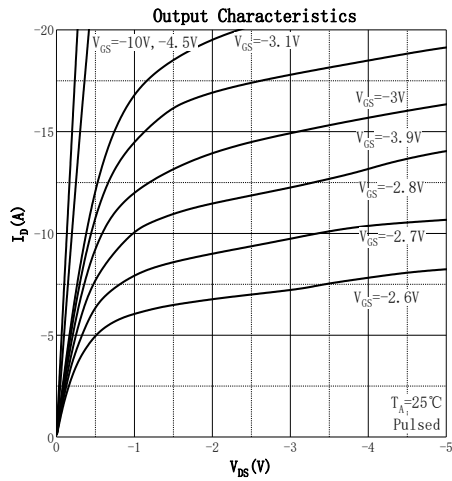
- 1.The maximum current rating is limited by package.And device mounted on a large heatsink
- 2.Pulse Test : Pulse Width $\leq 10\mu s$, duty cycle $\leq 1\%$.
- 3.Pulse Test : Pulse Width $\leq 300\mu s$, duty cycle $\leq 2\%$.
- 4.The power dissipation P_D is limited by $T_{J(MAX)} = 150^\circ C$.And device mounted on a large heatsink
- 5.Device mounted on $1in^2$ FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ C$.

Typical Characteristics

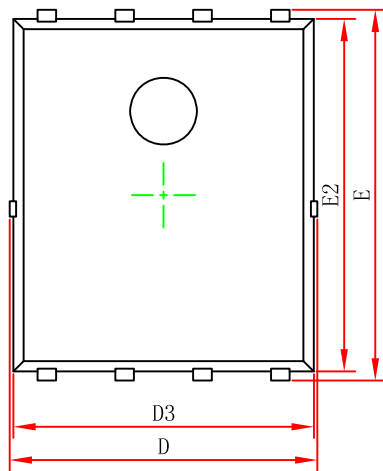
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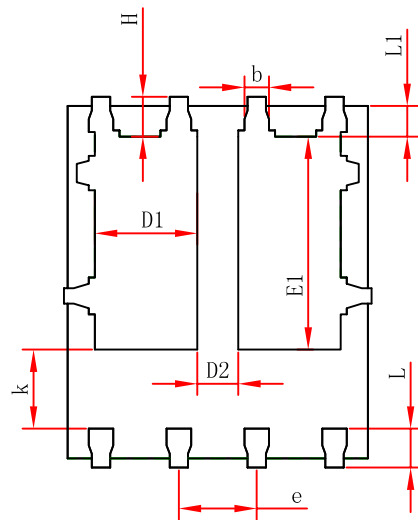
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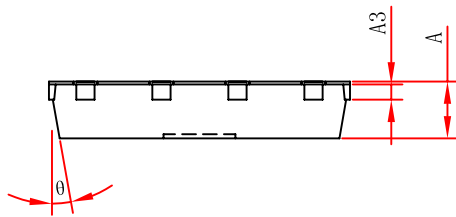
PDFN5×6-8L Package Information



Top View



Bottom View



Side View

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254 REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	1.470	1.870	0.058	0.074
D2	0.470	0.870	0.019	0.034
E1	3.375	3.575	0.133	0.141
D3	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°

单击下面可查看定价，库存，交付和生命周期等信息

[>>GP\(格瑞宝\)](#)