



# **Dual Operational Amplifier**

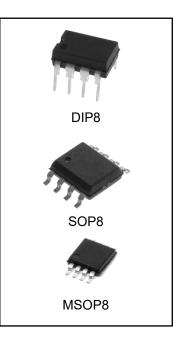
### **FEATURES**

- No Frequency Compensation Required.
- Short-Circuit Protection.
- Wide Common-Mode and Differential Voltage Ranges.
- Low-Power Consumption.
- No Latch Up When Input Common Mode Range is Exceeded.

#### DESCRIPTION

The LM1458 and the LM1558 are general purpose dual operational amplifiers. The two amplifiers share a common bias network and power supply leads. Otherwise, their operation is completely independent.

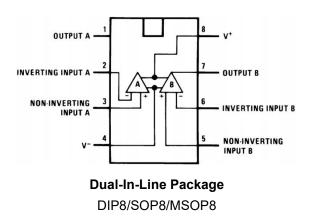
The LM1458 is identical to the LM1558 except that the LM1458 has its specifications guaranteed over the temperature range from  $0^{\circ}$ C to +70°C instead of -55°C to +125°C.



#### **ORDERING INFORMATION**

DEVICE	Package Type	MARKING	Packing	Packing Qty
LM1458N	DIP8	LM1458	TUBE	2000pcs/box
LM1458M/TR	SOP8	LM1458	REEL	2500pcs/reel
LM1458MM/TR	MSOP8	LM1458	REEL	3000pcs/reel
LM1558N	DIP8	LM1558	TUBE	2000pcs/box
LM1558M/TR	SOP8	LM1558	REEL	2500pcs/reel
LM1558MM/TR	MSOP8	LM1558	REEL	3000pcs/reel

## **Connection Diagram**





### **Absolute Maximum Ratings**

Supply Voltage	
LM1558	±22V
LM1458	±18V
Power Dissipation	
LM1558H/LM1458H	500 mW
LM1458N	400 mW
Differential Input Voltage	±30V
Input Voltage	±15V
Output Short-Circuit Duration	Continuous
Operating Temperature Range	
LM1558	−55°C to +125°C
LM1458	0°C to +70°C
Storage Temperature Range	−65°C to +150°C
Lead Temperature (Soldering, 10 sec.)	260°C
Soldering Information:	
DIP Package:Soldering (10 seconds)	260°C
SOP Package:Vapor Phase (60 seconds)	215°C
SOP Package:Infrared (15 seconds)	220°C
See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability mount devices.	" for other methods of soldering surface
ESD tolerance	300V

1. "Absolute Maximum Ratings" indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits.

2. The maximum junction temperature of the LM1558 is 150°C, while that of the LM1458 is 100°C. For operating at elevated temperatures, devices in the LMC package must be derated based on a thermal resistance of 150°C/W, junction to ambient or 20°C/W, junction to case. For the PDIP the device must be derated based on a thermal resistance of 187°C/W, junction to ambient.

3. For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.

4. Human body model,  $1.5 \text{ k}\Omega$  in series with 100 pF.



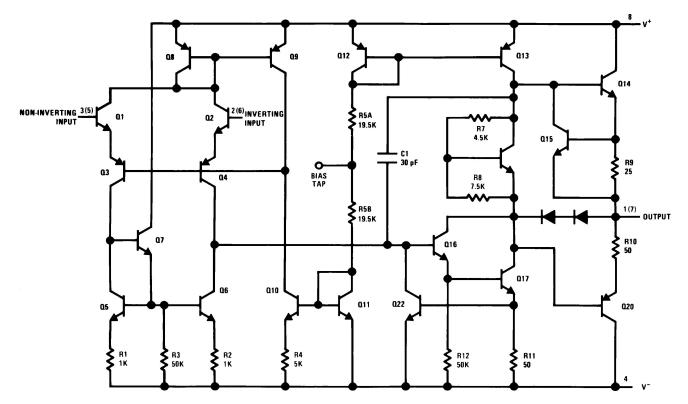
## **Electrical Characteristics**<sup>(1)</sup>

Parameter	Conditions		LM155	B	I	Units		
i arameter	Conditions	Min	Тур	Max	Min	Тур	Max	Onits
Input Offset Voltage	T <sub>A</sub> = 25°C, R <sub>S</sub> ≤ 10 kΩ		1.0	5.0		1.0	6.0	mV
Input Offset Current	T <sub>A</sub> = 25°C		80	200		80	200	nA
Input Bias Current	T <sub>A</sub> = 25°C		200	500		200	500	nA
Input Resistance	T <sub>A</sub> = 25°C	0.3	1.0		0.3	1.0		MΩ
Supply Current BothAmplifiers	T <sub>A</sub> = 25°C, V <sub>S</sub> = ±15V		3.0	5.0		3.0	5.6	mA
Large Signal Voltage Gain	$ \begin{array}{l} T_{\text{A}} = 25^{\circ}\text{C}, \ V_{\text{S}} = \pm 15\text{V} \\ V_{\text{OUT}} = \pm 10\text{V}, \ R_{\text{L}} \geq 2 \ k\Omega \end{array} $	50	160		20	160		V/mV
Input Offset Voltage	Rs ≤ 10 kΩ			6.0			7.5	mV
Input Offset Current				500			300	nA
Input Bias Current				1.5			0.8	μA
Large Signal Voltage Gain	$V_S = \pm 15V$ , $V_{OUT} = \pm 10V$ $R_L \ge k\Omega$	25			15			V/mV
Output Voltage Swing	$V_{s}$ = ±15V, $R_{L}$ = 10 k $\Omega$	±12	±14		±12	±14		V
Output Voltage Swing	$R_L = 2 k\Omega$	±10	±13		±10	±13		V
Input Voltage Range	Vs = ±15V	±12			±12			V
Common ModeRejection Ratio	R <sub>s</sub> ≤ 10 kΩ	70	90		70	90		dB
Supply VoltageRejection Ratio	R <sub>s</sub> ≤ 10 kΩ	77	96		77	96		dB

(1) These specifications apply for VS =  $\pm 15V$  and  $-55^{\circ}C \leq TA \leq 125^{\circ}C$ , unless otherwise specified. With the LM1458, however, all specifications are limited to  $0^{\circ}C \leq T_A \leq 70^{\circ}C$  and VS =  $\pm 15V$ .



#### SCHEMATIC DIAGRAM

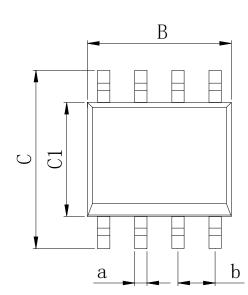


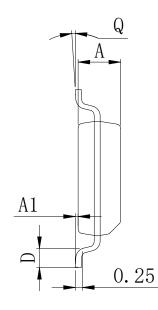
Numbers in parentheses are pin numbers for amplifier B.



## **Physical Dimensions**

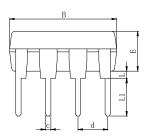
SOP8



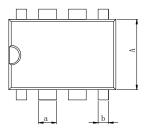


Dimensions In Millimeters(SOP8)										
Symbol:	А	A1	В	С	C1	D	Q	а	b	
Min:	1.35	0.05	4.90	5.80	3.80	0.40	0°	0.35	1.27 BSC	
Max:	1.55	0.20	5.10	6.20	4.00	0.80	8°	0.45	1.27 030	

DIP8







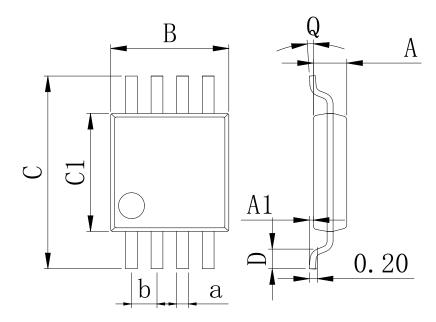
Dimensions In Millimeters(DIP8)											
Symbol:	А	В	D	D1	Е	L	L1	а	b	с	d
Min:	6.10	9.00	8.40	7.42	3.10	0.50	3.00	1.50	0.85	0.40	2.54 BSC
Max:	6.68	9.50	9.00	7.82	3.55	0.70	3.60	1.55	0.90	0.50	2.34 830

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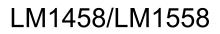


# **Physical Dimensions**

MSOP8



Dimensions In Millimeters(MSOP8)										
Symbol:	А	A1	В	С	C1	D	Q	а	b	
Min:	0.80	0.05	2.90	4.75	2.90	0.35	0°	0.25	0.65 BSC	
Max:	0.90	0.20	3.10	5.05	3.10	0.75	8°	0.35	0.00 650	





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