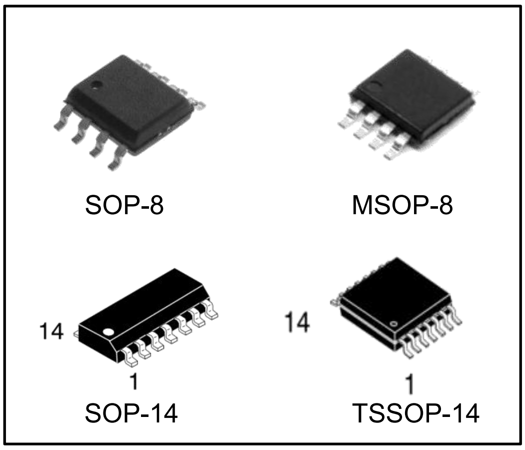
3MHz CMOS Rail-to-Rail IO Opamps

Features

- Single-Supply Operation from +2.1V ~ +5.5V
- Rail-to-Rail Input / Output
- Gain-Bandwidth Product: 3MHz (Typ)
- Low Input Bias Current: 1pA (Typ)
- Low Offset Voltage: 3.5mV (Max)
- Quiescent Current: 250μA per Amplifier (Typ)
- Operating Temperature: -40°C ~ +125°C
- Small Package:
- LMV552 Available in SOP-8 and MSOP-8 Packages
- LMV554 Available in SOP-14 and TSSOP-14 Packages



Ordering Information

DEVICE	Package Type	MARKING	Packing	Packing Qty
LMV552M/TR	SOP-8	LMV552	REEL	2500pcs/Reel
LMV552MM/TR	MSOP-8	LMV552,V552	REEL	3000pcs/Reel
LMV554M/TR	SOP-14	LMV554	REEL	2500pcs/Reel
LMV554MT/TR	TSSOP-14	LMV554	REEL	2500pcs/Reel

General Description

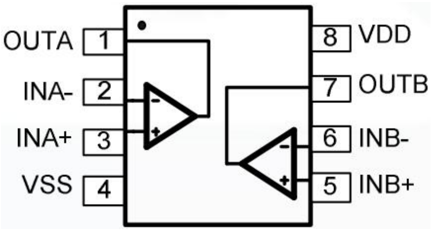
The LMV552/24 have a high gain-bandwidth product of 3MHz, a slew rate of 1.66V/μs, and a quiescent current of 250μA per amplifier at 5V. The LMV552/24 are designed to provide optimal performance in low voltage and low noise systems. They provide rail-to-rail output swing into heavy loads. The input common mode voltage range includes ground, and the maximum input offset voltage is 3.5mV for LMV552/24. They are specified over the extended industrial temperature range (-40℃ to+125℃). The operating range is from 2.1V to 5.5V. The LMV552 dual is available in Green SOP-8 and MSOP-8 packages. The LMV554 Quad is available in Green SOP-14 and TSSOP-14 packages.

Applications

- Sensors
- Active Filters
- Cellular and Cordless Phones
- Laptops and PDAs
- Audio
- Handheld Test Equipment
- Battery-Powered Instrumentation
- A/D Converters

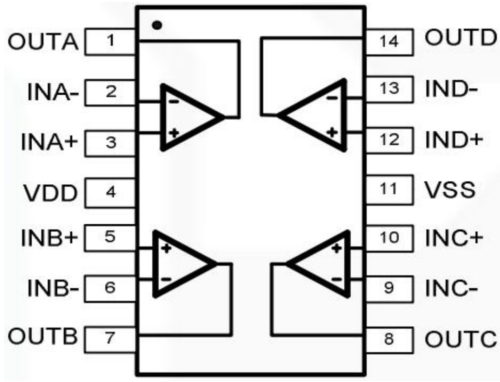
Pin Configuration

LMV552



SOP-8/MSOP-8

LMV554



SOP-14/TSSOP-14

Figure 1. Pin Assignment Diagram

Absolute Maximum Ratings

Condition	Min	Max
Power Supply Voltage (V_{DD} to Vss)	-0.5V	+7.5V
Analog Input Voltage (IN+ or IN-)	Vss-0.5V	V_{DD}+0.5V
PDB Input Voltage	Vss-0.5V	+7V
Operating Temperature Range	-40°C	+125°C
Junction Temperature	+160°C	
Storage Temperature Range	-55°C	+150°C
Lead Temperature (soldering, 10sec)	+245°C	
Package Thermal Resistance (TA=+25℃)		
SOP-8, θ_{JA}	125°C/W	
MSOP-8, θ_{JA}	216°C/W	
ESD Susceptibility		
HBM	8KV	
MM	400V	

Note: Stress greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions outside those indicated in the operational sections of this specification are not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Electrical Characteristics

(At Vs=5V, T_A = +25℃, V_{CM} = V_S/2, R_L = 600Ω, unless otherwise noted.)

PARAMETER	CONDITIONS	LMV552/4						
		TYP	MIN/MAX OVER TEMPERATURE					
		+25℃	+25℃	0℃ to 70℃	-40℃ to 85℃	-40 ℃ to125℃	UNITS	MIN / MAX
INPUT CHARACTERISTICS								
Input Offset Voltage (V_{OS})		0.8	3.5	3.9	4.3	4.6	mV	MAX
Input Bias Current (I_B)		1					pA	TYP
Input Offset Current (I_{OS})		1					pA	TYP
Input Common Mode Voltage Range (V_{CM})	V_S = 5.5V	-0.1 to +5.6					V	TYP
Common Mode Rejection Ratio(CMRR)	V_S = 5.5V, V_{CM} = -0.1V to 4V	82	65	64	64	63	dB	MIN
	V_S = 5.5V, V_{CM} = -0.1V to 5.6V	71					dB	MIN
Open-Loop Voltage Gain (A_{OL})	R_L = 600Ω, V_O = 0.15V to 4.85V	90	80	76	75	68	dB	MIN
	R_L = 10kΩ, V_O = 0.05V to 4.95V	100					dB	MIN
Input Offset Voltage Drift ($\Delta V_{OS}/\Delta T$)		2.4					μV/℃	TYP
OUTPUT CHARACTERISTICS								
Output Voltage Swing from Rail	R_L = 600Ω	0.1					V	TYP
	R_L = 10kΩ	0.015					V	TYP
Output Current (I_{OUT})		53	49	45	40	35	mA	MIN
Closed-Loop Output Impedance	f = 100kHz, G = 1	10					Ω	TYP
POWER-DOWN DISABLE								
Turn-On Time		4					μs	TYP
Turn-Off Time		1.2					μs	TYP
DISABLE Voltage-Off			0.8				V	MAX
DISABLE Voltage-On			2				V	MIN
POWER SUPPLY								
Operating Voltage Range			2.1	2.1	2.1	2.1	V	MIN
			5.5	5.5	5.5	5.5	V	MAX
Power Supply Rejection Ratio (PSRR)	V_S = +2.5V to +5.5V	91	74	72	72	68	dB	MIN
Quiescent Current/Amplifier (I_Q)	V_{CM} = (-VS) + 0.5V	250	350	427	450	515	μa	MAX
Supply Current when Disabled (LMV551N Only)	I_{OUT} = 0	90					nA	MAX

Electrical Characteristics

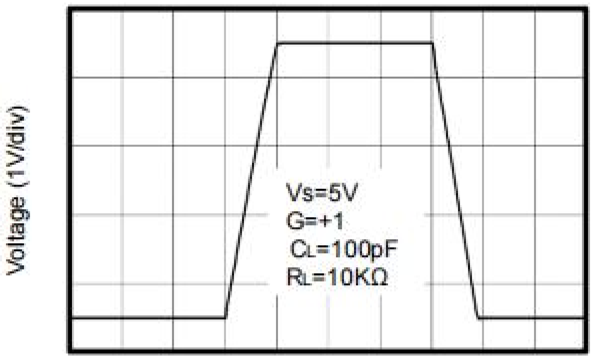
(At Vs=5V, T_A = +25℃, VCM = V_S/2, R_L = 600Ω, unless otherwise noted.)

PARAMETER	CONDITIONS	LMV552/4						
		TYP	MIN/MAX OVER TEMPERATURE					
		+25℃	+25℃	0℃ to 70℃	-40℃ to 85℃	-40℃to 125℃	UNITS	MIN / MAX
DYNAMIC PERFORMANCE								
Gain-Bandwidth Product (GBP)	R_L = 10kΩ, C_L = 100pF	3					MHz	TYP
Phase Margin ($φ_O$)	R_L = 10kΩ, C_L = 100pF	50					Degrees	TYP
Full Power Bandwidth (BWP)	＜1% distortion, R_L = 600Ω	50					kHz	TYP
Slew Rate (SR)	G = +1, 2V Step, R_L = 10kΩ	1.66					V/μs	TYP
Settling Time to 0.1% (t_S)	G = +1, 2V Step, R_L = 600Ω	0.5					μs	TYP
Overload Recovery Time	V_{IN} ·Gain = VS, R_L = 600Ω	4.5					μs	TYP
NOISE PERFORMANCE								
Voltage Noise Density (e_n)	f = 1kHz	18					$nV\sqrt{Hz}$	TYP
Current Noise Density (i_n)	f = 1kHz	4.5					$fA\sqrt{Hz}$	TYP

Typical Performance characteristics

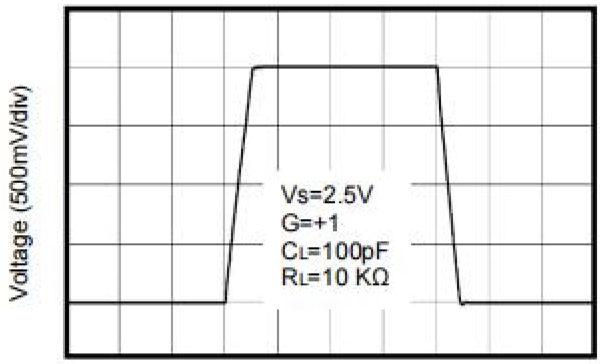
(At Vs=5V, T_A = +25℃, V_{CM} = V_S/2, R_L = 600Ω, unless otherwise noted.)

Large-Signal Step Response



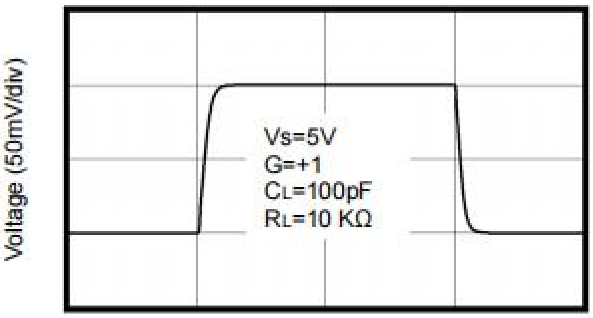
Time (2µs/div)

Large-Signal Step Response



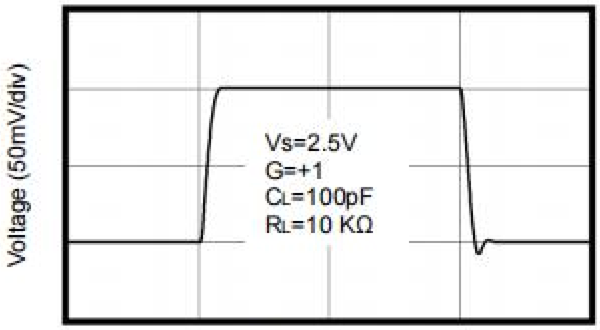
Time (2µs/div)

Small-Signal Step Response



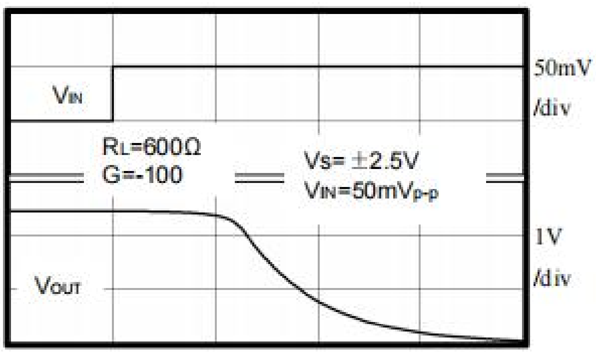
Time (1µs/div)

Small-Signal Step Response



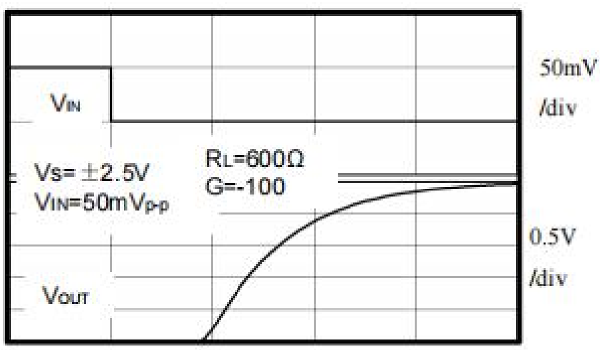
Time (1µs/div)

Positive Overload Recovery



Time (5µs/div)

Negative Overload Recovery

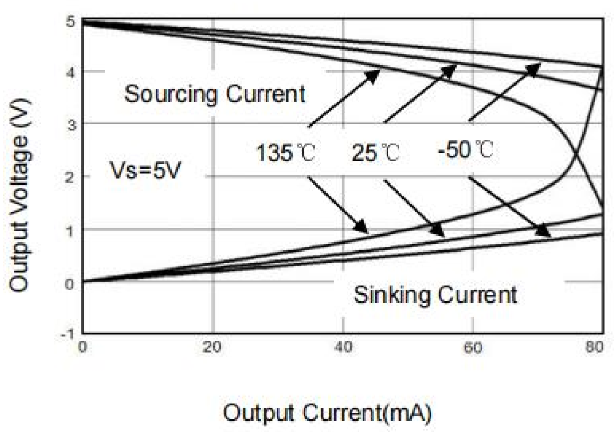


Time (5µs/div)

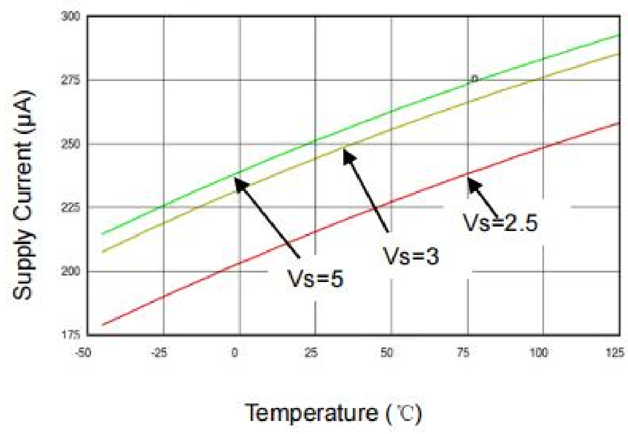
Typical Performance characteristics

(At Vs=5V, T_A= +25℃, V_{CM} = V_S/2, RL = 600Ω, unless otherwise noted.)

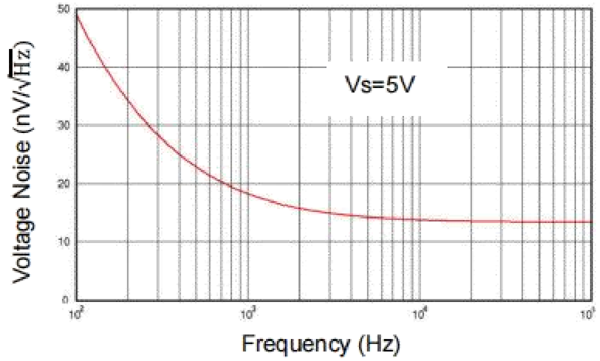
Output Voltage Swing vs.Output Current



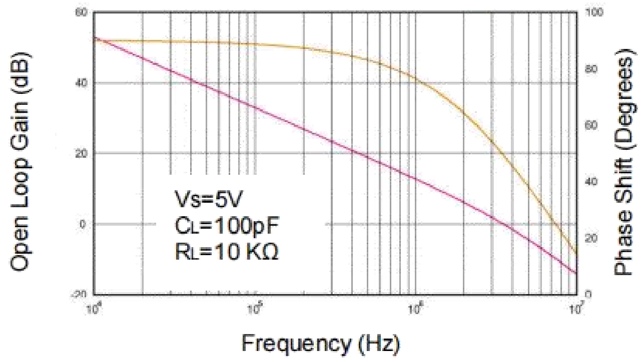
Supply Current vs. Temperature



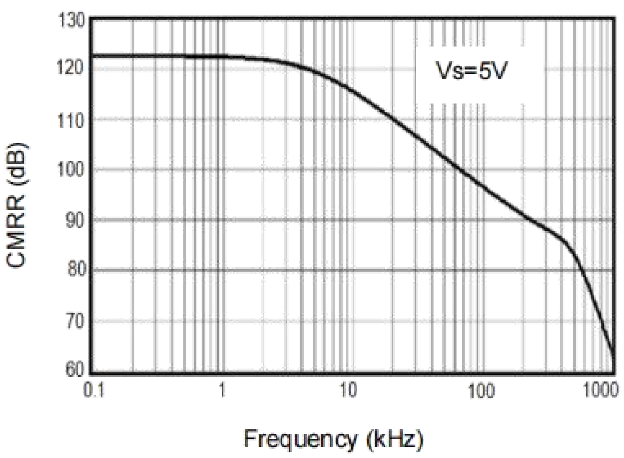
Input Voltage Noise Spectral Density vs. Frequency



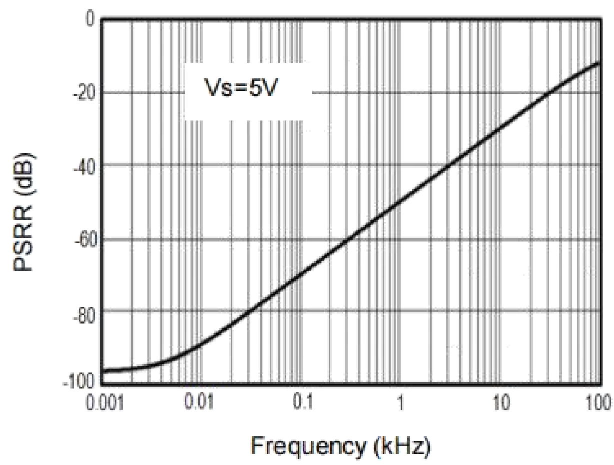
Open Loop Gain, Phase Shift vs. Frequency



CMRR vs. Frequency



PSRR vs. Frequency



Application Note

Size

LMV552/554 series op amps are unity-gain stable and suitable for a wide range of general-purpose applications. The small footprints of the LMV552/554 series packages save space on printed circuit boards and enable the design of smaller electronic products.

Power Supply Bypassing and Board Layout

LMV552/554 series operates from a single 2.1V to 5.5V supply or dual ±1.05V to ±2.75V supplies. For best performance, a 0.1μF ceramic capacitor should be placed close to the V_{DD} pin in single supply operation. For dual supply operation, both V_{DD} and V_{SS} supplies should be bypassed to ground with separate 0.1μF ceramic capacitors.

Low Supply Current

The low supply current (typical 250μA per channel) of LMV552/554 series will help to maximize battery life. They are ideal for battery powered systems.

Operating Voltage

LMV552/554 series operate under wide input supply voltage (2.1V to 5.5V). In addition, all temperature specifications apply from -40℃ to +125℃. Most behavior remains unchanged throughout the full operating voltage range. These guarantees ensure operation throughout the single Li-Ion battery lifetime.

Rail-to-Rail Input

The input common-mode range of LMV552/554 series extends 100mV beyond the supply rails (V_{SS}-0.1V to V_{DD}+0.1V). This is achieved by using complementary input stage. For normal operation, inputs should be limited to this range.

Rail-to-Rail Output

Rail-to-Rail output swing provides maximum possible dynamic range at the output. This is particularly important when operating in low supply voltages. The output voltage of LMV552/554 series can typically swing to less than 2mV from supply rail in light resistive loads (>100kΩ), and 60mV of supply rail in moderate resistive loads (10kΩ).

Capacitive Load Tolerance

The LMV552/554 family is optimized for bandwidth and speed, not for driving capacitive loads. Output capacitance will create a pole in the amplifier's feedback path, leading to excessive peaking and potential oscillation. If dealing with load capacitance is a requirement of the application, the two strategies to consider are (1) using a small resistor in series with the amplifier's output and the load capacitance and (2) reducing the bandwidth of the amplifier's feedback loop by increasing the overall noise gain.

Figure 2. shows a unity gain follower using the series resistor strategy. The resistor isolates the output from the capacitance and, more importantly, creates a zero in the feedback path that compensates for the pole created by the output capacitance.

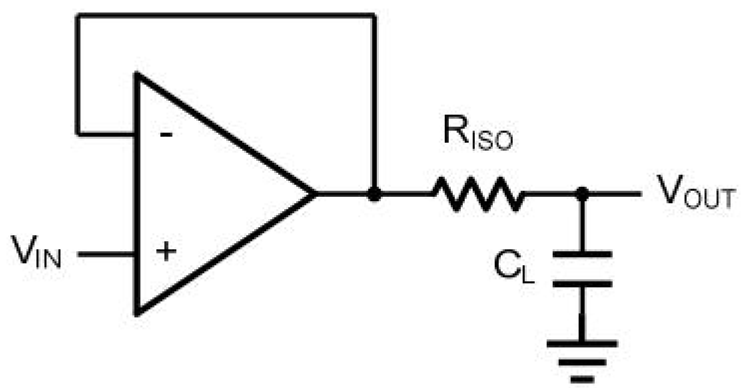


Figure 2. Indirectly Driving a Capacitive Load Using Isolation Resistor

The bigger the R_{ISO} resistor value, the more stable V_{OUT} will be. However, if there is a resistive load RL in parallel with the capacitive load, a voltage divider (proportional to R_{ISO}/R_L) is formed, this will result in a gain error.

The circuit in Figure 3 is an improvement to the one in Figure 2. R_F provides the DC accuracy by feed-forward the VIN to R_L. C_F and R_{ISO} serve to counteract the loss of phase margin by feeding the high frequency component of the output signal back to the amplifier's inverting input, thereby preserving the phase margin in the overall feedback loop. Capacitive drive can be increased by increasing the value of C_F. This in turn will slow down the pulse response.

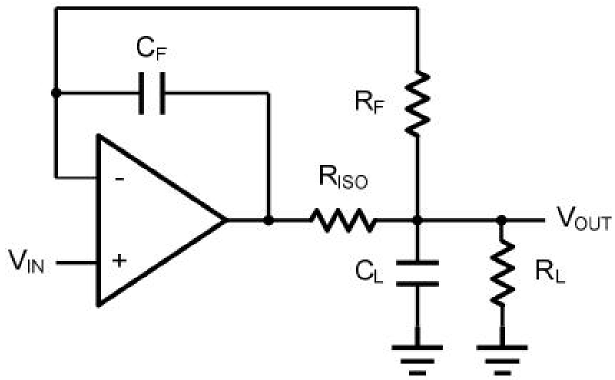


Figure 3. Indirectly Driving a Capacitive Load with DC Accuracy

Typical Application Circuits

Differential amplifier

The differential amplifier allows the subtraction of two input voltages or cancellation of a signal common the two inputs. It is useful as a computational amplifier in making a differential to single-end conversion or in rejecting a common mode signal. Figure 4. shown the differential amplifier using LMV552/554.

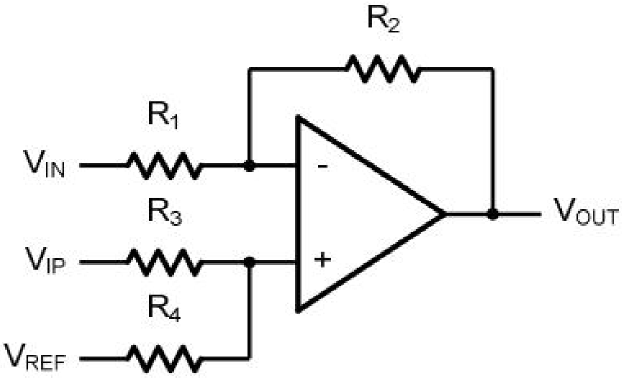


Figure 4. Differential Amplifier

$$V_{OUT} = (\frac{R_1 + R_2}{R_3 + R_4})\frac{R_4}{R_1} V_{IN} - \frac{R2}{R1} V_{IP} + (\frac{R_1 + R_2}{R_3 + R_4})\frac{R_3}{R_1} V_{REF}$$

If the resistor ratios are equal (i.e. R_1=R_3 and R_2=R_4), then

$$V_{OUT} = \frac{R_2}{R_1}(V_{IP} - V_{IN}) + V_{REF}$$

Low Pass Active Filter

The low pass active filter is shown in Figure 5. The DC gain is defined by $-R_2/R_1$. The filter has a -20dB/decade roll-off after its corner frequency $f_C=1/(2\pi R_3 C_1)$.

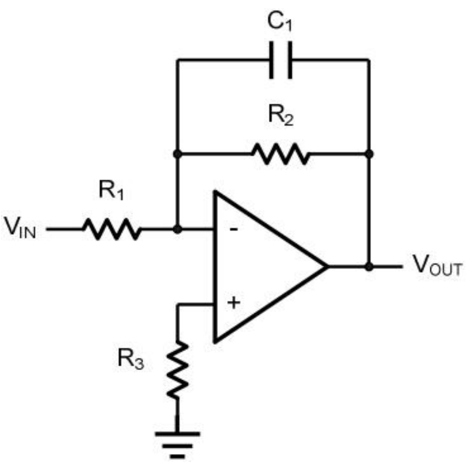


Figure 5. Low Pass Active Filter

Instrumentation Amplifier

The triple LMV552/554 can be used to build a three-op-amp instrumentation amplifier as shown in Figure 6. The amplifier in Figure 6 is a high input impedance differential amplifier with gain of R_2/R_1. The two differential voltage followers assure the high input impedance of the amplifier.

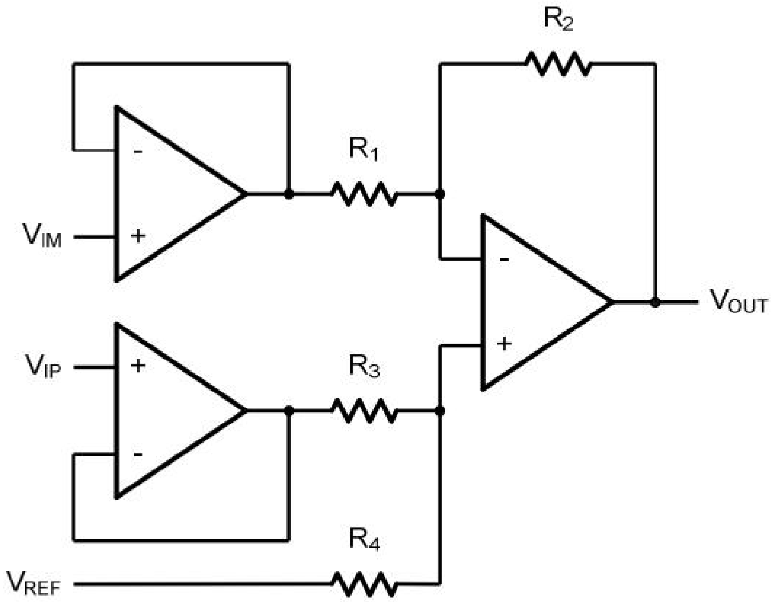
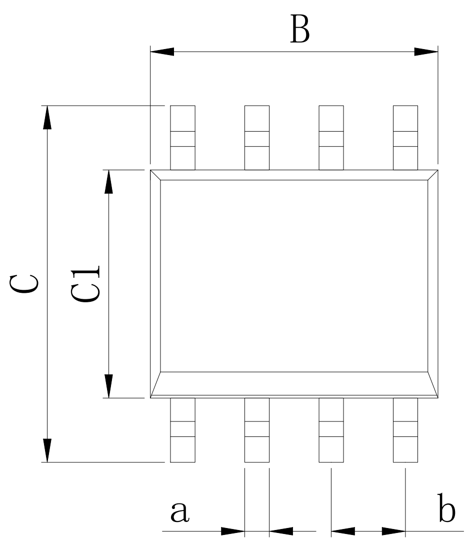
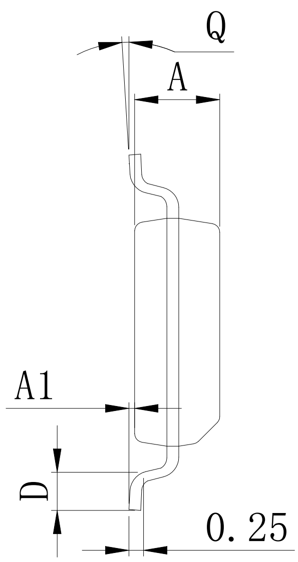


Figure 6. Instrument Amplifier

Physical Dimensions

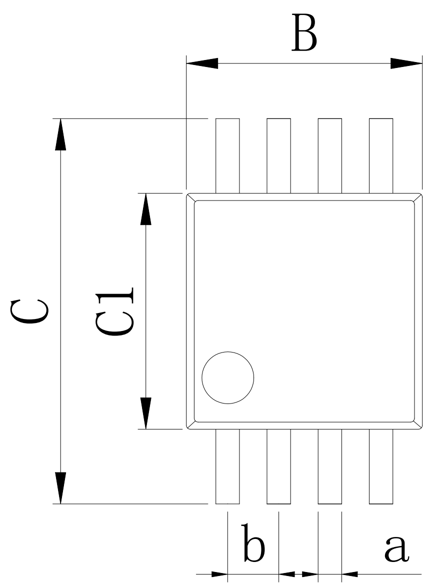
SOP-8

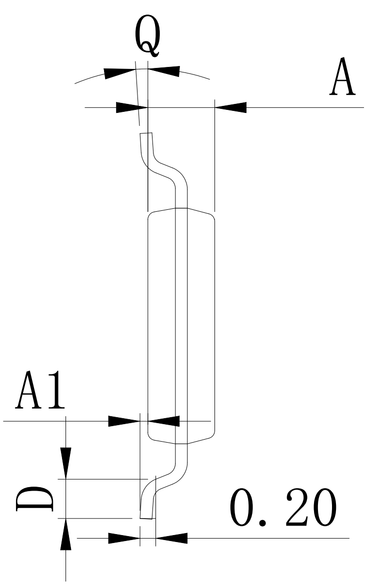




Dimensions In Millimeters(SOP-8)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	1.35	0.05	4.90	5.80	3.80	0.40	0°	0.35	1.27 BSC
Max:	1.55	0.20	5.10	6.20	4.00	0.80	8°	0.45	

MSOP-8

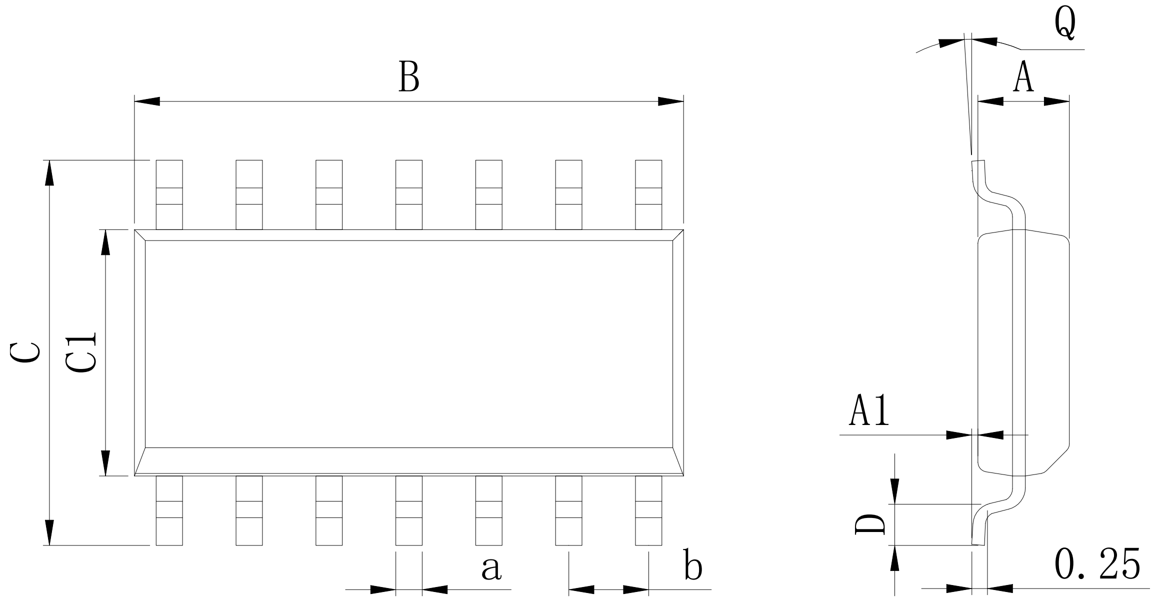




Dimensions In Millimeters(MSOP-8)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	0.80	0.05	2.90	4.75	2.90	0.35	0°	0.25	0.65 BSC
Max:	0.90	0.20	3.10	5.05	3.10	0.75	8°	0.35	

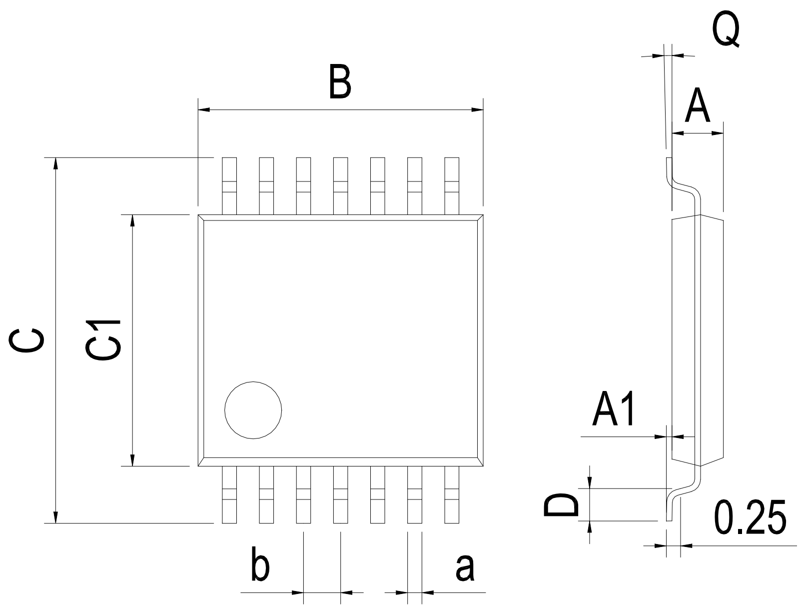
Physical Dimensions

SOP-14



Dimensions In Millimeters(SOP-14)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	1.35	0.05	8.55	5.80	3.80	0.40	0°	0.35	1.27 BSC
Max:	1.55	0.20	8.75	6.20	4.00	0.80	8°	0.45	

TSSOP-14



Dimensions In Millimeters(TSSOP-14)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	0.85	0.05	4.90	6.20	4.30	0.40	0°	0.20	0.65 BSC
Max:	0.95	0.20	5.10	6.60	4.50	0.80	8°	0.25	

Revision History

DATE	REVISION	PAGE
2015-10-6	New	1-14
2023-7-24	Update encapsulation type、Update Lead Temperature	1、2

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