

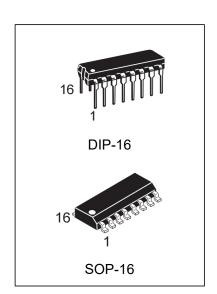
BCD-to-Decimal Decoder

Features

- Wide supply voltage range: 3.0V to 15V
- High noise immunity: 0.45 VDD (typ.)
- Low power TTL compatibility: fan out of 2 driving 74L or 1 driving 74LS
- Low power
- Glitch free outputs
- "Positive logic" on inputs and outputs

Applications

- Code conversion
- Address decoding
- Indicator-tube decode



Ordering Information

DEVICE	Package Type	MARKING	Packing	Packing Qty
CD4028BE	DIP-16	CD4028B	TUBE	1000pcs/box
CD4028BM/TR	SOP-16	CD4028B	REEL	2500pcs/reel

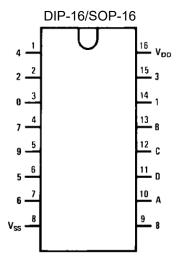


General Description

The CD4028B is a BCD to decimal or binary-to-octal decoder consisting of 4 inputs, decoding logic gates, and 10 output buffers. A BCD code applied to the 4 inputs, A, B, C, and D, results in a high level at the selected 1-of-10 decimal decoded outputs. Similarly, a 3-bit binary code applied to inputs A, B, and C is decoded in octal at outputs 0–7. A high level signal at the D input inhibits octal decoding and causes outputs 0–7 to go LOW.

All inputs are protected against static discharge damage by diode clamps to VDD and VSS.

Connection Diagram

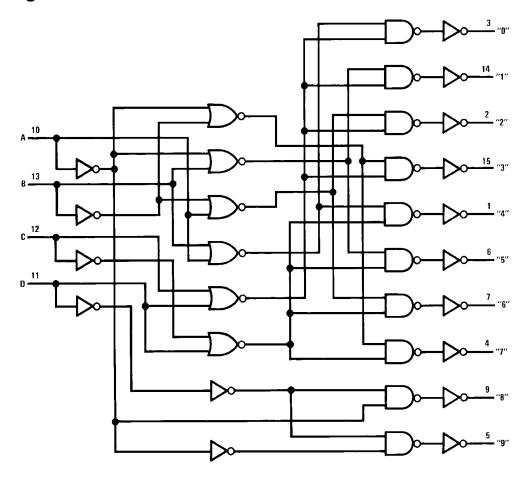


Truth Table

		D	С	В	Α	0	1	2	3	4	5	6	7	8	9	
		0	0	0	0	1	0	0	0	0	0	0	0	0	0	
		0	0	0	1	0	1	0	0	0	0	0	0	0	0	
		0	0	1	0	0	0	1	0	0	0	0	0	0	0	
		0	0	1	1	0	0	0	1	0	0	0	0	0	0	
		0	1	0	0	0	0	0	0	1	0	0	0	0	0	BCD States
		0	1	0	1	0	0	0	0	0	1	0	0	0	0	
		0	1	1	0	0	0	0	0	0	0	1	0	0	0	
1	HIGH Level	0	1	1	1	0	0	0	0	0	0	0	1	0	0	
0	LOW Level	1	0	0	0	0	0	0	0	0	0	0	0	1	0	
		1	0	0	1	0	0	0	0	0	0	0	0	0	1	
		1	0	1	0	0	0	0	0	0	0	0	0	1	0	
		1	0	1	1	0	0	0	0	0	0	0	0	0	1	
		1	1	0	0	0	0	0	0	0	0	0	0	1	0	Extraordinary States
		1	1	0	1	0	0	0	0	0	0	0	0	0	1	
		1	1	1	0	0	0	0	0	0	0	0	0	1	0	
		1	1	1	1	0	0	0	0	0	0	0	0	0	1	



Logic Diagram



Absolute Maximum Ratings

	MIN	MAX		
Supply Voltage (VDD)	-0.5V	+18V		
Input Voltage (VIN)	-0.5V	Vdd+0.5V		
Storage Temperature Range (TS)	-65℃	-65℃ +150℃		
Davier Dissipation (Da)	Dual-In-Line	700	mW	
Power Dissipation (PD)	Small Outline	500	mW	
(Soldering, 10 seconds)	24	5℃		

Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not ensured.



Recommended Operating Conditions

CHARACTERISTIC	Min.	Max.	Units
Supply Voltage (VDD)	3	15	V
Input Voltage (V _{IN})	0	VDD	V
Operating Temperature Range (T _A)	-40	+85	$^{\circ}$

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed, they are not meant to imply that the devices should be operated at these limits. The table of "Recom- mended Operating Conditions" and "Electrical Characteristics" provides conditions for actual device operation.

Note 2: VSS = 0V unless otherwise specified.

DC Electrical Characteristics (Note 2)

Cumbal	Parameter	Conditions	4	0℃		25℃		8	5℃	Units
Symbol	Parameter	Conditions	Min	Max	Min	Тур	Max	Min	Max	Units
I _{DD}	Quiescent Device Current	VDD = 5V, VIN = VDD or VSS VDD = 10V, VIN = VDD or VSS VDD = 15V, VIN = VDD or VSS		20 40 80		0.01 0.01 0.02	20 40 80		150 300 600	μΑ μΑ μΑ
VoL	LOW Level Output Voltage	IO < 1 µA, VIL = 0V, VIH = VDD VDD = 5V VDD = 10V VDD = 15V		0.05 0.05 0.05		0 0 0	0.05 0.05 0.05		0.05 0.05 0.05	V V V
Voн	HIGH Level Output Voltage	I _O < 1 µA, V _{IL} = 0V, V _{IH} = V _{DD} V _{DD} = 5V V _{DD} = 10V V _{DD} = 15V	4.95 9.95 14.95		4.95 9.95 14.95	5 10 15		4.95 9.95 14.95		V V V
VIL	LOW Level Input Voltage	O < 1 µA VDD = 5V, VO = 0.5V or 4.5V VDD = 10V, VO = 1V or 9V VDD = 15V, VO = 1.5V or 13.5V		1.5 3.0 4.0		2.25 4.5 6.75	1.5 3.0 4.0		1.5 3.0 4.0	V V V
VIH	HIGH Level Input Voltage	IO < 1 µA VDD = 5V, VO = 0.5V or 4.5V VDD = 10V, VO = 1V or 9V VDD = 15V, VO = 1.5V or 13.5V	3.5 7.0 11.0		3.5 7.0 11.0			3.5 7.0 11.0		V V V
l _{OL}	LOW Level Output Current (Note 3)	VIH = VDD, VIL = 0V VDD = 5V, VO = 0.4V VDD = 10V, VO = 0.5V VDD = 15V, VO = 1.5V	0.52 1.3 3.6		0.44 1.1 3.0	0.88 2.2 6.0		0.36 0.9 2.4		mA mA mA
ЮН	HIGH Level Output Current (Note 3)	VIH = VDD, VIL = 0V VDD = 5V, VO = 4.6V VDD = 10V, VO = 9.5V VDD = 15V, VO = 13.5V	-0.2 -0.5 -1.4		-0.16 -0.4 -1.2	-0.32 -0.8 -3.5		-0.12 -0.3 -1.0		mA mA mA
IIN	Input Current	V _{DD} = 5V, V _{IN} = 0V V _{DD} = 15V, V _{IN} =15V		-0.3 -0.3			-0.3 -0.3		-1.0 -1.0	μA μA

Note 3: IOL and IOH are tested one output at a time.



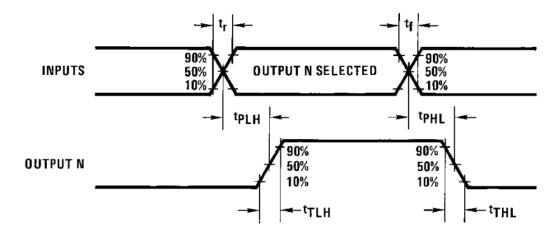
AC Electrical Characteristics (Note 4)

TA = 25° C, CL =50 pF, RL = 200k, Input tr = tf =20 ns, unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Units
tphL or tpLH	Propagation Delay Time	VCC = 5V		240	480	ns
		VCC = 10V		100	200	ns
		VCC = 15V		70	140	ns
tTHL or tTLH	Transition Time	VCC = 5V		175	350	ns
		V _{CC} = 10V		75	150	ns
		VCC = 15V		60	110	ns
CIN	Input Capacitance	Any Input		5	7.5	pF

Note 4: AC Parameters are guaranteed by DC correlated testing.

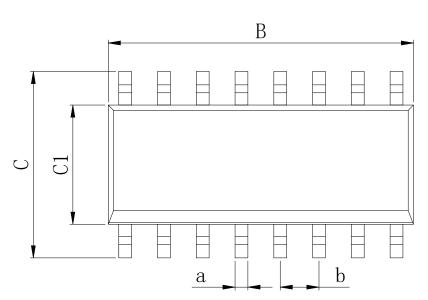
Witching Time Waveforms

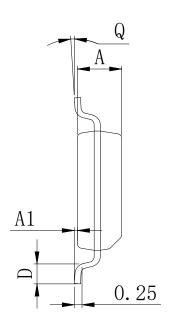




Physical Dimensions

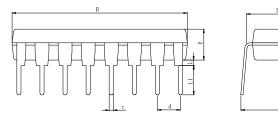
SOP-16

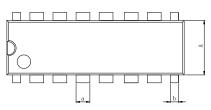




Dimensions In Millimeters(SOP-16)												
Symbol:	Α	A1	В	С	C1	D	Q	а	b			
Min:	1.35	0.05	9.80	5.80	3.80	0.40	0°	0.35	4 27 BCC			
Max:	1.55	0.20	10.0	6.20	4.00	0.80	8°	0.45	1.27 BSC			

DIP-16





Dimensions In Millimeters(DIP-16)												
Symbol:	Α	В	D	D1	Е	L	L1	а	b	С	d	
Min:	6.10	18.94	8.10	7.42	3.10	0.50	3.00	1.50	0.85	0.40	0 E4 DCC	
Max:	6.68	19.56	10.9	7.82	3.55	0.70	3.60	1.55	0.90	0.50	2.54 BSC	



Revision History

DATE	REVISION	PAGE					
2011-9-8	New	1-8					
2023-9-8	Update encapsulation type 、 Update Lead Temperature 、 Updated DIP-16	1. 3. 6					
2023-9-0	dimension、Add annotation for Maximum Ratings.						



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