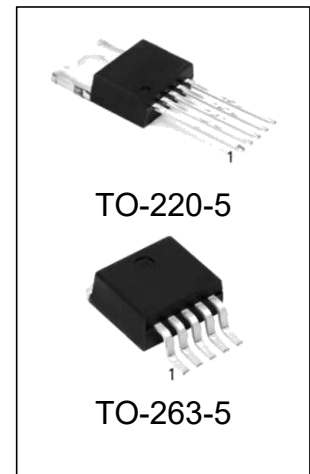


800mA Fast Ultra Low Dropout Linear Regulators

Features

- Ultra low dropout voltage
- Low ground pin current
- Load regulation of 0.02%
- 15 μ A quiescent current in shutdown mode
- Guaranteed output current of 0.8A DC
- Available in TO-263 and TO-220 packages
- Output voltage accuracy $\pm 1.5\%$
- Sense option improves better load regulation (LP3964)
- Extremely low output capacitor requirements
- Overt emperature/overcurrent protection
- -40°C to $+125^{\circ}\text{C}$ junction temperature range



Ordering Information

DEVICE	Package Type	MARKING	Packing	Packing Qty
LP3964T-1.8	TO-220-5	LP3964-1.8	TUBE	1000pcs/Box
LP3964T-2.5	TO-220-5	LP3964-2.5	TUBE	1000pcs/Box
LP3964T-3.3	TO-220-5	LP3964-3.3	TUBE	1000pcs/Box
LP3964T-5.0	TO-220-5	LP3964-5.0	TUBE	1000pcs/Box
LP3964T-ADJ	TO-220-5	LP3964-ADJ	TUBE	1000pcs/Box
LP3964S-1.8/TR	TO-263-5	LP3964-1.8	REEL	500pcs/Reel
LP3964S-2.5/TR	TO-263-5	LP3964-2.5	REEL	500pcs/Reel
LP3964S-3.3/TR	TO-263-5	LP3964-3.3	REEL	500pcs/Reel
LP3964S-5.0/TR	TO-263-5	LP3964-5.0	REEL	500pcs/Reel
LP3964S-ADJ/TR	TO-263-5	LP3964-ADJ	REEL	500pcs/Reel

General Description

The LP3964 series of fast ultra low-dropout linear regulators operate from a +2.5V to +7.0V input supply. Wide range of preset output voltage options are available. These ultra low dropout linear regulators respond very fast to step changes in load which makes them suitable for low voltage microprocessor applications. The LP3964 are developed on a CMOS process which allows low quiescent current operation independent of output load current. This CMOS process also allows the LP3964 to operate under extremely low dropout conditions.

Dropout Voltage:

Ultra low dropout voltage; typically 24mV at 80mA load current and 240mV at 800mA load current.

Ground Pin Current:

Typically 4mA at 800mA load current.

Shutdown Mode:

Typically 15 μ A quiescent current when the shutdown pin is pulled low.

SENSE:

Sense pin improves regulation at remote loads.

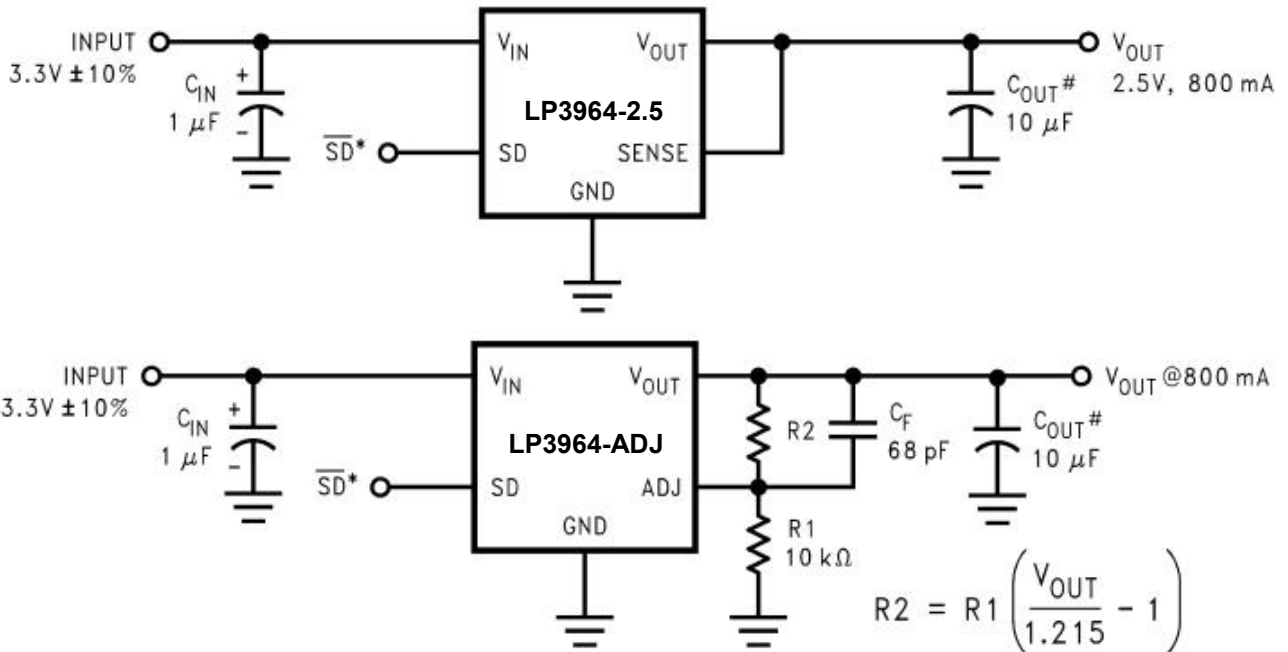
Precision Output Voltage:

Multiple output voltage options are available ranging from 1.8V 、 2.5V、 3.3V、 5.0V and adjustable , with a guaranteed accuracy of $\pm 1.5\%$ at room temperature, and $\pm 3.0\%$ over all conditions (varying line, load, and temperature).

Applications

- Microprocessor power supplies
- GTL, GTL+, BTL, and SSTL bus terminators
- Power supplies for DSPs
- SCSI terminator
- Post regulators
- High efficiency linear regulators
- Battery chargers
- Other battery powered applications

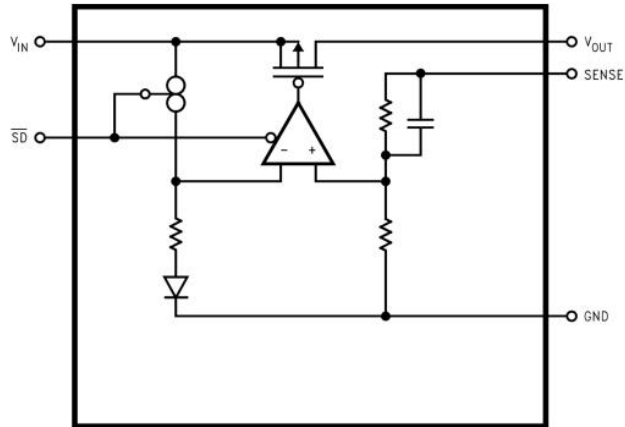
Typical Application Circuits



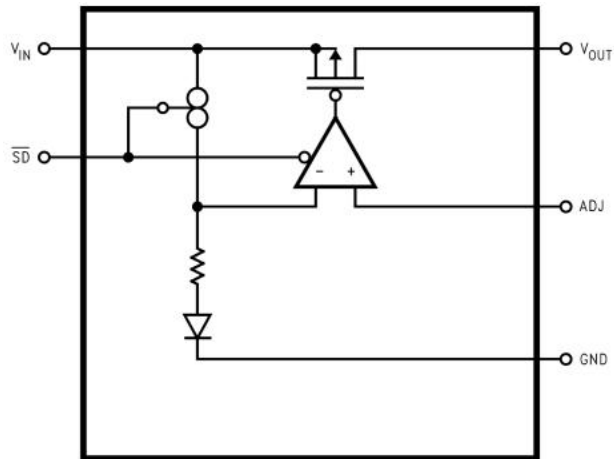
Minimum output capacitance is $10 \mu F$ to ensure stability over full load current range. More capacitance provides superior dynamic performance and additional stability margin.

* \overline{SD} and $ERROR$ pins must be pulled high through a $10k\Omega$ pull-up resistor. Connect the $ERROR$ pin to ground if this function is not used. See applications section for more information.

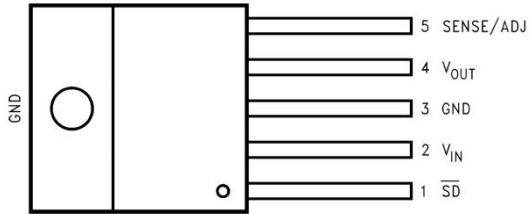
Block Diagram LP3964



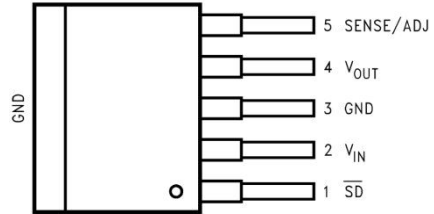
Block Diagram LP3964-ADJ



Connection Diagrams



Top View
TO-220-5 Package



Top View
TO-263-5 Package

Pin Description for TO-220-5 and TO263-5 Packages

Pin #	LP3964	
	Name	Function
1	\overline{SD}	Shutdown
2	V_{IN}	Input Supply
3	GND	Ground
4	V_{OUT}	Output Voltage
5	SENSE/ADJ	Remote Sense Pin or Output Adjust Pin

Absolute Maximum Ratings (Note 1)

Condition	Min	Max
Storage Temperature Range	-65°C	+150°C
Lead Temperature: (Soldering, 10 sec.)	-	245°C
ESD Rating (Note 3)	-	2kV
Power Dissipation (Note 2)	Internally Limited	
Input Supply Voltage (Survival)	-0.3V	+7.5V
Shutdown Input Voltage (Survival)	-0.3V	V _{IN} +0.3V
Output Voltage (Survival), (Note 6), (Note 7)	-0.3V	+7.5V
I _{OUT} (Survival)	Short Circuit Protected	
Maximum Voltage for SENSE Pin	-	V _{OUT} +0.3V

Operating Ratings

Condition	Min	Max
Input Supply Voltage (Operating)(Note 12)	2.5V	7.0V
Shutdown Input Voltage (Operating)	-0.3V	V _{IN} +0.3V
Maximum Operating Current (DC)	-	0.8A
Operating Junction Temp. Range	-40°C	+125°C

Electrical Characteristics

Limits in standard typeface are for $T_J = 25^\circ\text{C}$, and limits in **boldface type** apply over the **full operating temperature range**. Unless otherwise specified: $V_{IN} = V_{O(NOM)} + 1\text{V}$, $I_L = 10\text{ mA}$, $C_{OUT} = 10\mu\text{F}$, $V_{SD} = V_{IN} - 0.3\text{V}$.

Symbol	Parameter	Conditions	Typ (Note 4)	LP3964 (Note 5)		Units
				Min	Max	
V_O	Output Voltage Tolerance (Note 8)	$V_{OUT} + 1\text{V} < V_{IN} < 7.0\text{V}$ $10\text{ mA} < I_L < 800\text{ mA}$	0	-1.5	+1.5	%
		$3.135 \leq V_{IN} \leq 7.0$ for $V_{OUT} = 2.5\text{V}$		-3.0	+3.0	
ΔVOL	Output Voltage Line Regulation (Note 8)	$V_{OUT} + 1\text{V} < V_{IN} < 7.0\text{V}$,	0.02 0.06			%
$\frac{\Delta V_O}{\Delta I_{OUT}}$	Output Voltage Load Regulation (Note 8)	$10\text{ mA} < I_L < 800\text{ mA}$	0.02 0.08			%
$V_{IN} - V_{OUT}$	Dropout Voltage (Note 10)	$I_L = 80\text{ mA}$	24		30 35	mV
		$I_L = 800\text{ mA}$	240		300 350	
I_{GND}	Ground Pin Current In Normal Operation Mode	$I_L = 80\text{ mA}$	3		9 10	mA
		$I_L = 800\text{ mA}$	4		14 15	
I_{GND}	Ground Pin Current In Shutdown Mode (Note 11)	$V_{SD} \leq 0.2\text{V}$	15		25 75	μA
$I_O(\text{PK})$	Peak Output Current	(Note 2)	1.5	1.2 1.1		A
SHORT CIRCUIT PROTECTION						
ISC	Short Circuit Current		2.8			A
OVER TEMPERATURE PROTECTION						
Tsh(t)	Shutdown Threshold		165			$^\circ\text{C}$
Tsh(h)	Thermal Shutdown Hysteresis		10			$^\circ\text{C}$
SHUTDOWN INPUT						
V_{SDT}	Shutdown Threshold	Output = High	V_{IN}	$V_{IN} - 0.3$		V
		Output = Low	0		0.2	
T_{dOFF}	Turn-off delay	$I_L = 800\text{ mA}$	20			μs
T_{dON}	Turn-on delay	$I_L = 800\text{ mA}$	25			μs
I_{SD}	$\overline{\text{SD}}$ Input Current	$V_{SD} = V_{IN}$	1			nA

Electrical Characteristics

Limits in standard typeface are for $T_J = 25^\circ\text{C}$, and limits in **boldface type** apply over the **full operating temperature range**. Unless otherwise specified: $V_{IN} = V_{O(NOM)} + 1\text{V}$, $I_L = 10\text{ mA}$, $C_{OUT} = 10\mu\text{F}$, $V_{SD} = V_{IN} - 0.3\text{V}$.

Symbol	Parameter	Conditions	Typ (Note 4)	LP3964 (Note 5)		Units
				Min	Max	
AC PARAMETERS						
PSRR	Ripple Rejection	$V_{IN} = V_{OUT} + 1.5\text{V}$ $C_{OUT} = 100\mu\text{F}$ $V_{OUT} = 3.3\text{V}$	60			dB
		$V_{IN} = V_{OUT} + 0.3\text{V}$ $C_{OUT} = 100\mu\text{F}$ $V_{OUT} = 3.3\text{V}$	40			
$\rho_{n(f)}$	Output Noise Density	$f = 120\text{Hz}$	0.8			μV
e_n	Output Noise Voltage	$\text{BW} = 10\text{Hz} - 100\text{kHz}$	150			$\mu\text{V (rms)}$
	(rms)	$\text{BW} = 300\text{Hz} - 300\text{kHz}$	100			

Note 1: Absolute maximum ratings indicate limits beyond which damage to the device may occur. Operating ratings indicate conditions for which the device is intended to be functional, but does not guarantee specific performance limits. For guaranteed specifications and test conditions, see Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

Note 2: At elevated temperatures, devices must be derated based on package thermal resistance. The devices in TO220 package must be derated at $\theta_{jA} = 50^\circ\text{C/W}$ (with 0.5in², 1oz. copper area), junction-to-ambient (with no heat sink). The devices in the TO263 surface-mount package must be derated at $\theta_{jA} = 60^\circ\text{C/W}$ (with 0.5in², 1oz. copper area), junction-to-ambient. The devices in SOT223 package must be derated at $\theta_{jA} = 90^\circ\text{C/W}$ (with 0.5in², 1oz. copper area), junction-to-ambient.

Note 3: The human body model is a 100pF capacitor discharged through a 1.5k Ω resistor into each pin.

Note 4: Typical numbers are at 25°C and represent the most likely parametric norm.

Note 5: Limits are 100% production tested at 25°C. Limits over the operating temperature range are guaranteed through correlation using Statistical Quality Control (SQC) methods. The limits are used to calculate National's Average Outgoing Quality Level (AOQL).

Note 6: If used in a dual-supply system where the regulator load is returned to a negative supply, the LP3964 output must be diode-clamped to ground.

Note 7: The output PMOS structure contains a diode between the V_{IN} and V_{OUT} terminals. This diode is normally reverse biased. This diode will get forward biased if the voltage at the output terminal is forced to be higher than the voltage at the input terminal. This diode can typically withstand 200mA of DC current and 1Amp of peak current.

Note 8: Output voltage line regulation is defined as the change in output voltage from the nominal value due to change in the input line voltage. Output voltage load regulation is defined as the change in output voltage from the nominal value due to change in load current. The line and load regulation specification contains only the typical number. However, the limits for line and load regulation are included in the output voltage tolerance specification.

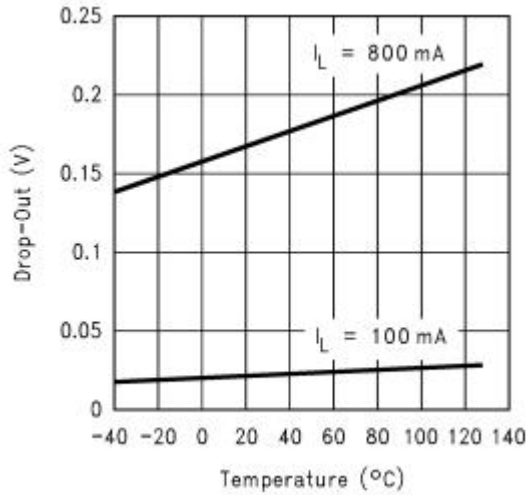
Note 9: Dropout voltage is defined as the minimum input to output differential voltage at which the output drops 2% below the nominal value. Dropout voltage specification applies only to output voltages of 2.5V and above. For output voltages below 2.5V, the drop-out voltage is nothing but the input to output differential, since the minimum input voltage is 2.5V.

Note 10: This specification has been tested for $-40^\circ\text{C} \leq T_J \leq 85^\circ\text{C}$ since the temperature rise of the device is negligible under shutdown conditions.

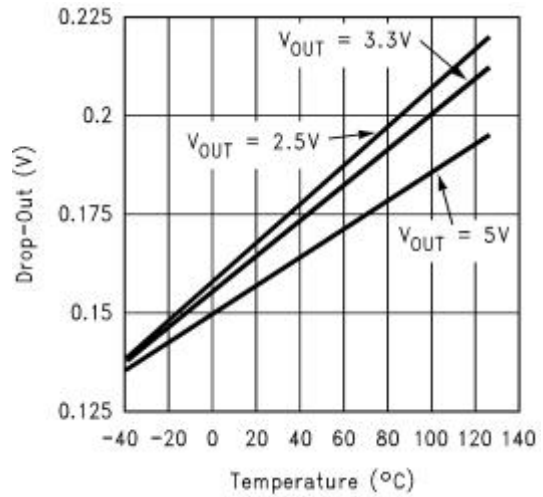
Typical Performance Characteristics

Unless otherwise specified, $V_{IN} = V_{O(NOM)} + 1V$, $V_{OUT} = 2.5V$, $C_{OUT} = 10\mu F$, $I_{OUT} = 10mA$, $C_{IN} = 10\mu F$, $V_{SD} = V_{IN}$, and $T_A = 25^\circ C$.

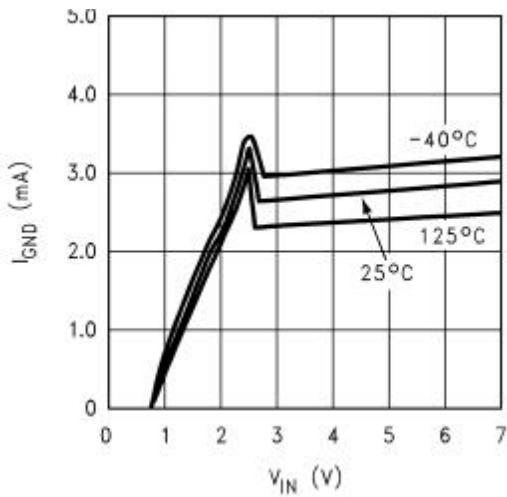
Drop-Out Voltage Vs Temperature for Different Load Currents



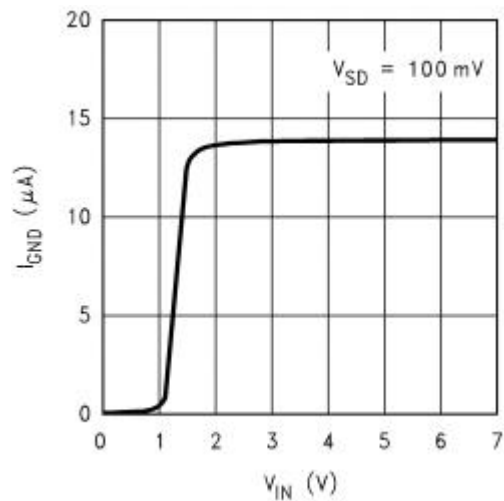
Drop-Out Voltage Vs Temperature for Different Output Voltages ($I_{OUT} = 800mA$)



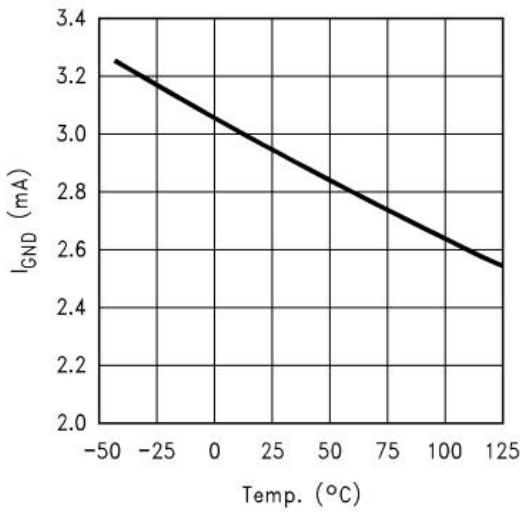
Ground Pin Current vs Input Voltage ($V_{SD} = V_{IN}$)



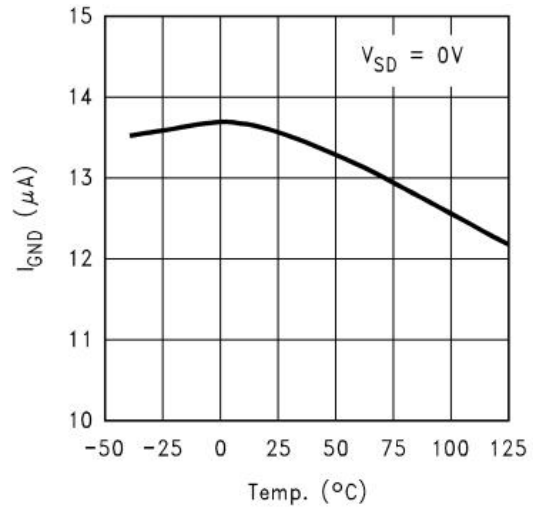
Ground Pin Current vs Input Voltage ($V_{SD} = 100mV$)



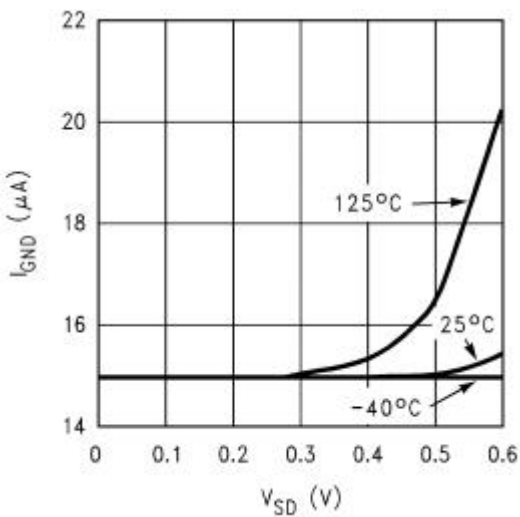
Ground Current Vs Temperature ($V_{SD}=V_{IN}$)



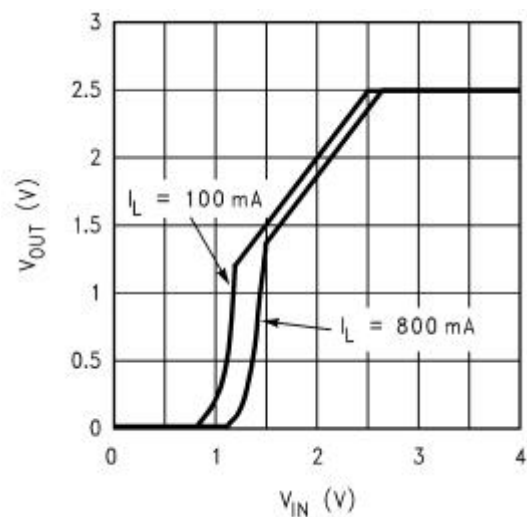
Ground Current Vs Temperature ($V_{SD}=0V$)



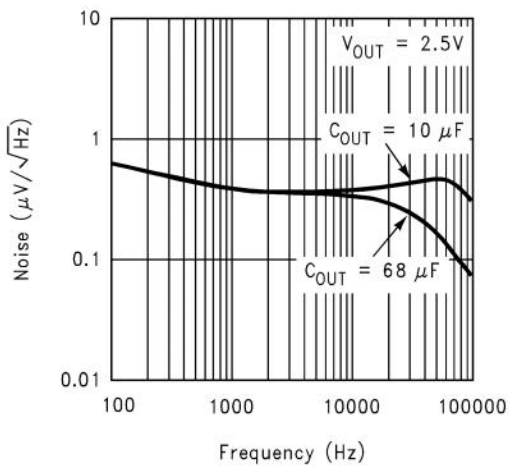
Ground Pin Current Vs Shutdown Pin Voltage



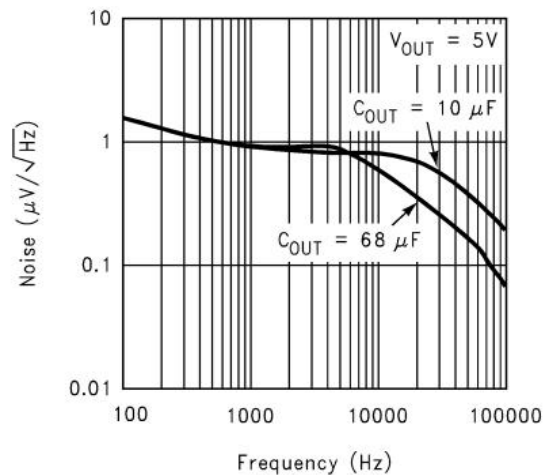
Input Voltage Vs Output Voltage



Output Noise Density, $V_{OUT}=2.5V$

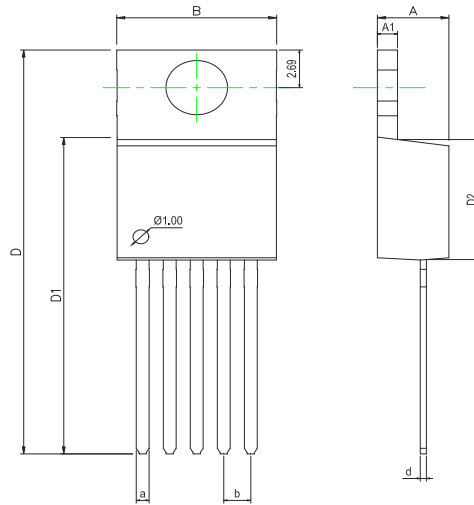


Output Noise Density, $V_{OUT}=5V$



Physical Dimensions

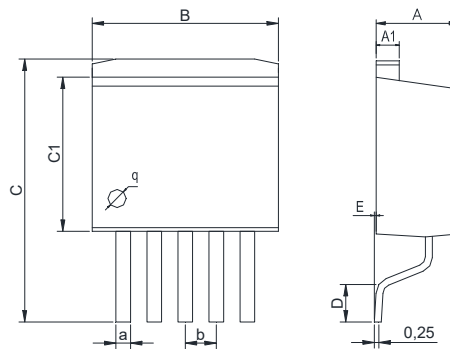
TO-220-5



Dimensions In Millimeters(TO-220-5)

Symbol:	A	A1	B	D	D1	D2	a	d	b
Min:	4.52	1.25	10	28.2	22.4	8.69	0.71	0.33	1.70BSC
Max:	4.62	1.29	10.3	28.9	22.6	8.79	0.97	0.42	

TO-263-5



Dimensions In Millimeters(TO263-5)

Symbol:	A	A1	B	C	C1	D	E	a	b
Min:	4.45	1.22	10	13.7	8.40	1.90	0	0.71	1.70BSC
Max:	4.62	1.32	10.4	14.6	8.90	2.10	0.20	0.97	

Revision History

DATE	REVISION	PAGE
2014-6-23	New	1-13
2023-7-24	Modify the package dimension diagram TO220-5、 Update encapsulation type、 Update Lead Temperature	11、 1、 6

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