

250MHZ CMOS Rail-to-Rail Output Opamps

Features

Single-Supply Operation from +2.5V ~ +5.5V

• Rail-to-Rail Output

• -3dB Bandwidth(G=+1): 250MHz (Typ)

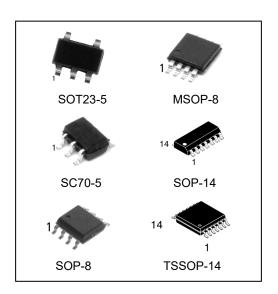
• Low Input Bias Current: 1pA (Typ)

Quiescent Current: 2.8mA/Amplifier (Typ)

Operating Temperature: -40°C ~ +125°C

Small Package:

HGV8051 Available in SOT23-5 and SC70-5 Packages HGV8052 Available in SOP-8,MSOP-8 and DFN-8 Packages HGV8054 Available in SOP-14 and TSSOP-14 Packages



Ordering Information

DEVICE	Package Type	MARKING	Packing	Packing Qty
HGV8051M5/TR	SOT23-5	8051	REEL	3000pcs/reel
HGV8051M7/TR	SC70-5	8051	REEL	3000pcs/reel
HGV8052M/TR	SOP-8	V8052	REEL	2500pcs/reel
HGV8052MM/TR	MSOP-8	8052	REEL	3000pcs/reel
HGV8052DQ/TR	DFN-8	8052	REEL	3000pcs/reel
HGV8054M/TR	SOP-14	HGV8054	REEL	2500pcs/reel
HGV8054MT/TR	TSSOP-14	V8054	REEL	2500pcs/reel



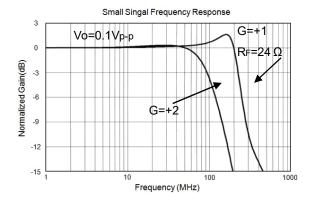
General Description

The HGV8051(single), HGV8052(dual), HGV8054(quad) are rail-to-rail output voltage feedback amplifiers offering ease of use and low cost. They have bandwidth and slew rate typically found in current feedback amplifiers. All have a wide input common-mode voltage range and output voltage swing, making them easy to use on single supplies as low as 2.5V. Despite being low cost, the HGV805X series provide excellent overall performance. They offer wide bandwidth to 250 MHz (G = +1) along with 0.1dB flatness out to 52 MHz (G = +2) and offer a typical low power of 2.8 mA/amplifier.

The HGV805X series is low distortion and fast settling make it ideal for buffering high speed A/D or D/A converters. The HGV8051 has a power-down disable feature that reduces the supply current to 50μ A. These features make the HGV8051/2 ideal for portable and battery-powered applications where size and power are critical. All are specified over the extended -40 $^{\circ}$ C to +125 $^{\circ}$ C temperature range.

Applications

- Imaging
- Photodiode Preamp
- DVD/CD
- Filters
- Professional Video and Cameras
- Hand Sets
- Base Stations
- A-to-D Driver





Pin Configuration

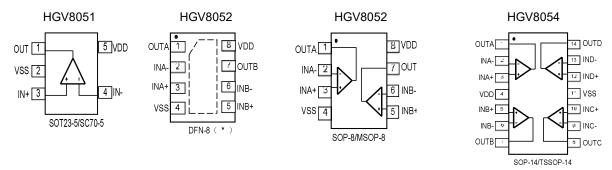


Figure 1. Pin Assignment Diagram

Absolute Maximum Ratings

Condition	Min	Max			
Power Supply Voltage (VDD to Vss)	-0.5V	+7.5V			
Analog Input Voltage (IN+ or IN-)	Vss-0.5V	VDD+0.5V			
PDB Input Voltage	Vss-0.5V	+7V			
Operating Temperature Range	-40°C	+125°C			
Junction Temperature	+16	0°C			
Storage Temperature Range	-55°C	+150°C			
Lead Temperature (soldering, 10sec)	+260°C				
Package Thermal Resistance (TA=+25℃)					
SOP-8, θJA	125°C/W				
MSOP-8, θJA	216°C/W				
SOT23-5, θJA	190°	C/W			
SC70-5, θJA	333°	C/W			
ESD Susceptibility					
НВМ	6K	ïV			
MM	400	DV			

Note:

Stress greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions outside those indicated in the operational sections of this specification are not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.



Electrical Performance Characteristics

 $(G=+2, RF=887\Omega, RG=887\Omega, and RL=150\Omega)$ connected to VS/2, unless otherwise noted. Typical values are at TA=+25°C.)

				HGV	/8051/52	/54		
DADAMETED	CONDITIONS	TYP	N	AM/NIN	OVER	TEMPER	RATURE	
PARAMETER	CONDITIONS	+25 ℃	+25℃	0℃ to70℃	-40℃to 85℃	-40℃ to125℃	UNITS	MIN/ MAX
DYNAMIC								
PERFORMANCE -3dB Small Signal	G = +1, Vo = 0.1V p-p, RF = 24Ω ,							
Bandwidth	RL = 150Ω	180					MHz	TYP
	G = +1, Vo = 0.1V p-p, RF = 24Ω , RL = $1k\Omega$	250					MHz	TYP
	$G = +2$, $Vo = 0.1V p-p$, $RL = 50\Omega$	55					MHz	TYP
	$G = +2$, $Vo = 0.1V p-p$, $RL = 150\Omega$	93					MHz	TYP
	G = +2, Vo = 0.1V p-p, RL = $1k\Omega$	122					MHz	TYP
	$G = +2$, $Vo = 0.1V p-p$, $RL = 10k\Omega$	130					MHz	TYP
Gain-Bandwidth Product	G = +10, $RL = 150Ω$	115					MHz	TYP
	$G = +10$, $RL = 1k\Omega$	150					MHz	TYP
Bandwidth for 0.1dB Flatness	G = +2, Vo = 0.1V p-p, RL = 150 Ω , RF =887 Ω	52					MHz	TYP
Slew Rate	G = +1, 2V Output Step	77/-151					V/µs	TYP
	G = +2, 2V Output Step	88/-119					V/µs	TYP
	G = +2, 4V Output Step	93/-131					V/µs	TYP
Rise-and-Fall Time	G = +2, Vo = 0.2V _{p-p} , 10% to 90%	4.5					ns	TYP
	$G = +2$, $Vo = 2V_{p-p}$, 10% to 90%	18					ns	TYP
Settling Time to 0.1%	G = +2, 2V Output Step	50					ns	TYP
Overload Recovery Time	$VIN \cdot G = +VS$	18					ns	TYP
NOISE/DISTORTION PERFORMANCE								
Input Voltage Noise	f = 1MHz	4.9					nV/ Hz	TYP
Differential Gain Error (NTSC)	G = +2, $RL = 150Ω$	0.03					%	TYP
Differential Phase Error (NTSC)	G = +2, $RL = 150Ω$	0.08					degree	TYP
DC PERFORMANCE								
Input Offset Voltage (VOS)		±2	±8	±8.9	±9.5	±9.8	mV	MAX
Input Offset Voltage Drift		2					µV/℃	TYP
Input Bias Current (IB)		1					PA	TYP
Input offset Current (IOS)		2					PA	TYP
Open-Loop Gain (AOL)	$V_O = 0.3V$ to 4.7V, $R_L = 150\Omega$	80	75	74	74	73	dB	MIN
	$V_0 = 0.2V \text{ to } 4.8V, R_L = 1k\Omega$	104	92	91	91	80	dB	MIN
INPUT CHARACTERISTICS Input Common-Mode	VCM = -0.1V to +3.5V	0.2 to					V	TYP
Voltage Range (VCM)	V CIVI0.1V 10 13.3V	-0.2 to +3.8					\ \	MIN
Common-Mode Rejection Ratio (CMRR)		80	66	65	65	62	dB	



Electrical Performance Characteristics

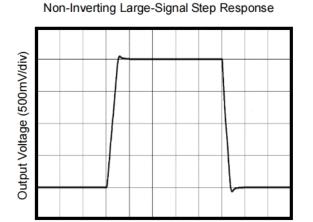
(G= +2, RF=887Ω, RG=887Ω, and RL=150Ω connected to VS/2, unless otherwise noted. Typical values are at TA =+25°C.)

				HGV80	51/52/54	ı		
PARAMETER	CONDITIONS	TYP	N	IIN/MAX	OVER	TEMPER	ATURE	
PARAWETER	CONDITIONS	+25 ℃	+25℃	0℃ to70℃	-40℃to 85℃	-40℃ to125℃	UNITS	MIN/ MAX
OUTPUT CHARACTERISTICS								
Output Voltage Swing from Rail	R _L = 150Ω	0.12					V	TYP
	$R_L = 1k\Omega$	0.03					V	TYP
Output Current		80	60				mA	MIN
Closed-Loop Output Impedance	f<100kHz	0.08					Ω	TYP
POWER-DOWN DISABLE								
(HGV8051/2 only)								
Turn-On Time		236					ns	TYP
Turn-Off Time		52					ns	TYP
DISABLE Voltage-Off			0.8				V	MAX
DISABLE Voltage-On			2				V	MIN
POWER SUPPLY								
Operating Voltage Range			2.5	2.7	2.7	2.7	V	MIN
			5.5	5.5	5.5	5.5	V	MAX
Quiescent Current (per amplifier)		2.8	3.65				mA	MAX
Supply Current when Disabled per		50	70	85	100	137	μA	MAX
amplifier(HGV8051/2 only) Power Supply Rejection Ratio (PSRR)	Δ VS = +2.7V to +5.5V, VCM = (-VS) +0.5	80	67	67	65	62	dB	MIN

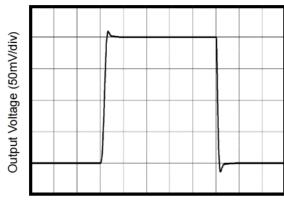


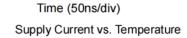
Typical Performance characteristics

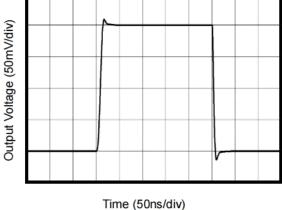
 $(Vs=+5V,G=+2,RF=887\Omega,RG=887\Omega,andRL=150\Omega)$ connected to Vs/2, Typical values are at TA=+25°C.)



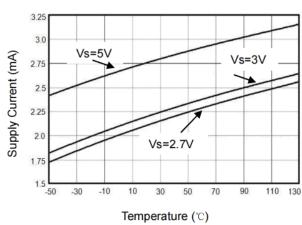


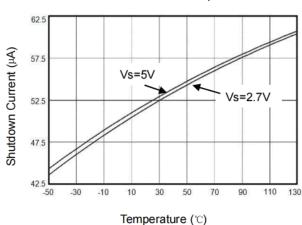






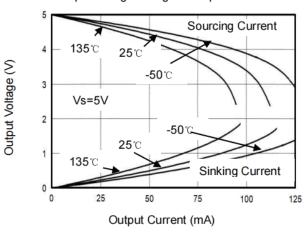


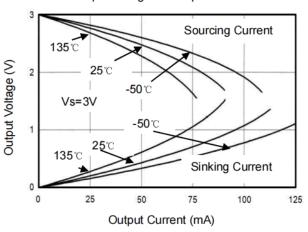




Output Voltage Swing vs. Output Current

Output Voltage vs. Output Current

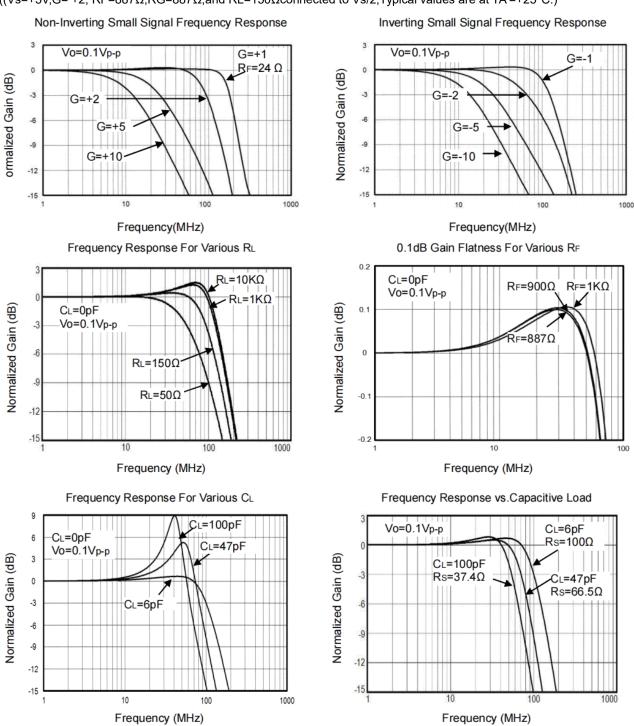






Typical Performance characteristics

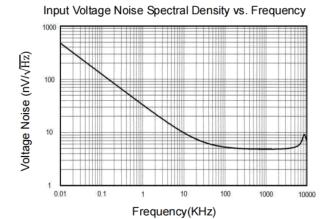
((Vs=+5V,G= +2, RF=887 Ω ,RG=887 Ω ,and RL=150 Ω connected to Vs/2,Typical values are at TA =+25°C.)

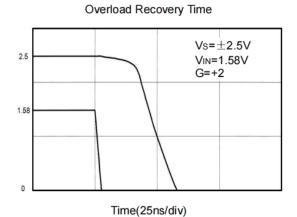


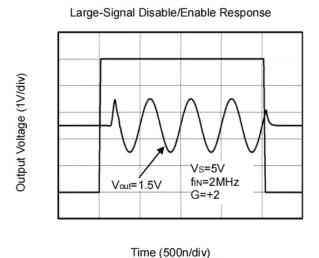


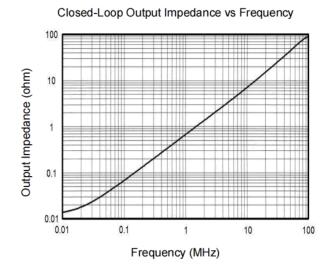
Typical Performance characteristics

 $(Vs=+5V,G=+2,RF=887\Omega,RG=887\Omega,andRL=150\Omega)$ connected to Vs/2, Typical values are at TA=+25°C.)











Application Note

Driving Capacitive Loads

HGV805X series op amps are unity-gain stable and suitable for a wide range of general-purpose applications. The small footprints of the HGV805X series packages save space on printed circuit boards and enable the design of smaller electronic products.

Power Supply Bypassing and Board Layout

HGV805X series operates from a single 2.5V to 5.5V supply or dual ± 1.25 V to ± 2.75 V supplies. For best performance, a 0.1 μ F ceramic capacitor should be placed close to the VDD pin in single supply operation. For dual supply operation, both VDD and VSS supplies should be bypassed to ground with separate 0.1 μ F ceramic capacitors.

Low Supply Current

The low supply current (typical 2.8mA per channel) of HGV805X series will help to maximize battery life. They are ideal for battery powered systems.

Operating Voltage

HGV805X series operate under wide input supply voltage (2.5V to 5.5V). In addition, all temperature specifications apply from

-40°C to +125°C. Most behavior remains unchanged throughout the full operating voltage range. These guarantees ensure operation throughout the single Li-lon battery lifetime.

Rail-to-Rail Output

Rail-to-Rail output swing provides maximum possible dynamic range at the output. This is particularly important when operating in low supply voltages. The output voltage of HGV805X series can typically swing to less than 8mV from supply rail in light resistive loads ($>1k\Omega$), and 30mV of supply rail in moderate resistive loads (150Ω).

Capacitive Load Tolerance

The HGV805X family is optimized for bandwidth and speed, not for driving capacitive loads. Output capacitance will create a pole in the amplifier's feedback path, leading to excessive peaking and potential oscillation. If dealing with load capacitance is a requirement of the application, the two strategies to consider are (1) using a small resistor in series with the amplifier's output and the load capacitance and (2) reducing the bandwidth of the amplifier's feedback loop by increasing the overall noise gain. Figure 2. shows a unity gain follower using the series resistor strategy. The resistor isolates the output from the capacitance and, more importantly, creates a zero in the feedback path that compensates for the pole created by the output capacitance.

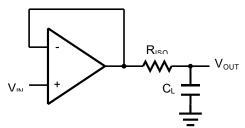


Figure 2. Indirectly Driving a Capacitive Load Using Isolation Resistor



The bigger the RISO resistor value, the more stable VOUT will be. However, if there is a resistive load RL in parallel with the capacitive load, a voltage divider (proportional to RISO/RL) is formed, this will result in a gain error.

The circuit in Figure 3 is an improvement to the one in Figure 2. RF provides the DC accuracy by feed-forward the VIN to RL. CF and RISO serve to counteract the loss of phase margin by feeding the high frequency component of the output signal back to the amplifier's inverting input, thereby preserving the phase margin in the overall feedback loop. Capacitive drive can be increased by increasing the value of CF. This in turn will slow down the pulse response.

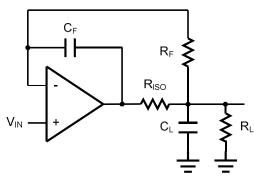


Figure 3. Indirectly Driving a Capacitive Load with DC Accuracy

Typical Application Circuits

Differential amplifier

The differential amplifier allows the subtraction of two input voltages or cancellation of a signal common the two inputs. It is useful as a computational amplifier in making a differential to single-end conversion or in rejecting a common mode signal. Figure 4. shown the differential amplifier using HGV805X.

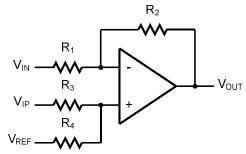


Figure 4. Differential Amplifier

$$V_{\text{OUT}} = \left(\frac{R_1 + R_2}{R_3 + R_4}\right) \frac{R_4}{R_1} V_{\text{IN}} - \frac{R_2}{R_1} V_{\text{IP}} + \left(\frac{R_1 + R_2}{R_3 + R_4}\right) \frac{R_2}{R_1} V_{\text{REF}}$$

If the resistor ratios are equal (i.e. $R_1=R_3$ and $R_2=R_4$), then

$$V_{\text{OUT}} = \frac{R_2}{R_1} (V_{\text{IP}} - V_{\text{IN}}) + V_{\text{REF}}$$



Low Pass Active Filter

The low pass active filter is shown in Figure 5. The DC gain is defined by -R2/R1. The filter has a -20dB/decade roll-off after its corner frequency $fC=1/(2\pi R3C1)$.

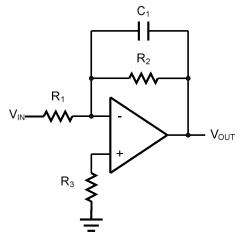


Figure 5. Low Pass Active Filter

Driving Video

The HGV805X can be used in video applications like in Figure 6.

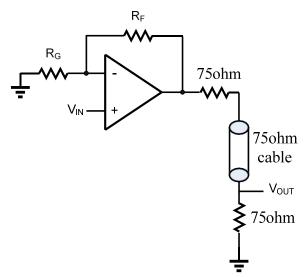
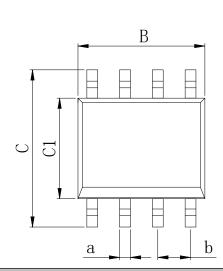
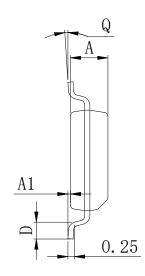


Figure 6. Typical video driving



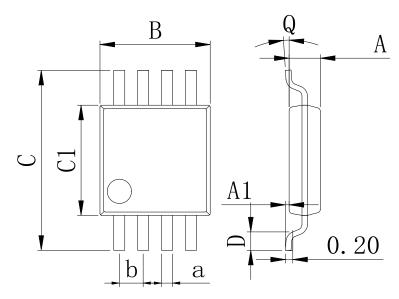
SOP8





Dimensions In Millimeters(SOP8)											
Symbol:	Α	A1	В	С	C1	D	Q	а	b		
Min:	1.35	0.05	4.90	5.80	3.80	0.40	0°	0.35	1.27 BSC		
Max:	1.55	0.20	5.10	6.20	4.00	0.80	8°	0.45	1.27 BSC		

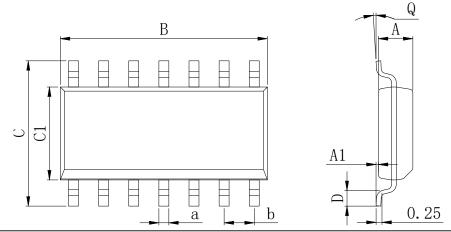
MSOP8



Dimensions In Millimeters(MSOP8)											
Symbol:	Α	A1	В	С	C1	D	Q	а	b		
Min:	0.80	0.05	2.90	4.75	2.90	0.35	0°	0.25	0.65 BSC		
Max:	0.90	0.20	3.10	5.05	3.10	0.75	8°	0.35	0.00 650		

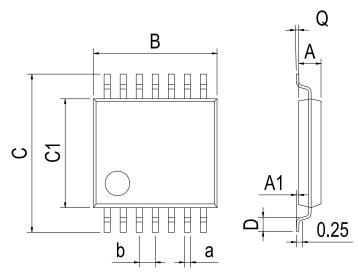


SOP14



Dimensions In Millimeters(SOP14)											
Symbol:	Α	A1	В	С	C1	D	Q	а	b		
Min:	1.35	0.05	8.55	5.80	3.80	0.40	0°	0.35	1.27 BSC		
Max:	1.55	0.20	8.75	6.20	4.00	0.80	8°	0.45	1.27 650		

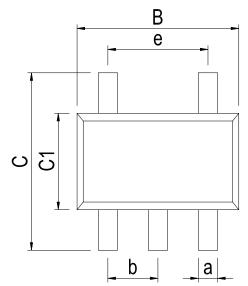
TSSOP14

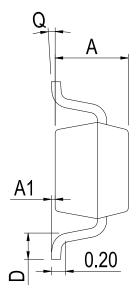


Dimensions In Millimeters(TSSOP14)											
Symbol:	А	A1	В	С	C1	D	Q	а	b		
Min:	0.85	0.05	4.90	6.20	4.30	0.40	0°	0.20	0.65 BSC		
Max:	0.95	0.20	5.10	6.60	4.50	0.80	8°	0.25	0.00 BSC		



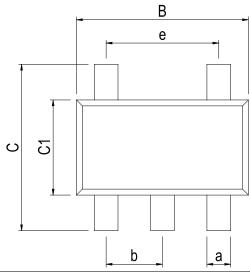
SOT23-5

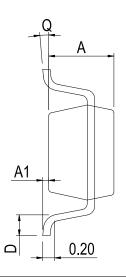




Dimensions In Millimeters(SOT23-5)												
Symbol:	А	A1	В	С	C1	D	Q	а	b	е		
Min:	1.05	0.00	2.82	2.65	1.50	0.30	0°	0.30	0.05 BSC	1 00 BSC		
Max:	1.15	0.15	3.02	2.95	1.70	0.60	8°	0.40	0.95 BSC	1.90 BSC		

SC70-5

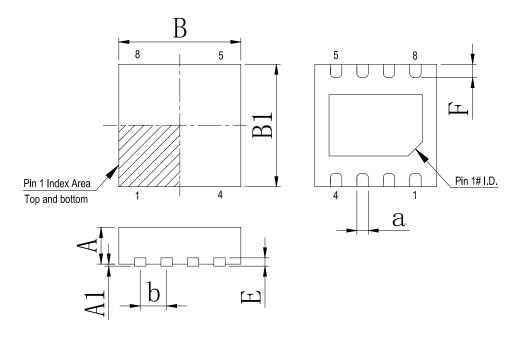




Dimensions In Millimeters(SC70-5)												
Symbol:	Α	A1	В	С	C1	D	Q	а	b	е		
Min:	0.90	0.00	2.00	2.15	1.15	0.26	0°	0.30	0.65 BSC	1.30 BSC		
Max:	1.00	0.15	2.20	2.45	1.35	0.46	8°	0.40	0.00 000	1.30 030		



DFN-8 4*4



Dimensions In Millimeters(DFN-8L 4*4)											
Symbol:	Α	A1	В	D	Е	F	а	а			
Min:	3.9	3.9	0.80	0.0	0.23	0.30	0.20	0.80TYP			
Max:	4.1	4.1	1.0	0.05	0.30	0.50	0.34	0.60119			

HGV8051/8052/8054

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