

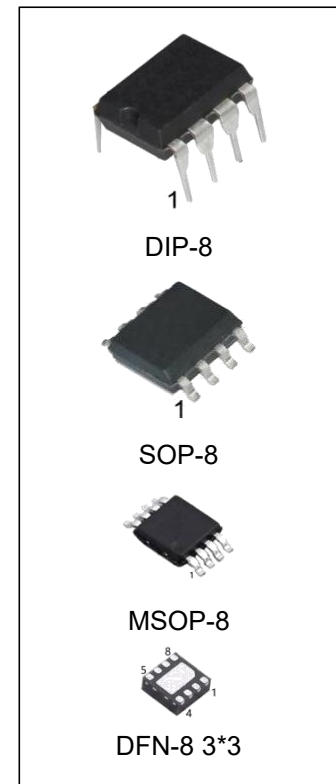
DUAL OPERATIONAL AMPLIFIER

DESCRIPTION

JRC4580 is the dual operational amplifier, specially designed for improving the tone control, which is most suitable for the audio application. Featuring noiseless, higher gain bandwidth, high output current and low distortion ratio, and it is most suitable not only for acoustic electronic part of audio pre-amp and active filter, but also for the industrial measurement tools. It is also suitable for the head phone amp at higher output current. And further more, it can be applied for the handy type set operational amplifier of general purpose in application of low voltage single supply type which is properly biased of the input low voltage source.

FEATURE

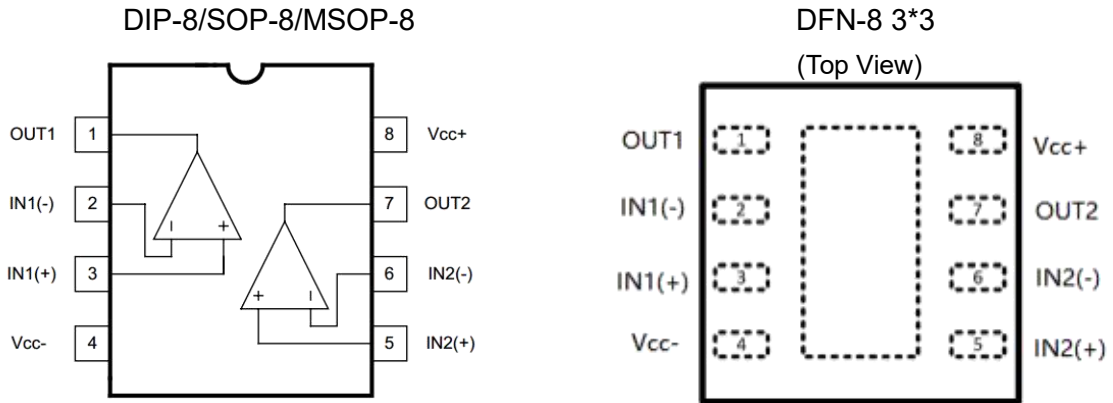
- Operating Voltage: $\pm 2V \sim \pm 16V$.
- Low Input Noise Voltage: $0.8\mu V_{rms}$ Typ.
- Wide Gain Bandwidth Product : 15mhz Typ.
- Low Distortion : 0.0005% Typ.
- Slew Rate: $5V/\mu A$ Typ.
- Package Outline DIP-8、SOP-8、DFN-8 and MSOP-8 .
- Bipolar Technology.



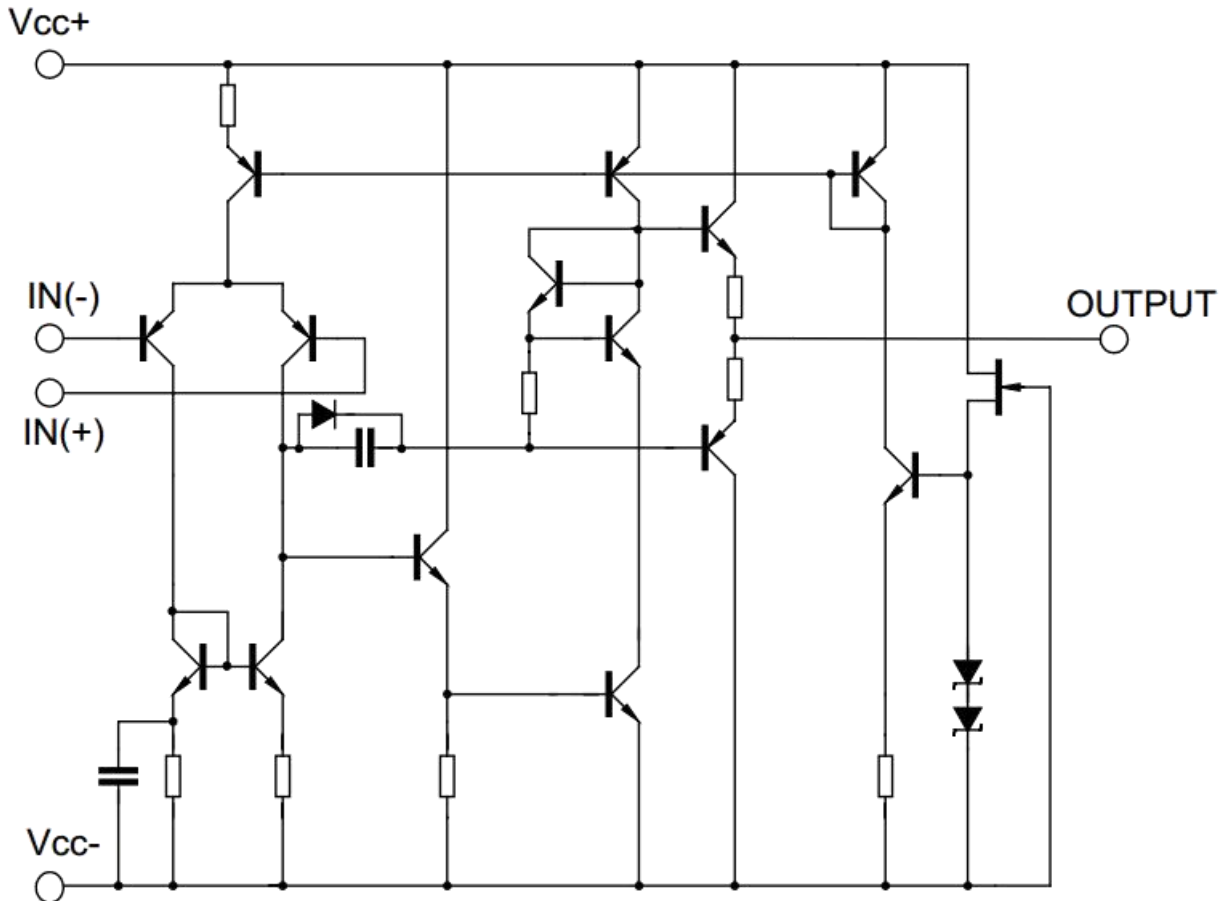
Ordering Information

DEVICE	Package Type	MARKING	Packing	Packing Qty
JRC4580N	DIP-8	4580	TUBE	2000pcs/box
JRC4580M/TR	SOP-8	4580	REEL	2500pcs/reel
JRC4580MM/TR	MSOP-8	4580	REEL	3000pcs/reel
JRC4580DQ/TR	DFN-8 3*3	4580	REEL	5000pcs/reel

PIN CONFIGURATION



EQUIVALENT CIRCUIT



ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Characteristic		Symbol	Value	Unit
Supply Voltage		V+/V-	±16	V
Input Voltage		V _{IC}	±15	V
Differential Input Voltage		V _{ID}	±30	V
Output Current		I _o	±50	mA
Power Dissipation	DIP-8	P _D	800	mW
	SOP-8		300	
	MSOP-8		250	
Operating Temperature Range		T _{OPR}	-40~85	°C
Storage Temperature Range		T _{stg}	-40~125	°C
Lead Temperature (Soldering, 10 seconds)		T _L	245	°C

Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not ensured.

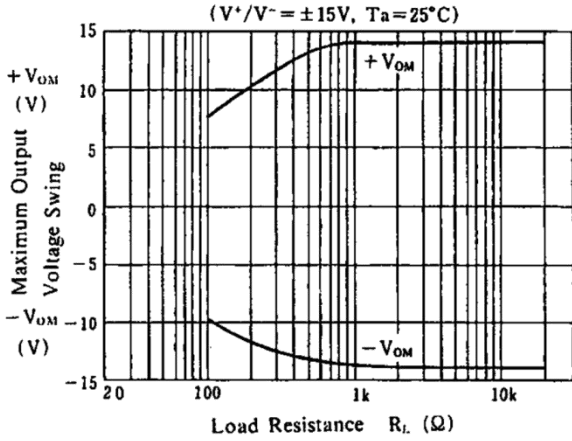
ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: Ta= 25°C, V+/V- =±15V)

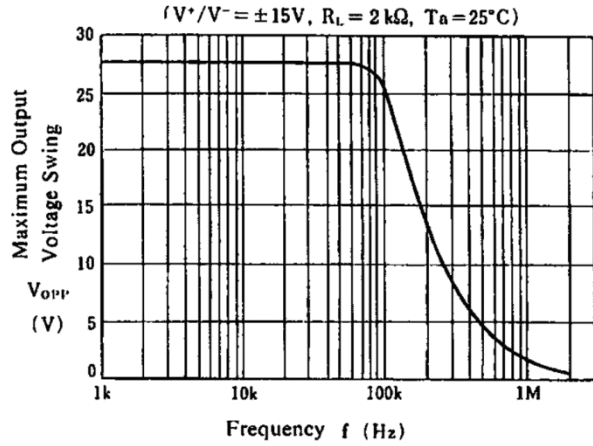
Parameter	Symbol	Test condition	Min	Typ	Max	Unit
Input Offset Voltage	V _{IO}	R _s ≤ 10 kΩ		0.5	3	mV
Input Offset Current	I _{IO}			5	200	nA
Input Bias Current	I _B			100	500	nA
Large Signal Voltage Gain	A _v	R _L ≥ 2kΩ, V _o = ±10V	90	110		dB
Output Voltage Swing	V _{OM}	R _L ≥ 2kΩ	±12	±13.5		V
Input Common Mode Voltage Range	V _{ICM}		±12	±13.5		V
Common Mode Rejection Ratio	CMR	R _s ≤ 10 kΩ	80	110		dB
Supply Voltage Rejection Ratio	SVR	R _s ≤ 10 kΩ	80	110		dB
Operating Current	I _{CC}			6	9	mA
Slew Rate	SR	R _L ≥ 2kΩ		5		V/μs
Gain Bandwidth Product	GB	f=10kHz		15		MHz
Total Harmonic Distortion	THD	A _v =20dB, V _o = 5 V, f=1kHz, R _L =2kΩ		0.0005		%
Input Noise Voltage	V _{NI}	RIAA R _s =2.2kΩ, 30kHz LPF		0.8		μVrms

CHARACTERISTICS CURVES

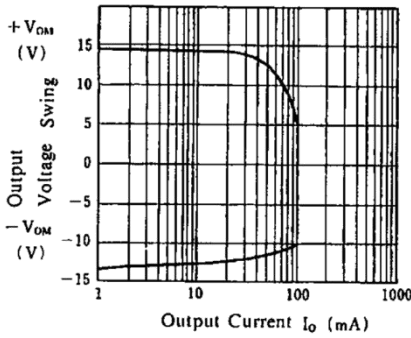
Maximum Output Voltage Swing vs. Load Resistance



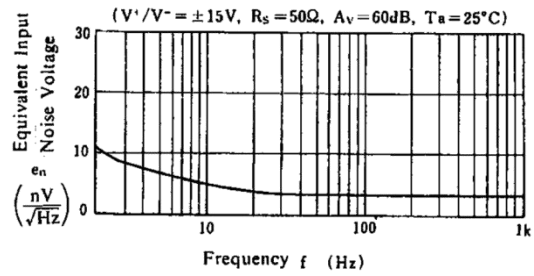
Maximum Output Voltage Swing vs. Frequency



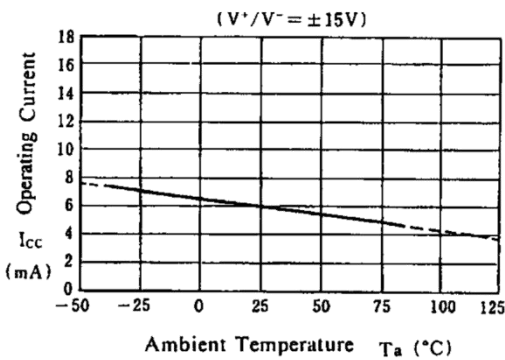
Output Voltage Swing vs. Output Current



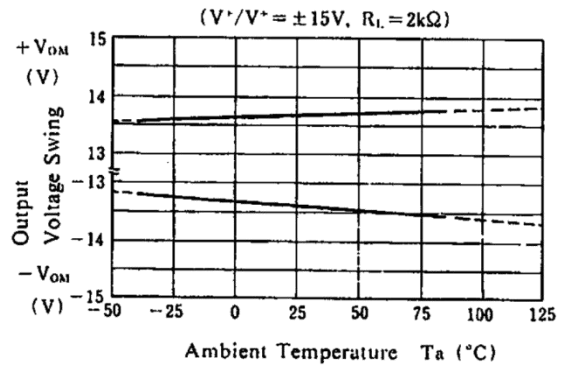
Equivalent Input Noise Voltage vs. Frequency



Operating Current vs. Temperature

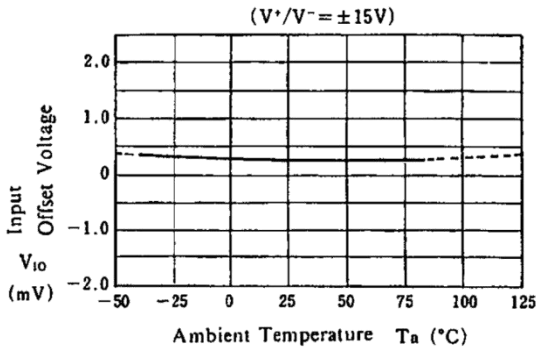


Output Voltage Swing vs. Temperature

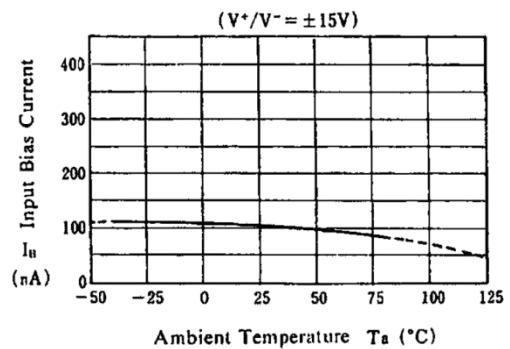


TYPICAL CHARACTERISTICS

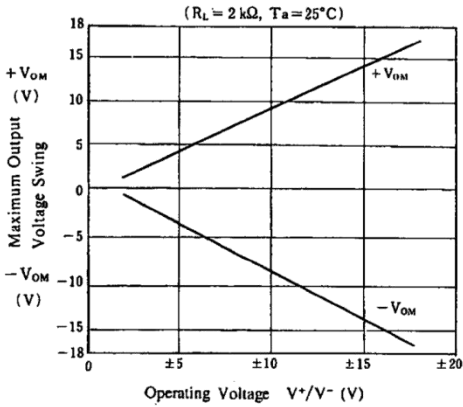
Input offset Voltage vs. Temperature



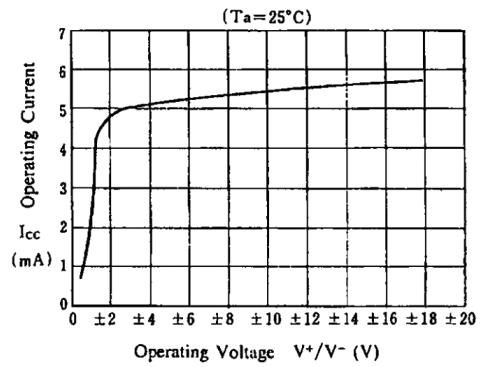
Input bias current vs. Temperature



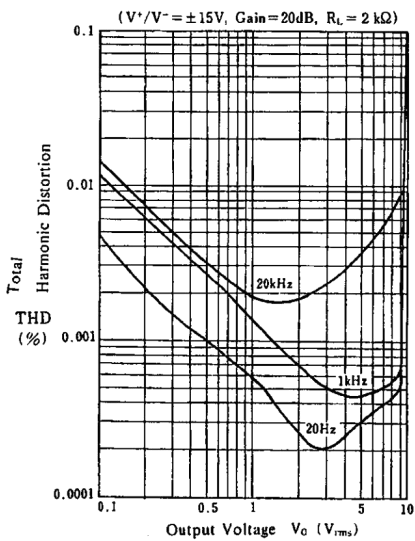
Maximum Output Voltage Swing vs. Operating Voltage



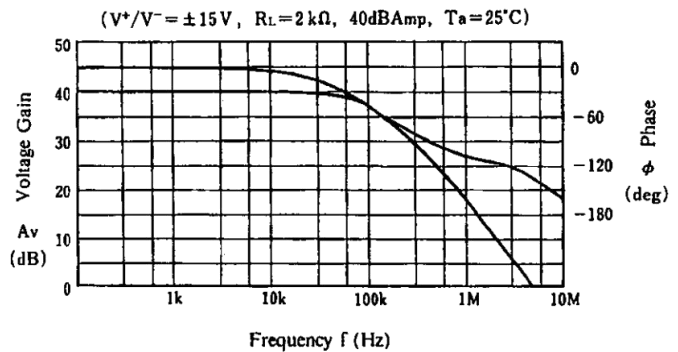
Operating Current vs. Operating Voltage



Total Harmonic Distortion vs. Output Voltage

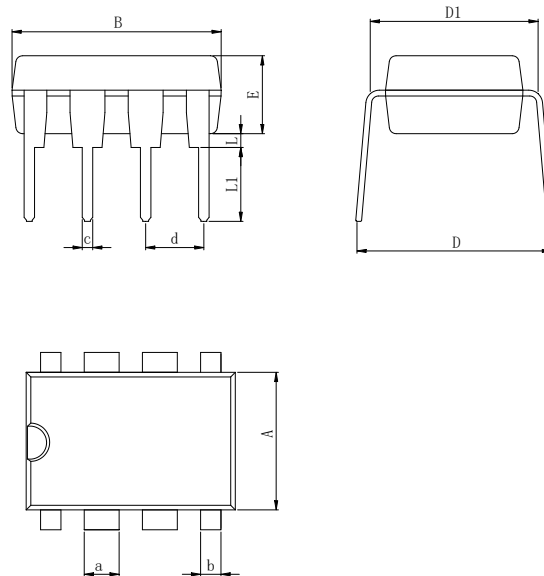


Voltage Gain Phase vs. Frequency



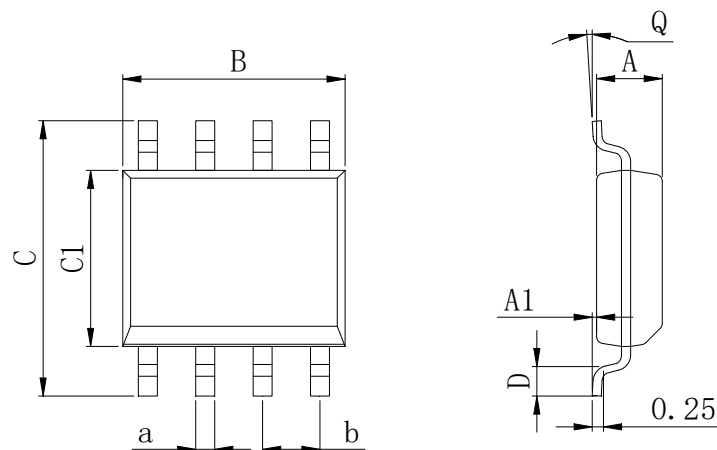
PHYSICAL DIMENSIONS

DIP-8



Dimensions In Millimeters(DIP-8)											
Symbol:	A	B	D	D1	E	L	L1	a	b	c	d
Min:	6.10	9.00	8.10	7.42	3.10	0.50	3.00	1.50	0.85	0.40	2.54 BSC
Max:	6.68	9.50	10.9	7.82	3.55	0.70	3.60	1.55	0.90	0.50	

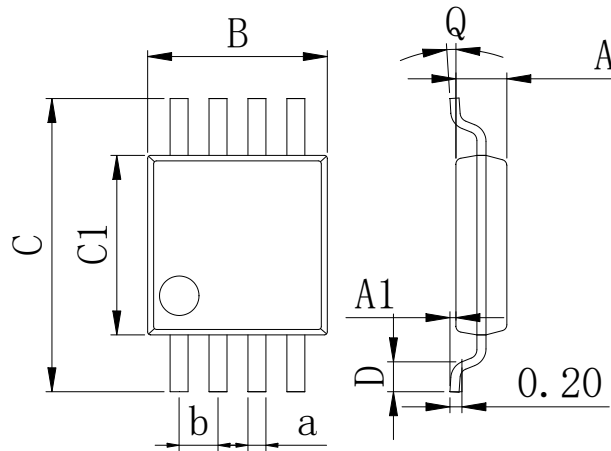
SOP-8 (150mil)



Dimensions In Millimeters(SOP-8)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	1.35	0.05	4.90	5.80	3.80	0.40	0°	0.35	1.27 BSC
Max:	1.55	0.20	5.10	6.20	4.00	0.80	8°	0.45	

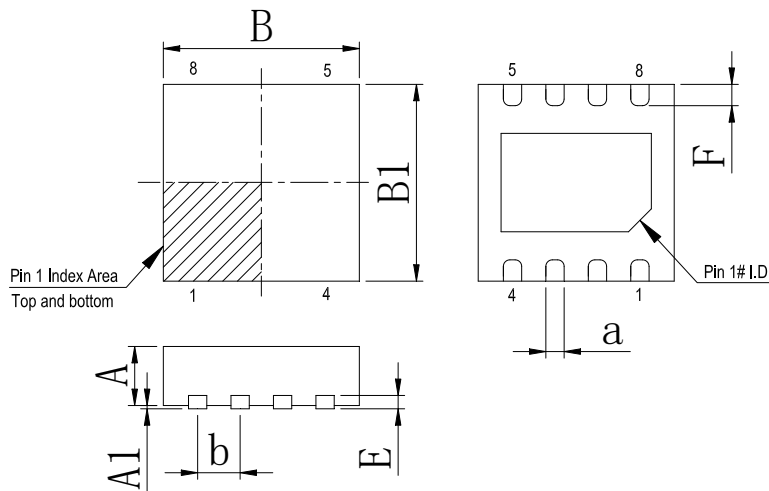
PHYSICAL DIMENSIONS

MSOP-8



Dimensions In Millimeters(MSOP-8)									
Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	0.80	0.05	2.90	4.75	2.90	0.35	0°	0.25	0.65 BSC
Max:	0.90	0.20	3.10	5.05	3.10	0.75	8°	0.35	

DFN-8 3*3



Dimensions In Millimeters(DFN-8 3*3)								
Symbol:	A	A1	B	B1	E	F	a	b
Min:	0.85	0.00	2.90	2.90	0.20	0.30	0.20	0.65 BSC
Max:	0.95	0.05	3.10	3.10	0.25	0.50	0.34	

REVISION HISTORY

DATE	REVISION	PAGE
2014-6-23	New	1-9
2024-8-20	Document reformatting	1-9

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