

FEATURES

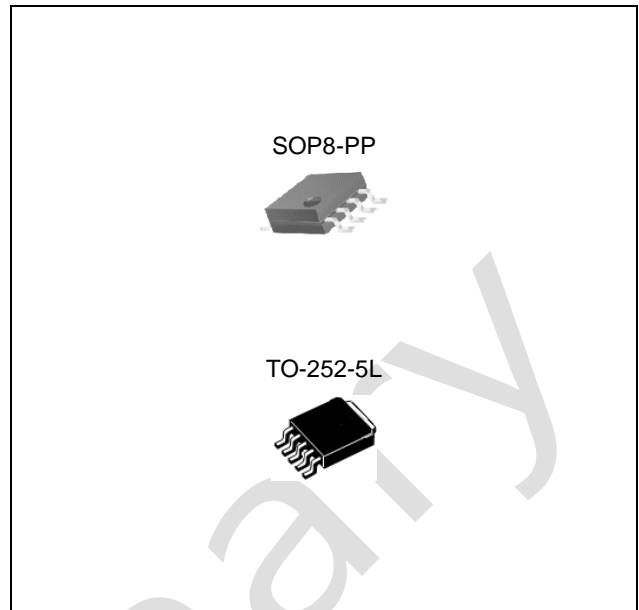
- Ultra Low Dropout Voltage
- Compatible with low ESR MLCC as Input / Output Capacitor
- Good Line and Load Regulation
- Guaranteed Output Current of 3A
- Available in SOP8-PP, TO-252-5L Packages
- Fixed Output: 1.0V, 1.2V, 1.5V, 1.8V, 2.5V, and 3.3V
- VOUT Power OK Signal
- Programmable Soft-Start Function
- Output Auto Discharge Function
- Over-Temperature/Over-Current Protection

APPLICATION

- LCD TVs and SETTOP Boxes
- Battery Powered Equipment
- Motherboards and Graphic Cards
- Microprocessor Power Supplies
- Peripheral Cards
- High Efficiency Linear Regulators
- Battery Chargers

DESCRIPTION

The TJ4330 series of high performance ultra-low dropout linear regulators operates from 2.5V to 5.5V input supply and provides ultra-low dropout voltage, high output current with low ground current. Wide range of preset output voltage options are available. These ultra-low dropout linear regulators respond fast to step changes in load which makes them suitable for low voltage micro-processor applications. The TJ4330 is developed on a CMOS process technology which allows low quiescent current operation independent of output load current. This CMOS process also allows the TJ4330 to operate under extremely low dropout conditions.



ORDERING INFORMATION

Device	Package
TJ4330GDP-ADJ	SOP8-PP
TJ4330GDP-X.X	
TJ4330GRS-ADJ	TO-252-5L

X.X = Output Voltage = 1.0, 1.2, 1.5, 1.8, 2.5, and 3.3

ABSOLUTE MAXIMUM RATINGS (Note 1)

CHARACTERISTIC	SYMBOL	MIN.	MAX.	UNIT
Input Supply Voltage (Survival)	V_{IN}	-	6.5	V
Maximum Output Current	I_{MAX}	-	3	A
Lead Temperature (Soldering, 5 sec)	T_{SOL}		260	°C
Storage Temperature Range	T_{STG}	-65	150	°C
Operating Junction Temperature Range	T_{JOPR}	-40	125	°C
Package Thermal Resistance *	$\theta_{JA-SOP8-PP}$		68	°C/W

* Calculated from package in still air, mounted to 2.6mm X 3.5mm(minimum foot print) 2 layer PCB without thermal vias per JESD51 standards.

Please contact us for more information about this product.

单击下面可查看定价，库存，交付和生命周期等信息

[>>HTC\(泰进\)](#)