

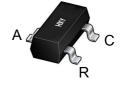
#### **DEVICE DESCRIPSION**

The TL432 is a three-terminal adjustable shunt regulator highly accurate 1.25V bandgap reference with a 0.5% tolerance.

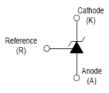
The device offers thermal stability, wide operating current (50mA) and an extended temperature range of 0 to  $105^{\circ}$ C for operation in power supply applications.

The TL432 offers a wide perating voltage range of up to 18V and is an excellent choice for voltage reference requirements in an isolated feedback circuit for 3.0V to 3.3V switching mode power supplies.

The tight tolerance quarantees a lower design cost for the power supply manufacturer by virtually eliminating the need for an extra power supply manufacturing process of the power supply.



**SOT-23** 



### **Equivalent Circuit**

#### **FEATURES**

Wide Programmable Prise Output Voltage from 1.25V to 18V.

Low Dynamic Output Resistance:0.05Ω Typical.

High Sink Current Capacity from 55uA-100mA.

Low Equivalent Full-Range Temperature Coefficient : 20PPM/℃ Typical.

Wide Operating Range of -40 to 125℃.

#### **APPLICATION**

Shunt Regulator High-Current Shunt Regulator Precision Current Limiter

#### **Package Marking and Ordering Information**

Product ID	Pack	Marking	Qty(PCS)
TL432	SOT-23	432	3000

### **Absolute Maximum Ratings(Ta=25℃)**

Symbol	Parameter	Value	Unit
$V_{KA}$	Cathode Voltage	18	V
I <sub>KA</sub>	Cathode Current Range (Continuous)	100	mA
I <sub>ref</sub>	Reference Input Current Range	6	μA
P <sub>D</sub>	Power Dissipation	350	mW
R <sub>OJA</sub>	Thermal Resistance From Junction To Ambient	357	°C/W
T <sub>J</sub> ,T <sub>stg</sub>	Operation Junction And Storage Temperature Range	-40~+125	$^{\circ}\!\mathbb{C}$



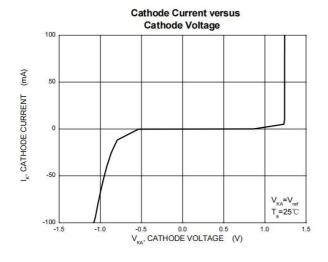
### **Electrical Characteristics (Ta=25℃ unless otherwise specified)**

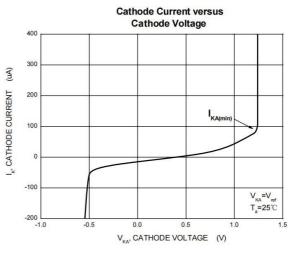
Symbol	Parameter	Test conditions	Min	Тур	Max	Unit
V <sub>ref</sub>	Reference input voltage	V <sub>KA</sub> =V <sub>REF</sub> , I <sub>KA</sub> =10mA	1.225		1.275	V
$\triangle V_{\text{ref}}/\triangle T$	Deviation of reference input voltage over temperature (note)	V <sub>KA</sub> =V <sub>REF</sub> , I <sub>KA</sub> =10mA, T <sub>MIN</sub> ≤Ta≤T <sub>MAX</sub>		4.5	16	mV
$\triangle V_{ref} / \triangle V_{KA}$	Ratio of change in reference input voltage to the change in cathode voltage	$I_{KA}$ =10mA, $\triangle$ V $_{KA}$ =1.25V $\sim$ 18V			2.4	mV/V
I <sub>ref</sub>	Reference input current	I <sub>KA</sub> =10mA, R1=10KΩ, R2=∞			0.5	μA
$\triangle I_{ref} / \triangle T$	Deviation of reference input current over full temperature range	$I_{KA}$ =10mA, R1=10KΩ, R2=∞ $T_A$ =0 to 70°C			0.6	μA
I <sub>KA(min)</sub>	Minimum cathode current for regulation	V <sub>KA</sub> =V <sub>REF</sub>			0.1	mA
I <sub>KA(OFF)</sub>	Off-state cathode current	V <sub>KA</sub> =36V, V <sub>REF</sub> =0			0.5	μA
Z <sub>KA</sub>	Dynamic impedance	V <sub>KA</sub> =V <sub>REF,</sub> I <sub>KA</sub> =1∼100mA, f≤1.0KHz	·	·	0.5	Ω

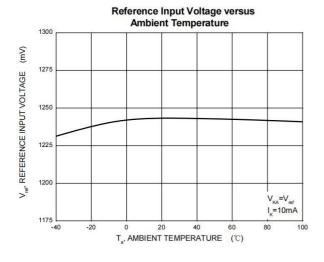
### **CLASSIFICATION cZV**<sub>ref</sub>

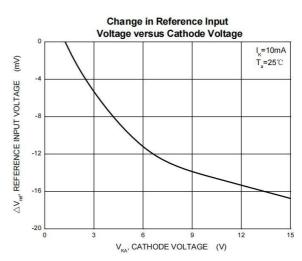
Rank	··· 0.5%	1%
Range	2.487-2.513	2.475-2.525

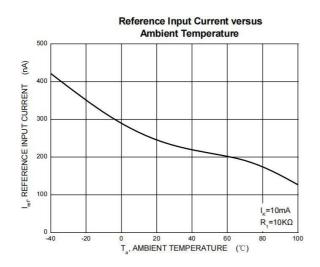
### **Typical Characteristics**

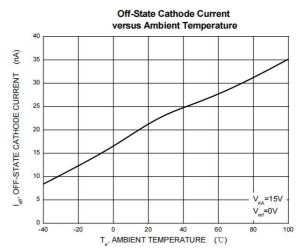




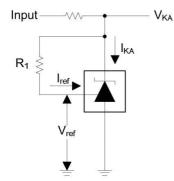




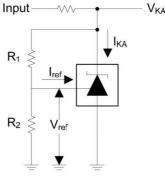




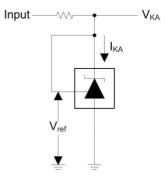
### **Test Circuit**



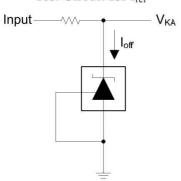
Test Circuit for I<sub>ref</sub>



Test Circuit for  $V_{KA}=V_{ref}(1+R1/R2)+R1*I_{ref}$ 



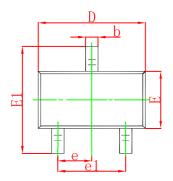
Test Circuit for V<sub>KA</sub>=V<sub>ref</sub>

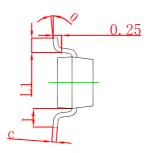


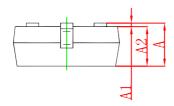
Test Circuit for Ioff



### **SOT-23 Package Outline Dimensions**

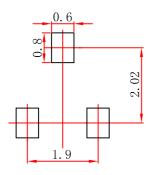






Cumbal	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

## **SOT-23 Suggested Pad Layout**



#### Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance:± 0.05mm.
  3.The pad layout is for reference purposes only.

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