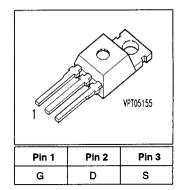


#### SIPMOS ® Power Transistor

- N channel
- Enhancement mode
- Avalanche-rated



Туре	v <sub>DS</sub>	Ь	R <sub>DS(on)</sub>	S(on) Package Ord	
BUZ 73 A	200 V	5.5 A	0.6 Ω	TO-220 AB	C67078-S1317-A3

Maximum	Ratings

Parameter	Symbol	Values	Unit
Continuous drain current	I <sub>D</sub>		Α
T <sub>C</sub> = 37 °C		5.5	
Pulsed drain current	/ <sub>Dpuls</sub>		
T <sub>C</sub> = 25 °C		22	
Avalanche current, limited by $T_{jmax}$	/ <sub>AR</sub>	7	
Avalanche energy,periodic limited by T <sub>jmax</sub>	E <sub>AR</sub>	6.5	mJ
Avalanche energy, single pulse	E <sub>AS</sub>		
$I_{\rm D}$ = 7 A, $V_{\rm DD}$ = 50 V, $R_{\rm GS}$ = 25 $\Omega$			
$L = 3.67 \text{ mH}, T_j = 25 ^{\circ}\text{C}$		120	
Gate source voltage	V <sub>GS</sub>	± 20	V
Power dissipation	P <sub>tot</sub>		W
T <sub>C</sub> = 25 °C		40	
Operating temperature		-55 <b>+</b> 150	.c
Storage temperature	$T_{ m stg}$	-55 + 150	
Thermal resistance, chip case	R <sub>thJC</sub>	≤ 3.1	k/w
Thermal resistance, chip to ambient	R <sub>thJA</sub>	75	
DIN humidity category, DIN 40 040		E	
IEC climatic category, DIN IEC 68-1		55 / 150 / 56	

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# **Electrical Characteristics,** at $T_{\rm j}$ = 25°C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Static Characteristics					
Drain- source breakdown voltage $V_{(BF)}$				-	V
$V_{GS} = 0 \text{ V}, I_D = 0.25 \text{ mA}, T_j = 25 \text{ °C}$	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	200	-	-	
Gate threshold voltage	V <sub>GS(th)</sub>				
$V_{\rm GS} = V_{\rm DS}$ , $I_{\rm D} = 1$ mA		2.1	3	4	
Zero gate voltage drain current	l <sub>DSS</sub>				μА
$V_{\rm DS}$ = 200 V, $V_{\rm GS}$ = 0 V, $T_{\rm j}$ = 25 °C		-	0.1	1	
$V_{\rm DS}$ = 200 V, $V_{\rm GS}$ = 0 V, $T_{\rm j}$ = 125 °C		-	10	100	
Gate-source leakage current	l <sub>GSS</sub>				nA
$V_{GS} = 20 \text{ V}, \ V_{DS} = 0 \text{ V}$		-	10	100	
Drain-Source on-resistance	R <sub>DS(on)</sub>				Ω
$V_{\rm GS} = 10  \rm V,  I_{\rm D} = 4.5  \rm A$		-	0.5	0.6	



Data Book

# **Electrical Characteristics**, at $T_j = 25$ °C, unless otherwise specified

Parameter	Symbol	Values		Unit	
		min.	typ.	max.	
Dynamic Characteristics					
Transconductance	9 <sub>fs</sub>				s
$V_{DS} \ge 2 \cdot I_D \cdot R_{DS(on)max}$ , $I_D = 4.5 \text{ A}$		3	4.2	_  -	
Input capacitance	Ciss				pF
$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$			400	530	_
Output capacitance	Coss				Ì
$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		-	85	130	_
Reverse transfer capacitance	C <sub>rss</sub>				
$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		-	45	70	
Turn-on delay time	t <sub>d(on)</sub>				ns
$V_{\rm DD}$ = 30 V, $V_{\rm GS}$ = 10 V, $I_{\rm D}$ = 3 A	1			Ì	
$R_{\rm GS}$ = 50 $\Omega$		-	10	15	
Rise time	t <sub>r</sub>				
$V_{\rm DD}$ = 30 V, $V_{\rm GS}$ = 10 V, $I_{\rm D}$ = 3 A					
$R_{\rm GS}$ = 50 $\Omega$		-	40	60	
Turn-off delay time	t <sub>d(off)</sub>				
$V_{\rm DD}$ = 30 V, $V_{\rm GS}$ = 10 V, $I_{\rm D}$ = 3 A					
$R_{\rm GS}$ = 50 $\Omega$		-	55	75	_
Fall time	t <sub>4</sub>	-			
$V_{\rm DD}$ = 30 V, $V_{\rm GS}$ = 10 V, $I_{\rm D}$ = 3 A					
$R_{\rm GS}$ = 50 $\Omega$		<u> -</u>	30	40	



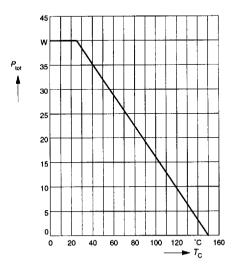
# **Electrical Characteristics**, at $T_{\rm j}$ = 25°C, unless otherwise specified

Parameter	Symbol	Values			Unit
A contract to the contract to		min.	typ.	max.	
Reverse Diode					
Inverse diode continuous forward current	Is				Α
T <sub>C</sub> = 25 °C		-	-	5.5	
Inverse diode direct current,pulsed	/ <sub>SM</sub>				
T <sub>C</sub> = 25 °C		-	-	22	
Inverse diode forward voltage	V <sub>SD</sub>				V
$V_{\rm GS} = 0 \text{ V}, I_{\rm F} = 14 \text{ A}$		-	1.3	1.7	
Reverse recovery time	t <sub>rr</sub>				ns
$V_{\rm R}$ = 100 V, $I_{\rm F} = I_{\rm S}$ , $dI_{\rm F}/dt$ = 100 A/ $\mu$ s		-	200	-	
Reverse recovery charge	Q <sub>rr</sub>				μC
$V_{R} = 100 \text{ V}, I_{F} = I_{S}, dI_{F}/dt = 100 \text{ A/µs}$		-	0.6	-	



#### Power dissipation

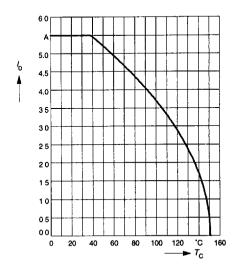
$$P_{\text{tot}} = f(T_{\text{C}})$$



#### Drain current

$$I_{\mathsf{D}} = f(T_{\mathsf{C}})$$

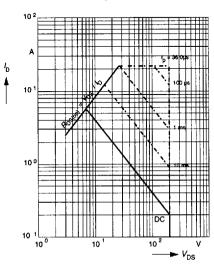
parameter: V<sub>GS</sub> ≥ 10 V



#### Safe operating area

$$I_{\rm D} = f(V_{\rm DS})$$

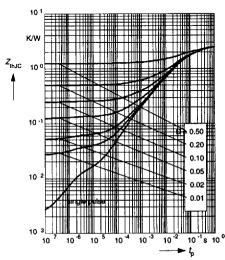
parameter: D = 0.01,  $T_{\rm C} = 25^{\circ}{\rm C}$ 



#### Transient thermal impedance

 $Z_{\text{th JC}} = f(t_{\text{p}})$ 

parameter:  $D = t_n / T$ 



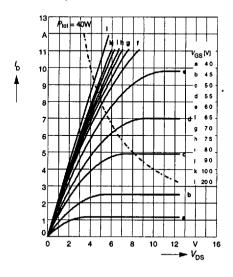
**■** 8532PO2 0733335 037 **■** 



#### Typ. output characteristics

$$I_{\mathsf{D}} = f(V_{\mathsf{DS}})$$

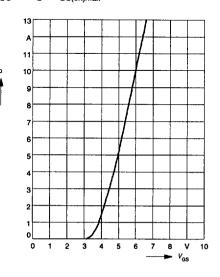
parameter: t<sub>p</sub> = 80 µs



## Typ. transfer characteristics $I_{\rm D} = f(V_{\rm GS})$

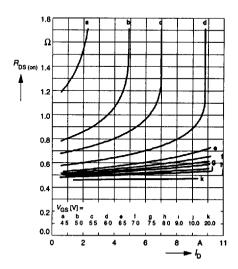
parameter: t<sub>p</sub> = 80 µs

V<sub>DS</sub>≥2 x I<sub>D</sub> x R<sub>DS(on)max</sub>



#### Typ. drain-source on-resistance

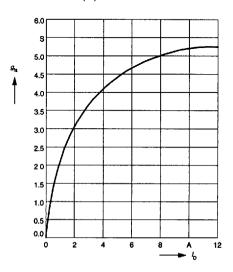
 $R_{DS \text{ (on)}} = f(I_D)$ parameter:  $V_{GS}$ 



## Typ. forward transconductance $g_{ts} = f(I_D)$

parameter:  $t_p = 80 \mu s$ ,

V<sub>DS</sub>≥2 x I<sub>D</sub> x R<sub>DS(on)max</sub>

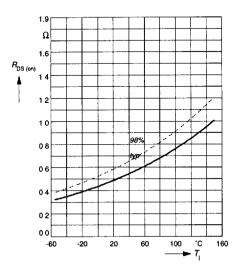




#### Drain-source on-resistance

 $R_{DS (on)} = f(T_j)$ 

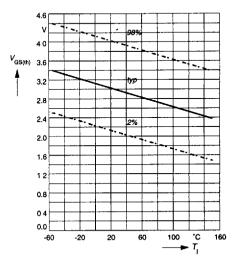
parameter:  $I_D = 4.5 \text{ A}, V_{GS} = 10 \text{ V}$ 



#### Gate threshold voltage

 $V_{GS(th)} = f(T_i)$ 

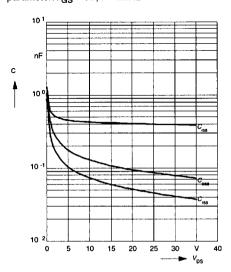
parameter:  $V_{GS} = V_{DS}$ ,  $I_D = 1$  mA



#### Typ. capacitances

 $C = f(V_{DS})$ 

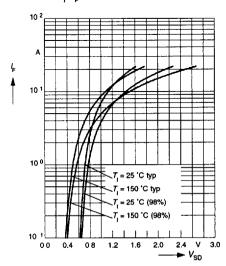
parameter:  $V_{GS} = 0V$ , f = 1MHz



#### Forward characteristics of reverse diode

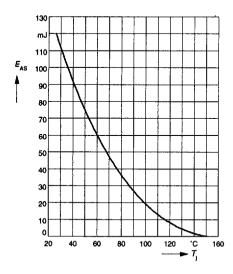
 $I_{\mathsf{F}} = f(V_{\mathsf{SD}})$ 

parameter:  $T_i$ ,  $t_D = 80 \mu s$ 



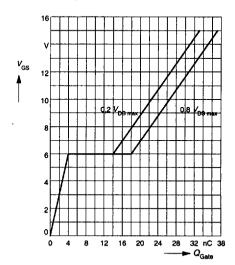


## Avalanche energy $E_{AS} = f(T_j)$ parameter: $I_D = 7$ A, $V_{DD} = 50$ V $R_{GS} = 25 \Omega$ , L = 3.67 mH



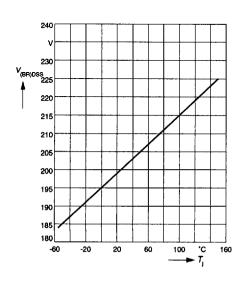
## Typ. gate charge

$$V_{GS} = f(Q_{Gate})$$
  
parameter:  $I_{D puls} = 14 A$ 



### Drain-source breakdown voltage

 $V_{(BR)DSS} = f(T_i)$ 





#### Gehäusemaßbilder

#### **Package Outlines**

(Maße in mm, wenn nicht anders angegeben)

(Dimensions in mm, unless otherwise specified)

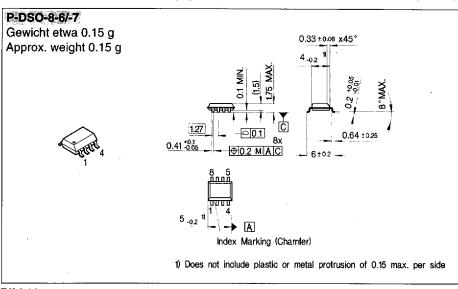
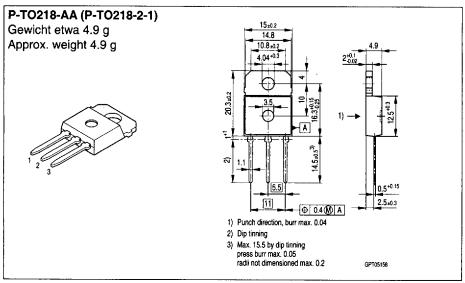


Bild 16

Figure 16

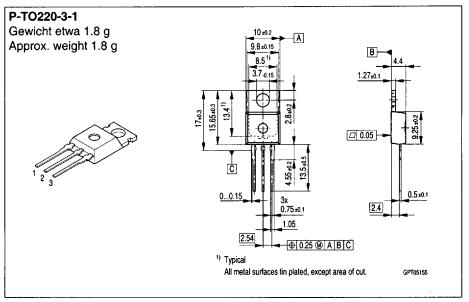


**Bild 17** 

Data Book

Figure 17





**Bild 18** 

Figure 18

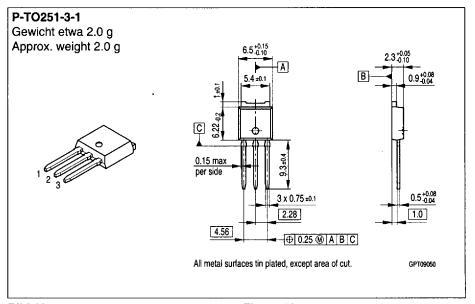


Bild 19

Data Book

Figure 19



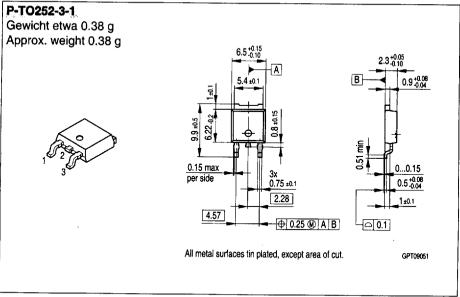


Bild 20

Figure 20

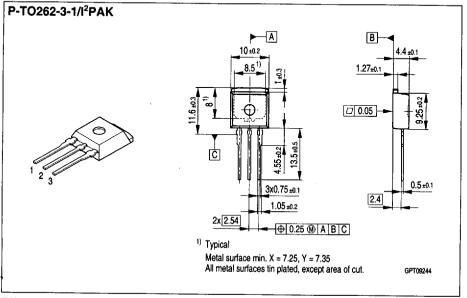


Bild 21

Figure 21



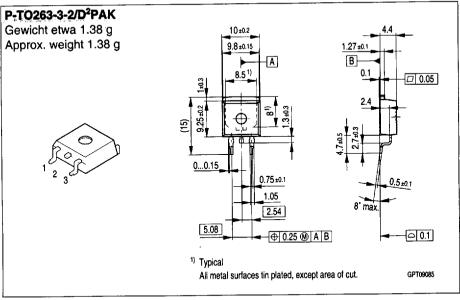


Bild 22

Figure 22

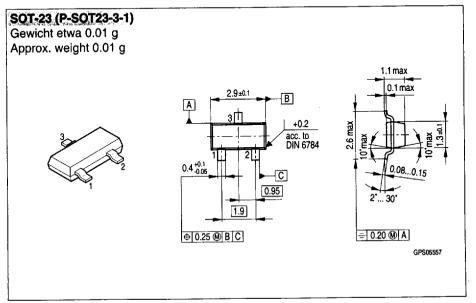


Bild 23

Figure 23



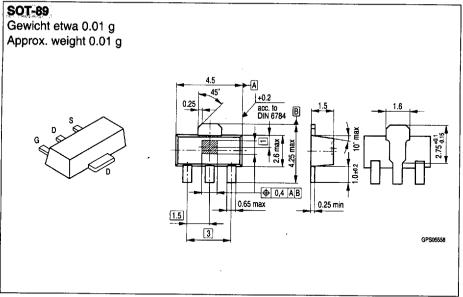


Bild 24

Figure 24

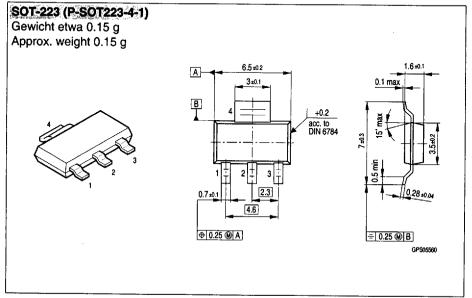


Bild 25

Figure 25



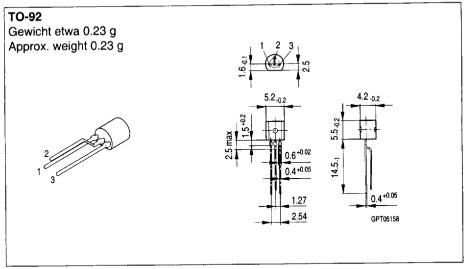


Bild 26

Figure 26

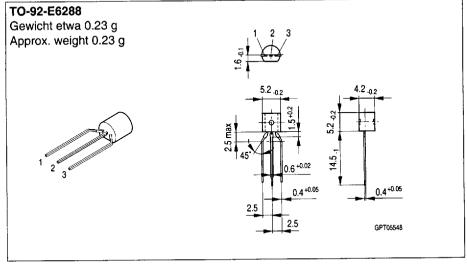


Bild 27

Figure 27

#### Sorts of Packing

Package outlines for tubes, trays etc. are contained in our Data Book "Package Information".

SMD = Surface Mounted Device

# 单击下面可查看定价,库存,交付和生命周期等信息

# >>Infineon(英飞凌)